

International
IR Rectifier

**RADIATION HARDENED
POWER MOSFET
SURFACE MOUNT (SMD-1)**

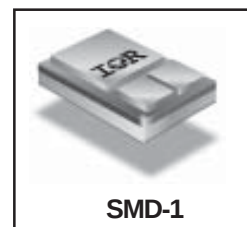
PD - 91300D

**IRHN9250
JANSR2N7423U
200V, P-CHANNEL**

REF: MIL-PRF-19500/662

RAD Hard™ HEXFET® TECHNOLOGY**Product Summary**

Part Number	Radiation Level	RDS(on)	ID	QPL Part Number
IRHN9250	100K Rads (Si)	0.315Ω	-14A	JANSR2N7423U
IRHN93250	300K Rads (Si)	0.315Ω	-14A	JANSF2N7423U

**SMD-1**

International Rectifier's RADHard HEXFET™ technology provides high performance power MOSFETs for space applications. This technology has over a decade of proven performance and reliability in satellite applications. These devices have been characterized for both Total Dose and Single Event Effects (SEE). The combination of low Rds(on) and low gate charge reduces the power losses in switching applications such as DC to DC converters and motor control. These devices retain all of the well established advantages of MOSFETs such as voltage control, fast switching, ease of paralleling and temperature stability of electrical parameters.

Features:

- Single Event Effect (SEE) Hardened
- Low RDS(on)
- Low Total Gate Charge
- Proton Tolerant
- Simple Drive Requirements
- Ease of Paralleling
- Hermetically Sealed
- Surface Mount
- Ceramic Package
- Light Weight

Absolute Maximum Ratings**Pre-Irradiation**

	Parameter		Units
ID @ VGS = -12V, TC = 25°C	Continuous Drain Current	-14	A
ID @ VGS = -12V, TC = 100°C	Continuous Drain Current	-9.0	
IDM	Pulsed Drain Current ①	-56	
PD @ TC = 25°C	Max. Power Dissipation	150	W
	Linear Derating Factor	1.2	W/°C
VGS	Gate-to-Source Voltage	±20	V
EAS	Single Pulse Avalanche Energy ②	500	mJ
IAR	Avalanche Current ①	-14	A
EAR	Repetitive Avalanche Energy ①	15	mJ
dv/dt	Peak Diode Recovery dv/dt ③	-41	V/ns
TJ	Operating Junction	-55 to 150	°C
TSTG	Storage Temperature Range		
	PCKG Mounting Surface Temp.	300 (for 5s)	
	Weight	2.6 (typical)	g

For footnotes refer to the last page

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IRHN9250, JANSR2N7423U

Pre-Irradiation

Electrical Characteristics @ T_j = 25°C (Unless Otherwise Specified)

	Parameter	Min	Typ	Max	Units	Test Conditions
B _V DSS	Drain-to-Source Breakdown Voltage	-200	—	—	V	V _{GS} = 0V, I _D = -1.0mA
ΔB _V DSS/ΔT _J	Temperature Coefficient of Breakdown Voltage	—	-0.24	—	V/°C	Reference to 25°C, I _D = -1.0mA
R _{DS(on)}	Static Drain-to-Source On-State Resistance	—	—	0.315	Ω	V _{GS} = -12V, I _D = -9.0A④
		—	—	0.33		V _{GS} = -12V, I _D = -14A④
V _{GS(th)}	Gate Threshold Voltage	-2.0	—	-4.0	V	V _{DS} = V _{GS} , I _D = -1.0mA
g _{fs}	Forward Transconductance	4.0	—	—	S (S)	V _{DS} > -15V, I _{DS} = -9.0A ④
I _{DSS}	Zero Gate Voltage Drain Current	—	—	-25	μA	V _{DS} = -160V, V _{GS} = 0V
		—	—	-250		V _{DS} = -160V, V _{GS} = 0V, T _J = 125°C
I _{GSS}	Gate-to-Source Leakage Forward	—	—	-100	nA	V _{GS} = -20V
I _{GSS}	Gate-to-Source Leakage Reverse	—	—	100		V _{GS} = 20V
Q _g	Total Gate Charge	—	—	200	nC	V _{GS} = -12V, I _D = -14A V _{DS} = -100V
Q _{gs}	Gate-to-Source Charge	—	—	45		
Q _{gd}	Gate-to-Drain ('Miller') Charge	—	—	85		
t _{d(on)}	Turn-On Delay Time	—	—	60	ns	V _{DD} = -100V, I _D = -14A, V _{GS} = -12V, R _G = 2.35Ω
t _r	Rise Time	—	—	240		
t _{d(off)}	Turn-Off Delay Time	—	—	225		
t _f	Fall Time	—	—	220		
L _S + L _D	Total Inductance	—	4.0	—	nH	Measured from the center of drain pad to center of source pad
C _{iss}	Input Capacitance	—	4200	—	pF	V _{GS} = 0V, V _{DS} = -25V f = 1.0MHz
C _{oss}	Output Capacitance	—	690	—		
C _{rss}	Reverse Transfer Capacitance	—	160	—		

Source-Drain Diode Ratings and Characteristics

	Parameter	Min	Typ	Max	Units	Test Conditions
I _S	Continuous Source Current (Body Diode)	—	—	-14	A	
I _{SM}	Pulse Source Current (Body Diode) ①	—	—	-56		
V _{SD}	Diode Forward Voltage	—	—	-3.6	V	T _J = 25°C, I _S = -14A, V _{GS} = 0V ④
t _{rr}	Reverse Recovery Time	—	—	775	ns	T _J = 25°C, I _F = -14A, di/dt ≤ -100A/μs V _{DD} ≤ -50V ④
Q _{RR}	Reverse Recovery Charge	—	—	7.2	μC	
t _{on}	Forward Turn-On Time	Intrinsic turn-on time is negligible. Turn-on speed is substantially controlled by L _S + L _D .				

Thermal Resistance

	Parameter	Min	Typ	Max	Units	Test Conditions
R _{thJC}	Junction-to-Case	—	—	0.83	°C/W	soldered to a 1" square copper-clad board
R _{thJ-PCB}	Junction-to-PC board	—	6.6	—		

Note: Corresponding Spice and Saber models are available on the G&S Website.

For footnotes refer to the last page

Radiation Characteristics

IRHN9250, JANSR2N7423U

International Rectifier Radiation Hardened MOSFETs are tested to verify their radiation hardness capability. The hardness assurance program at International Rectifier is comprised of two radiation environments. Every manufacturing lot is tested for total ionizing dose (per notes 5 and 6) using the TO-3 package. Both pre- and post-irradiation performance are tested and specified using the same drive circuitry and test conditions in order to provide a direct comparison.

Table 1. Electrical Characteristics @ $T_j = 25^\circ\text{C}$, Post Total Dose Irradiation ⑤⑥

	Parameter	100K Rads(Si) ¹		300 K Rads (Si) ²		Units	Test Conditions
		Min	Max	Min	Max		
BV_{DSS}	Drain-to-Source Breakdown Voltage	-200	—	-200	—	V	$V_{GS} = 0V, I_D = -1.0mA$
$V_{GS(th)}$	Gate Threshold Voltage	-2.0	-4.0	-2.0	-5.0		$V_{GS} = V_{DS}, I_D = -1.0mA$
I_{GSS}	Gate-to-Source Leakage Forward	—	-100	—	-100	nA	$V_{GS} = -20V$
I_{GSS}	Gate-to-Source Leakage Reverse	—	100	—	100		$V_{GS} = 20V$
I_{DSS}	Zero Gate Voltage Drain Current	—	-25	—	-25	μA	$V_{DS} = -160V, V_{GS} = 0V$
$R_{DS(on)}$	Static Drain-to-Source ④ On-State Resistance (TO-3)	—	0.317	—	0.317	Ω	$V_{GS} = -12V, I_D = -9.0A$
$R_{DS(on)}$	Static Drain-to-Source ④ On-State Resistance (SMD-1)	—	0.315	—	0.315	Ω	$V_{GS} = -12V, I_D = -9.0A$
V_{SD}	Diode Forward Voltage ④	—	-3.6	—	-3.6	V	$V_{GS} = 0V, I_S = -14A$

1. Part number IRHN9250 (JANSR2N7423U)

2. Part numbers IRHN93250 (JANSF2N7423U)

International Rectifier radiation hardened MOSFETs have been characterized in heavy ion environment for Single Event Effects (SEE). Single Event Effects characterization is illustrated in Fig. a and Table 2.

Table 2. Single Event Effect Safe Operating Area

Ion	LET MeV/(mg/cm ²)	Energy (MeV)	Range (μm)	V_{DS} (V)				
				@ $V_{GS} = 0V$	@ $V_{GS} = 5V$	@ $V_{GS} = 10V$	@ $V_{GS} = 15V$	@ $V_{GS} = 20V$
Cu	28.0	285	43	-200	-200	-200	-200	—
Br	36.8	305	39	-200	-200	-160	-75	—

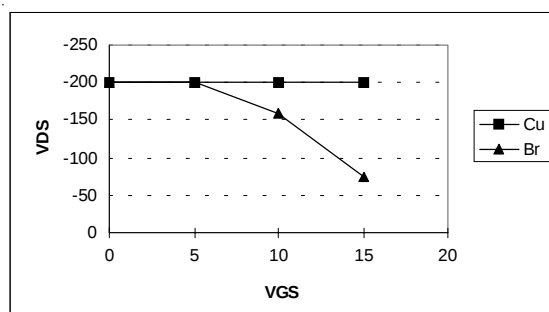


Fig a. Single Event Effect, Safe Operating Area

For footnotes refer to the last page

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Pre-Irradiation

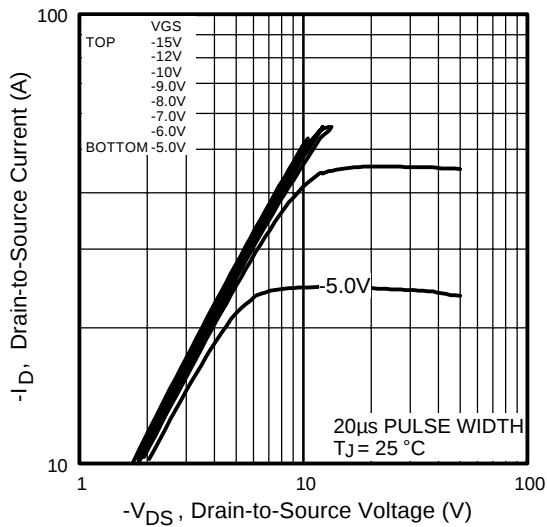


Fig1. Typical Output Characteristics

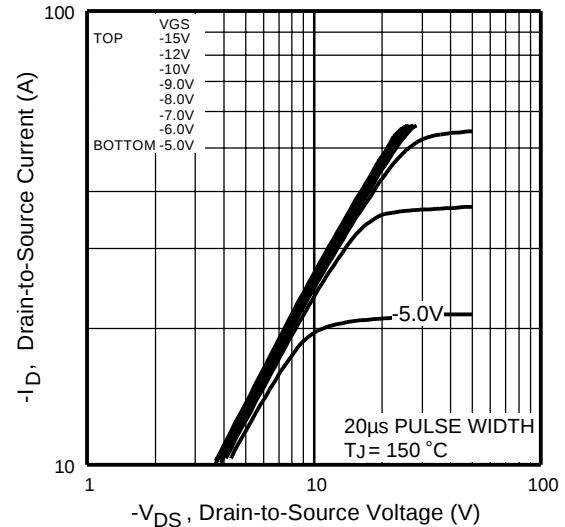


Fig2. Typical Output Characteristics

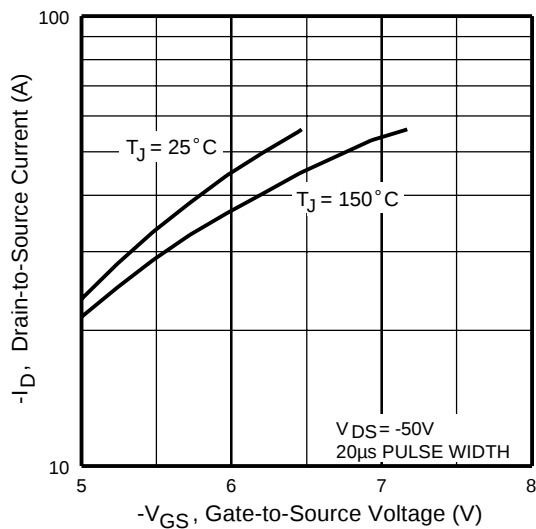


Fig3. Typical Transfer Characteristics

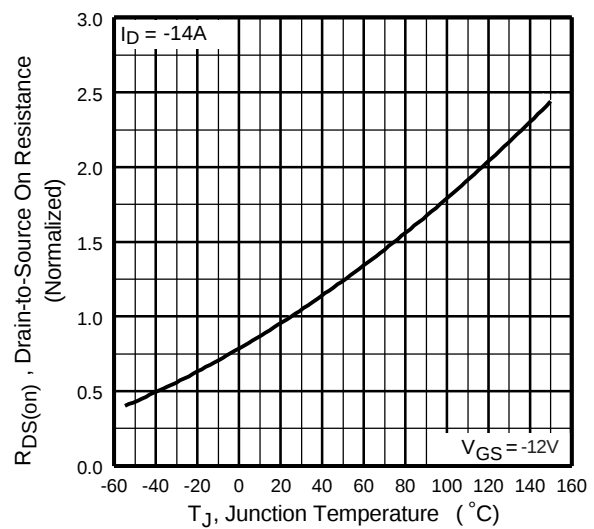


Fig4. Normalized On-Resistance Vs. Temperature

Pre-Irradiation

IRHN9250, JANSR2N7423U

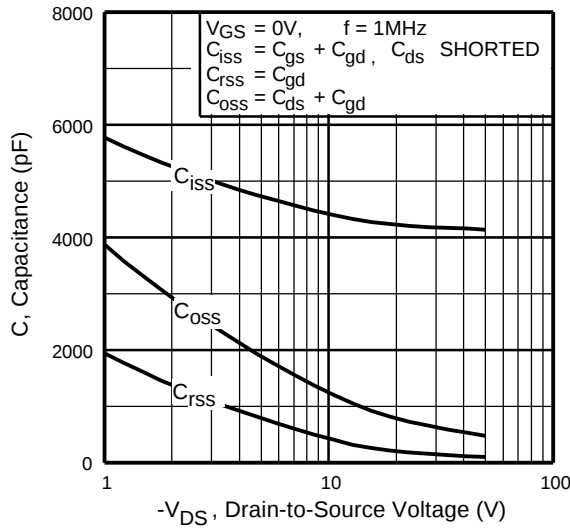


Fig5. Typical Capacitance Vs. Drain-to-Source Voltage

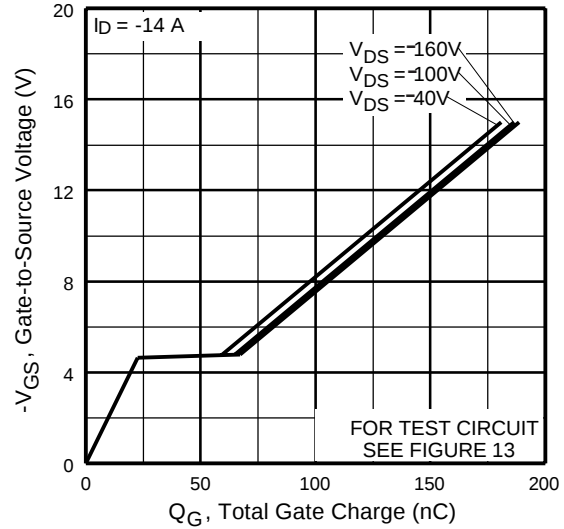


Fig6. Typical Gate Charge Vs. Gate-to-Source Voltage

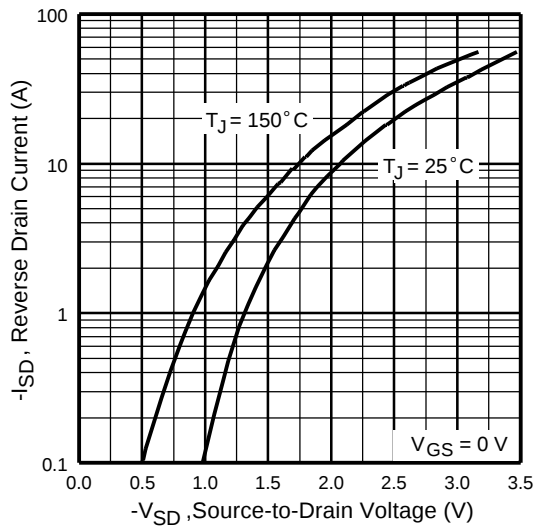


Fig7. Typical Source-Drain Diode Forward Voltage

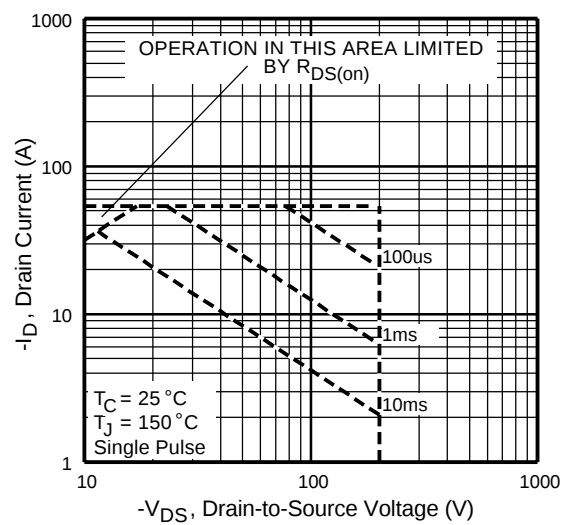


Fig8. Maximum Safe Operating Area

IRHN9250, JANSR2N7423U

Pre-Irradiation

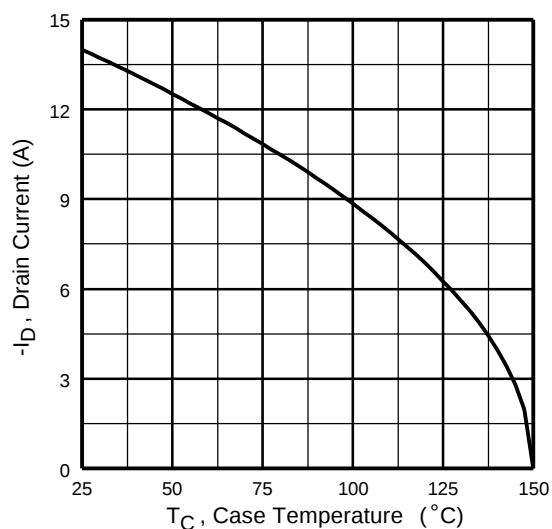


Fig9. Maximum Drain Current Vs. Case Temperature

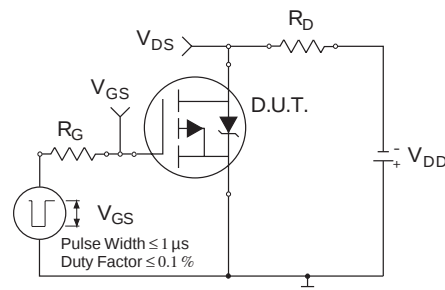


Fig10a. Switching Time Test Circuit

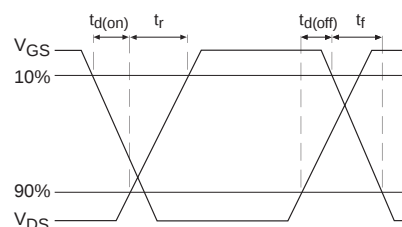


Fig10b. Switching Time Waveforms

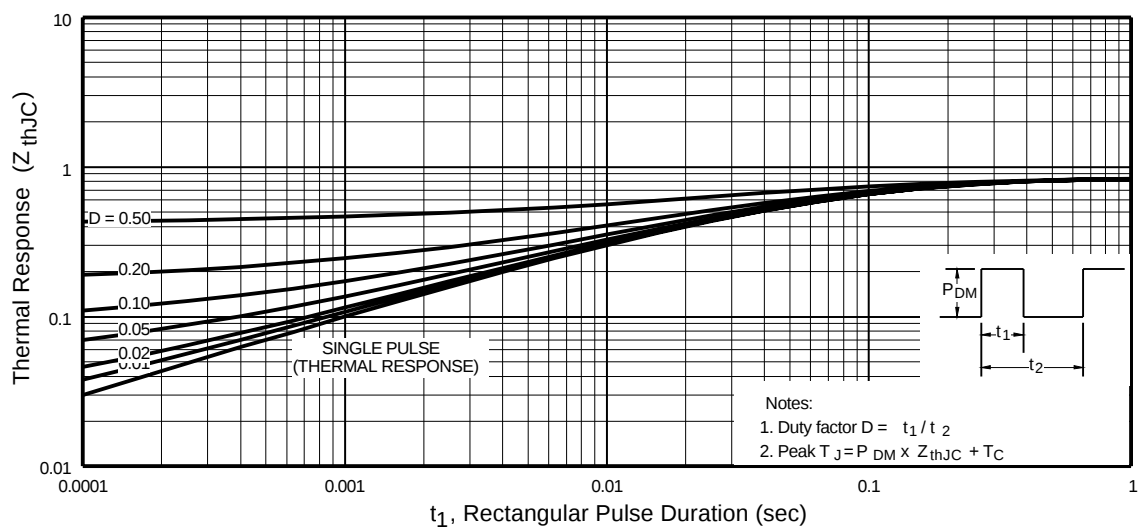


Fig11. Maximum Effective Transient Thermal Impedance, Junction-to-Case

Pre-Irradiation

IRHN9250, JANSR2N7423U

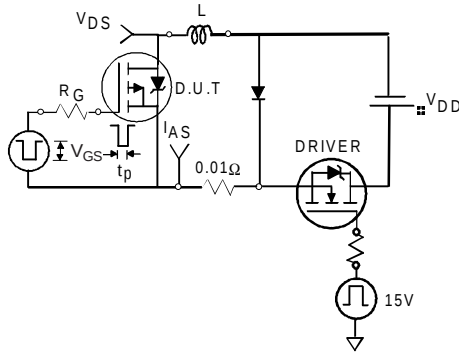


Fig 12a. Unclamped Inductive Test Circuit

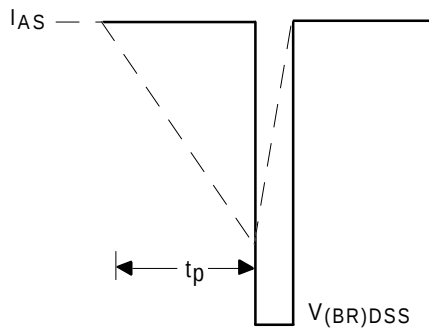


Fig 12b. Unclamped Inductive Waveforms

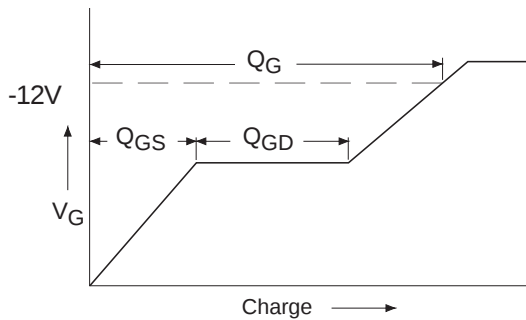


Fig 13a. Basic Gate Charge Waveform

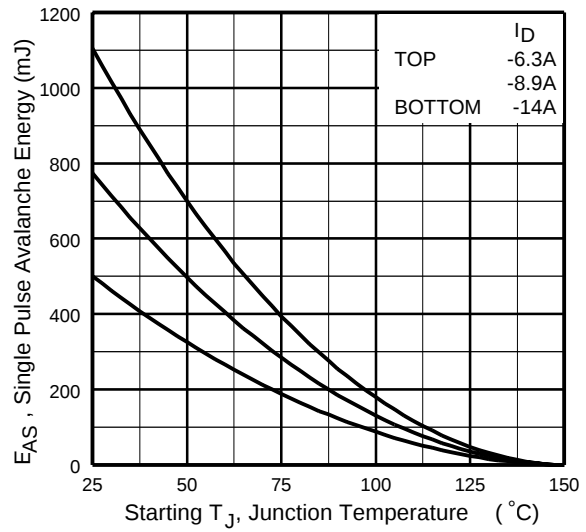


Fig 12c. Maximum Avalanche Energy Vs. Drain Current

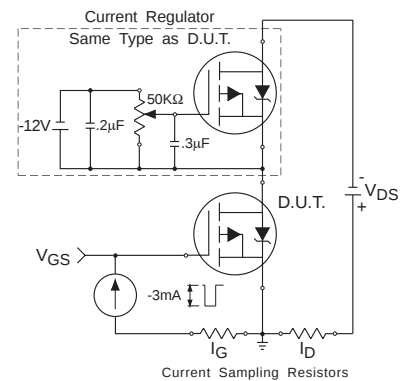
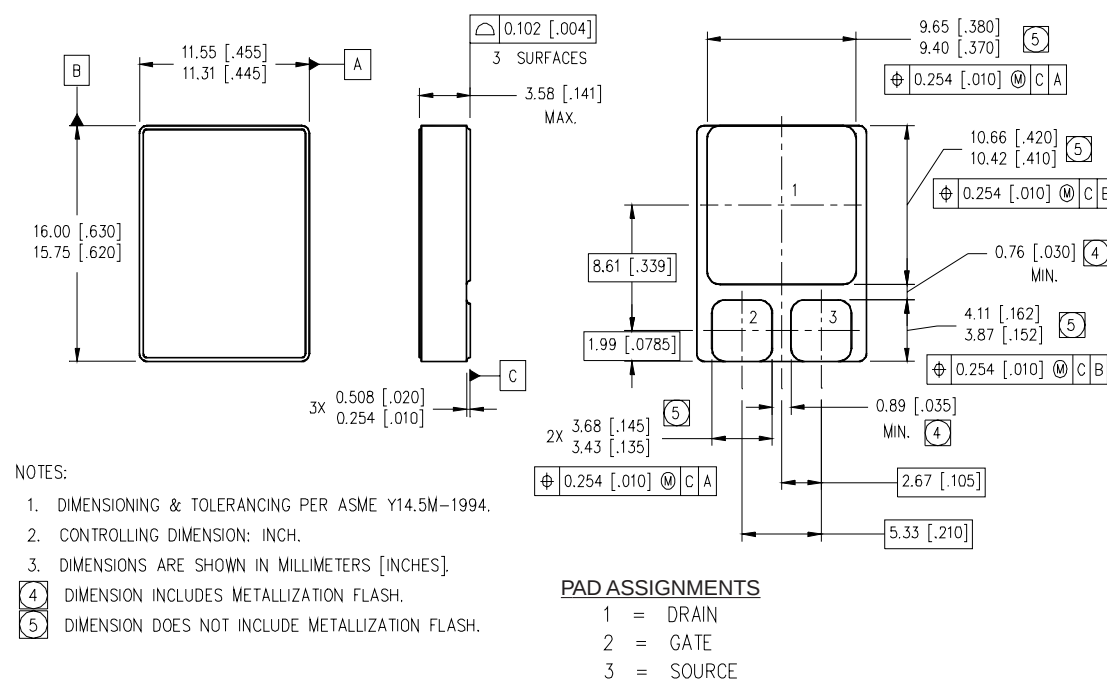


Fig 13b. Gate Charge Test Circuit

IRHN9250, JANSR2N7423U**Pre-Irradiation****Foot Notes:**

- ① Repetitive Rating; Pulse width limited by maximum junction temperature.
- ② $V_{DD} = -50V$, starting $T_J = 25^\circ C$, $L = 5.1mH$
Peak $I_L = -14A$, $V_{GS} = -12V$
- ③ $I_{SD} \leq -14A$, $di/dt \leq -600A/\mu s$,
 $V_{DD} \leq -200V$, $T_J \leq 150^\circ C$
- ④ Pulse width $\leq 300 \mu s$; Duty Cycle $\leq 2\%$
- ⑤ **Total Dose Irradiation with V_{GS} Bias.**
-12 volt V_{GS} applied and $V_{DS} = 0$ during irradiation per MIL-STD-750, method 1019, condition A.
- ⑥ **Total Dose Irradiation with V_{DS} Bias.**
-160 volt V_{DS} applied and $V_{GS} = 0$ during irradiation per MIL-STD-750, method 1019, condition A.

Case Outline and Dimensions — SMD-1

International
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