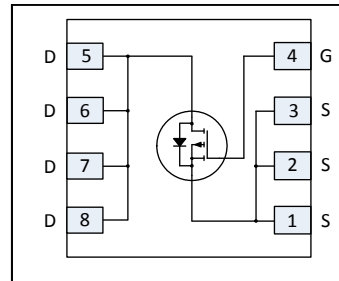


HEXFET® Power MOSFET

<b>V<sub>DSS</sub></b>	<b>-30</b>	<b>V</b>
<b>R<sub>DS(on)</sub> max</b> (@ V <sub>GS</sub> = -10V)	<b>14.6</b>	<b>mΩ</b>
(@ V <sub>GS</sub> = -4.5V)	<b>22.5</b>	
<b>Qg</b> (typical)	<b>32</b>	<b>nC</b>
<b>I<sub>D</sub></b> (@T <sub>A</sub> = 25°C)	<b>-11</b>	<b>A</b>



### Applications

- System/load switch,
- Charge or discharge switch for battery protection

### Features

Low Thermal Resistance to PCB (<3.8°C/W)
Low Profile (<1.05 mm)
Industry-Standard Pinout
Compatible with Existing Surface Mount Techniques
RoHS Compliant, Halogen-Free
MSL1, Consumer Qualification

results in  
⇒

### Benefits

Enable better Thermal Dissipation
Increased Power Density
Multi-Vendor Compatibility
Easier Manufacturing
Environmentally Friendlier
Increased Reliability

Base part number	Package Type	Standard Pack		Orderable Part Number
		Form	Quantity	
IRFHM9391PbF	PQFN 3.3mm x 3.3mm	Tape and Reel	4000	IRFHM9391TRPbF

### Absolute Maximum Ratings

	Parameter	Max.	Units
V <sub>GS</sub>	Gate-to-Source Voltage	± 25	V
I <sub>D</sub> @ T <sub>A</sub> = 25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V	-11	A
I <sub>D</sub> @ T <sub>A</sub> = 70°C	Continuous Drain Current, V <sub>GS</sub> @ 10V	-9.0	
I <sub>DM</sub>	Pulsed Drain Current	-90	
I <sub>D</sub> @ T <sub>C(Bottom)</sub> = 25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V	-38 <sup>⑤⑥</sup>	
I <sub>D</sub> @ T <sub>C(Bottom)</sub> = 100°C	Continuous Drain Current, V <sub>GS</sub> @ 10V	-24 <sup>⑤⑥</sup>	
I <sub>D</sub> @ T <sub>C</sub> = 25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V (Source Bonding Technology Limited)	-24 <sup>⑥</sup>	
P <sub>D</sub> @ T <sub>A</sub> = 25°C	Power Dissipation <sup>④</sup>	2.6	W
P <sub>D</sub> @ T <sub>C(Bottom)</sub> = 25°C	Power Dissipation <sup>④</sup>	33	
	Linear Derating Factor <sup>④</sup>	0.021	
T <sub>J</sub> T <sub>STG</sub>	Operating Junction and Storage Temperature Range	-55 to + 150	°C

Notes ① through ⑥ are on page 9

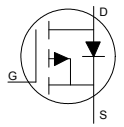
**Static @ T<sub>J</sub> = 25°C (unless otherwise specified)**

	Parameter	Min.	Typ.	Max.	Units	Conditions
BV <sub>DSS</sub>	Drain-to-Source Breakdown Voltage	-30	—	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = -250μA
ΔBV <sub>DSS</sub> /ΔT <sub>J</sub>	Breakdown Voltage Temp. Coefficient	—	0.02	—	V/°C	Reference to 25°C, I <sub>D</sub> = -1mA
R <sub>DS(on)</sub>	Static Drain-to-Source On-Resistance	—	10	—	mΩ	V <sub>GS</sub> = -20V, I <sub>D</sub> = -11A ②
		—	11.7	14.6		V <sub>GS</sub> = -10V, I <sub>D</sub> = -11A ②
		—	18	22.5		V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -11A ②
V <sub>GS(th)</sub>	Gate Threshold Voltage	-1.3	-1.8	-2.4	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -25μA
ΔV <sub>GS(th)</sub>	Gate Threshold Voltage Coefficient	—	-5.1	—	mV/°C	
I <sub>DSS</sub>	Drain-to-Source Leakage Current	—	—	-1.0	μA	V <sub>DS</sub> = -24V, V <sub>GS</sub> = 0V
		—	—	-150		V <sub>DS</sub> = -24V, V <sub>GS</sub> = 0V, T <sub>J</sub> = 125°C
I <sub>GSS</sub>	Gate-to-Source Forward Leakage	—	—	-10	μA	V <sub>GS</sub> = -25V
	Gate-to-Source Reverse Leakage	—	—	10		V <sub>GS</sub> = 25V
g <sub>fs</sub>	Forward Transconductance	16	—	—	S	V <sub>DS</sub> = -10V, I <sub>D</sub> = -9.0A
Q <sub>g</sub>	Total Gate Charge	—	16	—	nC	V <sub>GS</sub> = -4.5V, V <sub>DS</sub> = -15V, I <sub>D</sub> = -9.0A
Q <sub>g</sub>	Total Gate Charge	—	32	48	nC	V <sub>DS</sub> = -15V V <sub>GS</sub> = -10V I <sub>D</sub> = -9.0A
Q <sub>gs1</sub>	Pre-V <sub>th</sub> Gate-to-Source Charge	—	3.0	—		
Q <sub>gs2</sub>	Post-V <sub>th</sub> Gate-to-Source Charge	—	1.4	—		
Q <sub>gd</sub>	Gate-to-Drain Charge	—	8.0	—		
Q <sub>godr</sub>	Gate Charge Overdrive	—	19.6	—		
Q <sub>sw</sub>	Switch Charge (Q <sub>gs2</sub> + Q <sub>gd</sub> )	—	9.4	—		
Q <sub>oss</sub>	Output Charge	—	9.0	—	nC	V <sub>DS</sub> = -16V, V <sub>GS</sub> = 0V
R <sub>G</sub>	Gate Resistance	—	16	—	Ω	
t <sub>d(on)</sub>	Turn-On Delay Time	—	11	—	ns	V <sub>DD</sub> = -15V, V <sub>GS</sub> = -4.5V ② I <sub>D</sub> = -1.0A R <sub>G</sub> = 6.8Ω
t <sub>r</sub>	Rise Time	—	27	—		
t <sub>d(off)</sub>	Turn-Off Delay Time	—	72	—		
t <sub>f</sub>	Fall Time	—	60	—		
C <sub>iss</sub>	Input Capacitance	—	1543	—	pF	V <sub>GS</sub> = 0V V <sub>DS</sub> = -25V f = 1.0KHz
C <sub>oss</sub>	Output Capacitance	—	310	—		
C <sub>rss</sub>	Reverse Transfer Capacitance	—	208	—		

**Avalanche Characteristics**

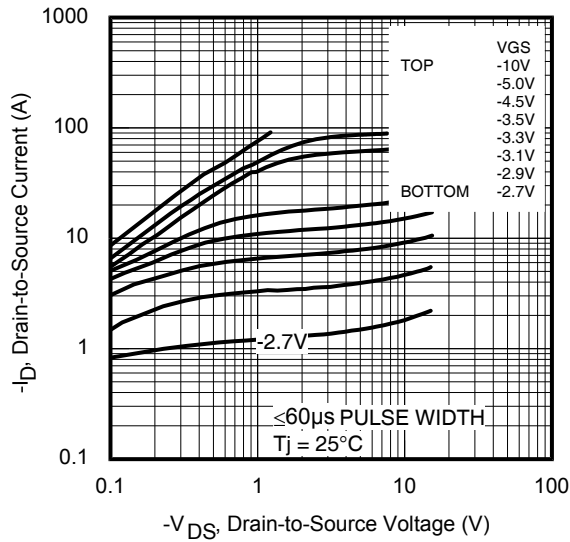
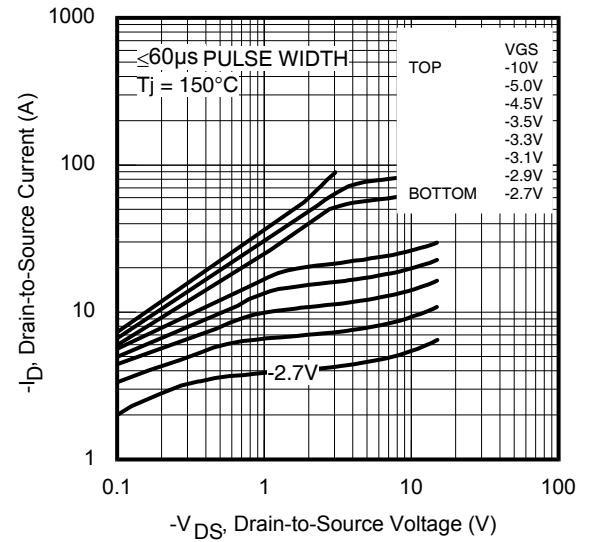
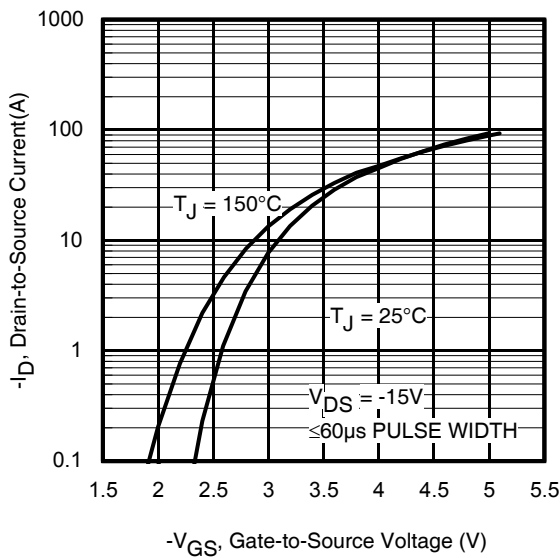
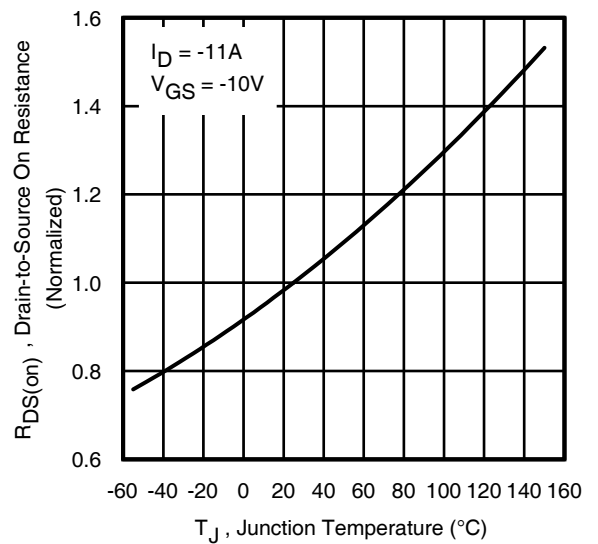
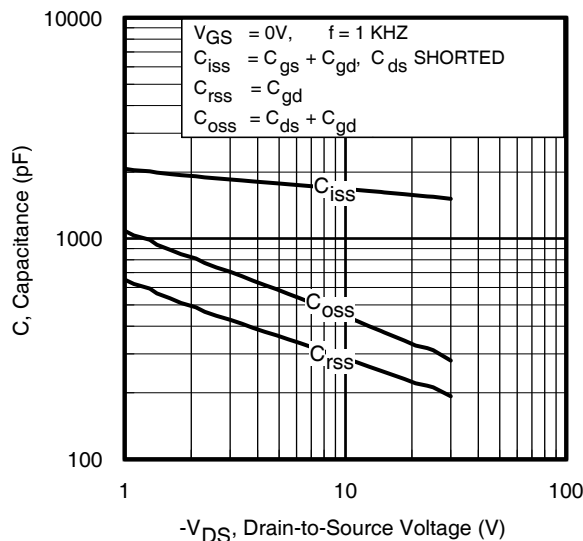
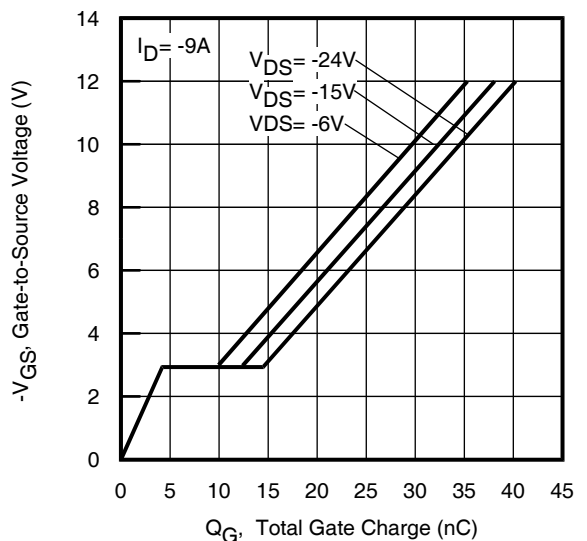
	Parameter	Typ.	Max.	Units
E <sub>AS</sub>	Single Pulse Avalanche Energy ①	—	75	mJ

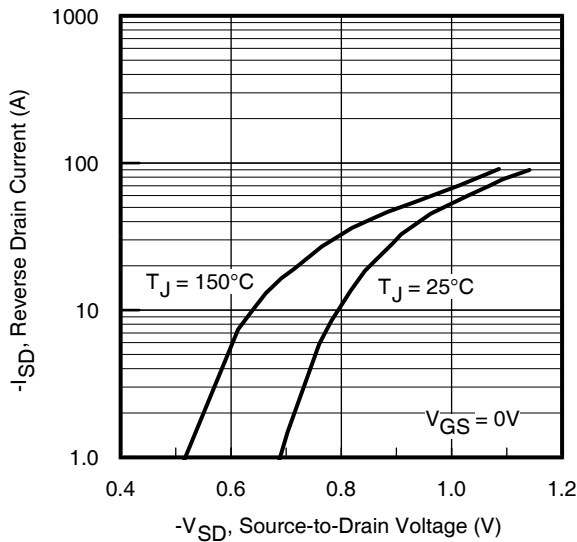
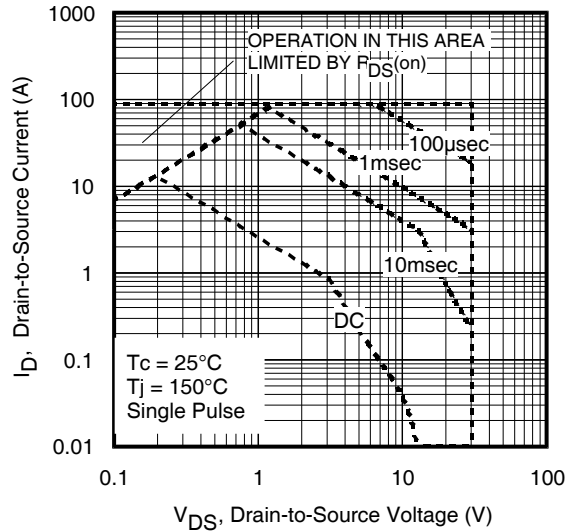
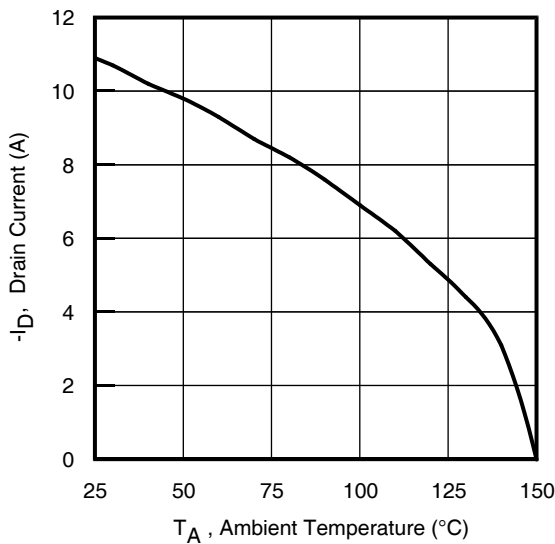
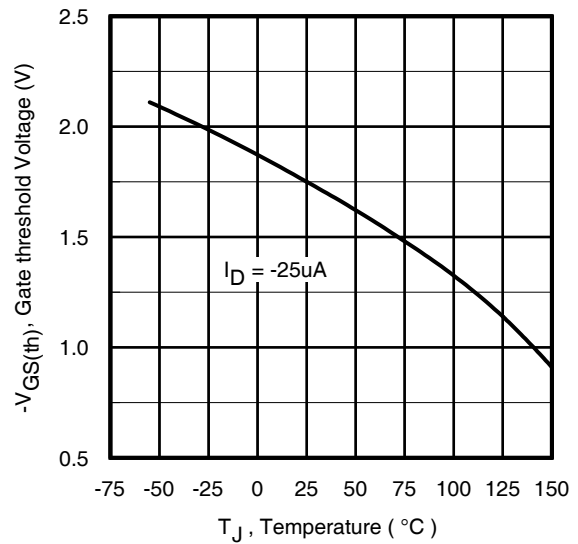
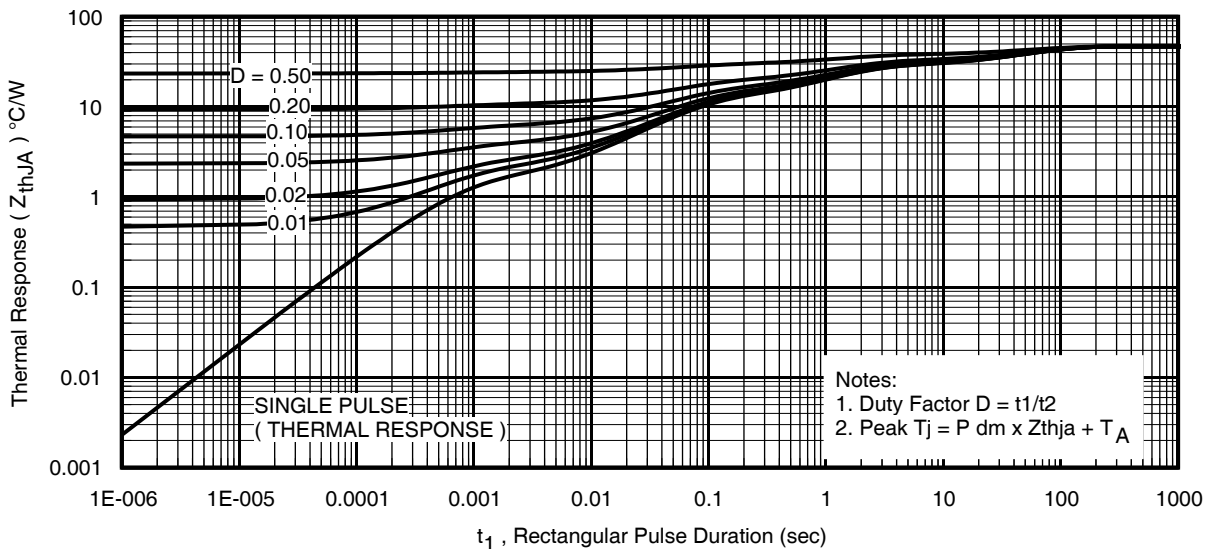
**Diode Characteristics**

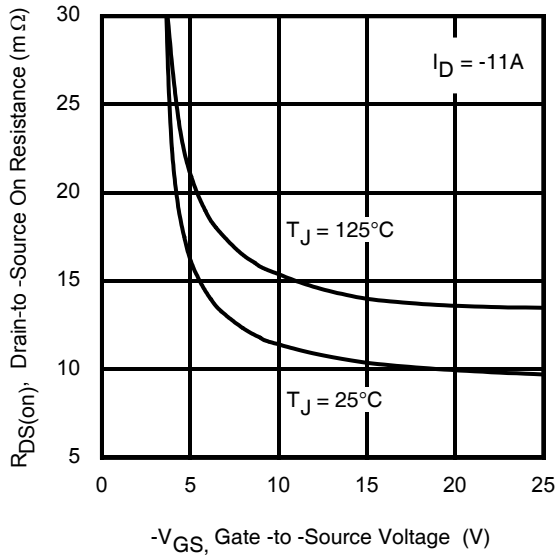
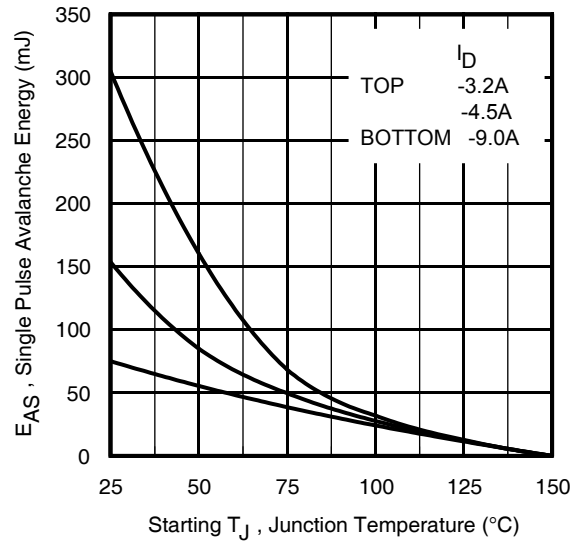
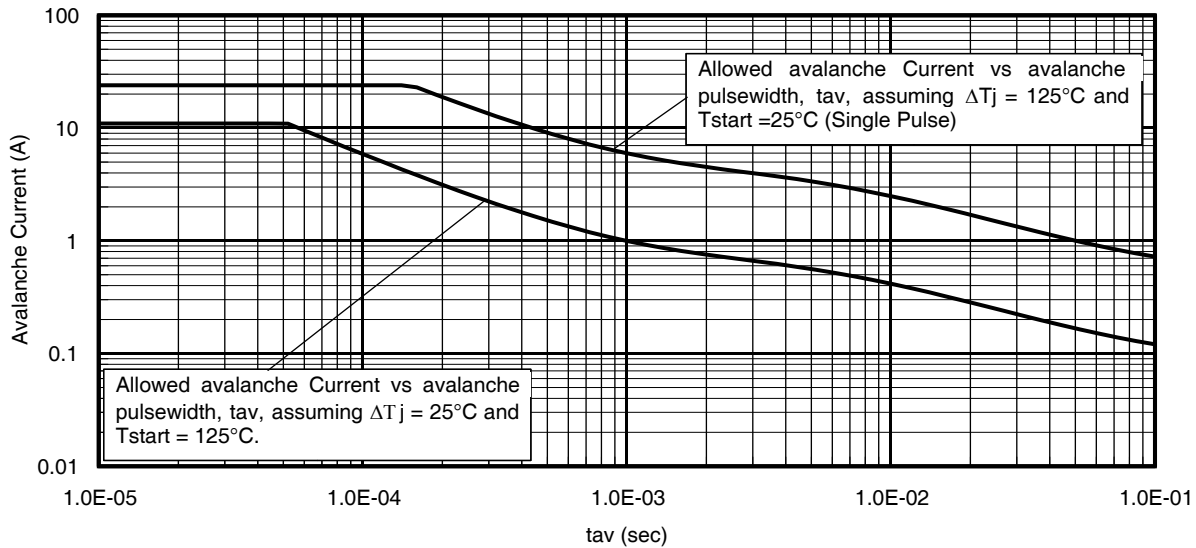
	Parameter	Min.	Typ.	Max.	Units	Conditions
I <sub>S</sub>	Continuous Source Current (Body Diode)	—	—	-2.8	A	MOSFET symbol showing the integral reverse p-n junction diode. 
I <sub>SM</sub>	Pulsed Source Current (Body Diode) ①	—	—	-90		
V <sub>SD</sub>	Diode Forward Voltage	—	—	-1.2	V	T <sub>J</sub> = 25°C, I <sub>S</sub> = -2.8A, V <sub>GS</sub> = 0V ②
t <sub>rr</sub>	Reverse Recovery Time	—	64	96	ns	T <sub>J</sub> = 25°C, I <sub>F</sub> = -2.8A, V <sub>DD</sub> = -24V
Q <sub>rr</sub>	Reverse Recovery Charge	—	25	38	nC	di/dt = 100A/μs ②

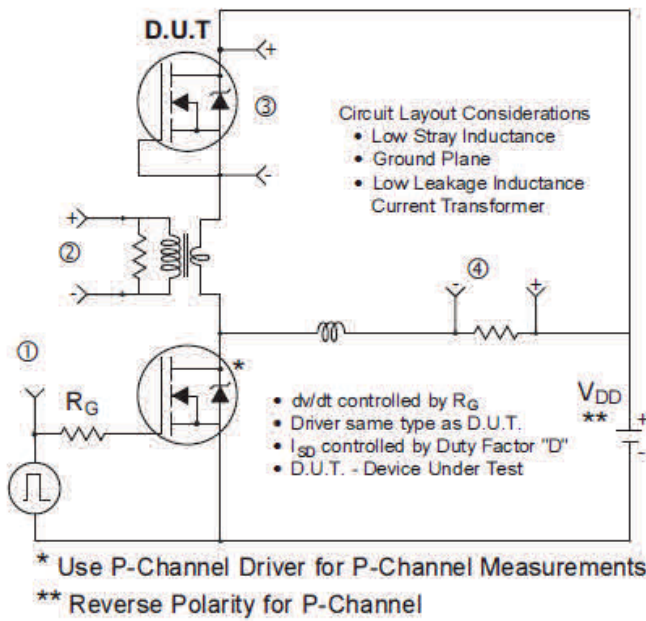
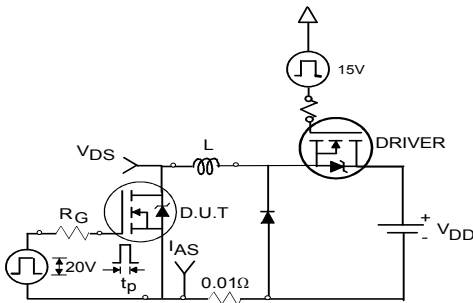
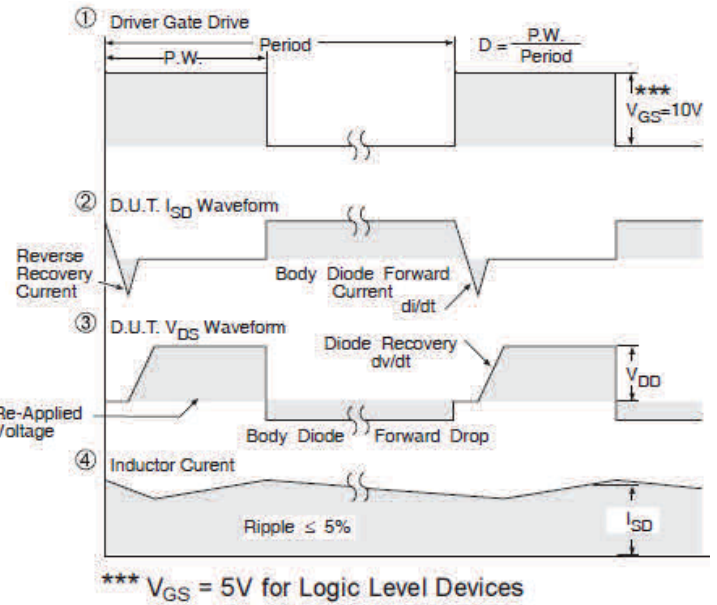
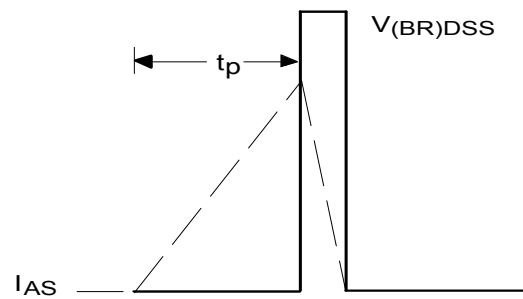
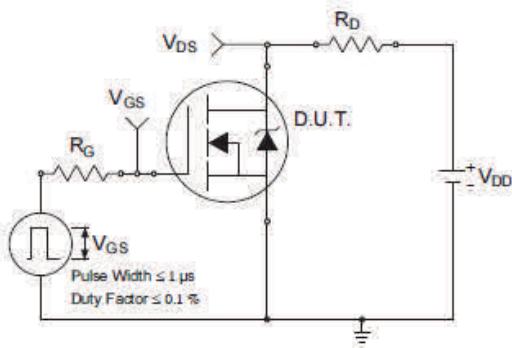
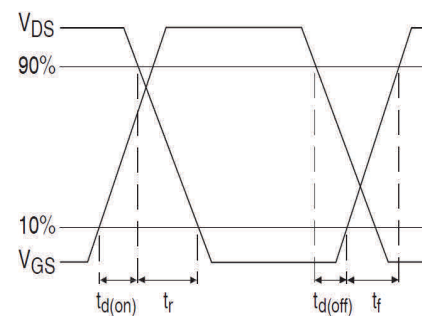
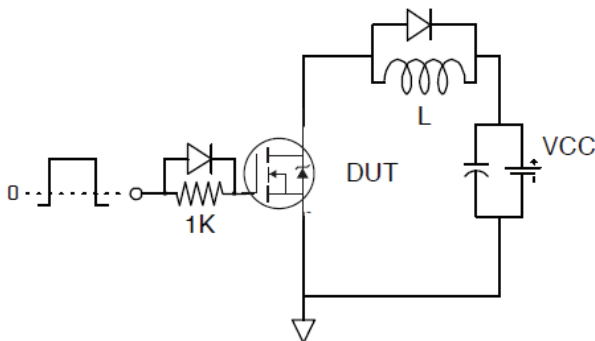
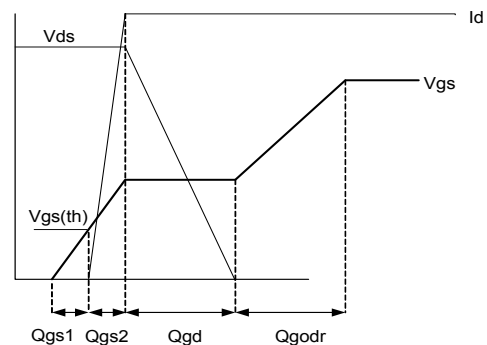
**Thermal Resistance**

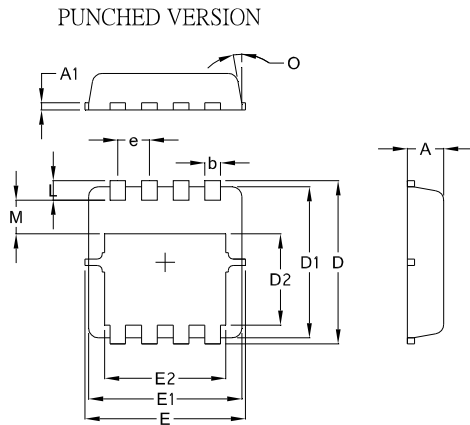
	Parameter	Typ.	Max.	Units
R <sub>θJC</sub> (Bottom)	Junction-to-Case ③	—	3.8	°C/W
R <sub>θJC</sub> (Top)	Junction-to-Case ③	—	42	
R <sub>θJA</sub>	Junction-to-Ambient ④	—	47	
R <sub>θJA</sub> (<10s)	Junction-to-Ambient ④	—	32	


**Fig 1.** Typical Output Characteristics

**Fig 2.** Typical Output Characteristics

**Fig 3.** Typical Transfer Characteristics

**Fig 4.** Normalized On-Resistance vs. Temperature

**Fig 5.** Typical Capacitance vs. Drain-to-Source Voltage

**Fig 6.** Typical Gate Charge vs. Gate-to-Source Voltage


**Fig 7.** Typical Source-Drain Diode Forward Voltage

**Fig 8.** Maximum Safe Operating Area

**Fig 9.** Maximum Drain Current vs. Case Temperature

**Fig 10.** Threshold Voltage Vs. Temperature

**Fig 11.** Maximum Effective Transient Thermal Impedance, Junction-to-Case


**Fig 12.** On- Resistance vs. Gate Voltage

**Fig 13.** Maximum Avalanche Energy vs. Drain Current

**Fig 14.** Typical Avalanche Current vs. Pulsewidth

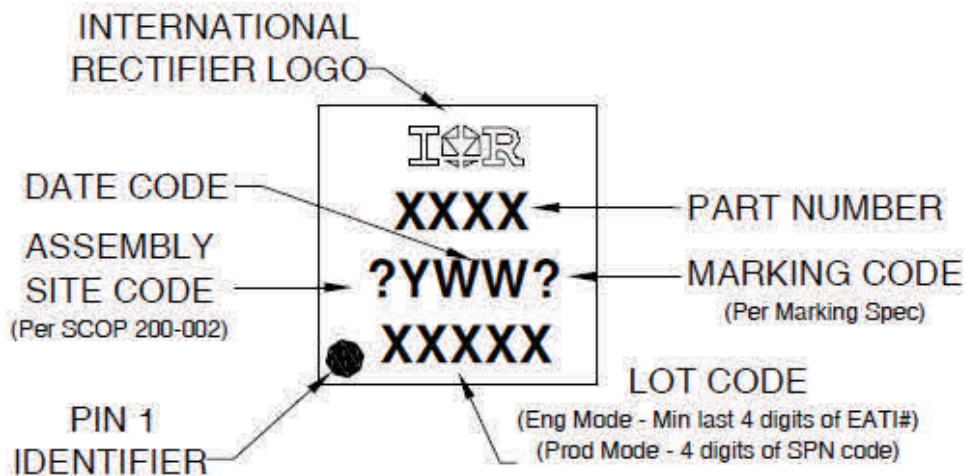

**Fig 15. Peak Diode Recovery  $dv/dt$  Test Circuit for N-Channel HEXFET<sup>®</sup> Power MOSFETs**

**Fig 16a. Unclamped Inductive Test Circuit**

**Fig 16b. Unclamped Inductive Waveforms**

**Fig 17a. Switching Time Test Circuit**

**Fig 17b. Switching Time Waveforms**

**Fig 18. Gate Charge Test Circuit**

**Fig 19. Gate Charge Waveform**

**PQFN 3.3 x 3.3 Package Details**


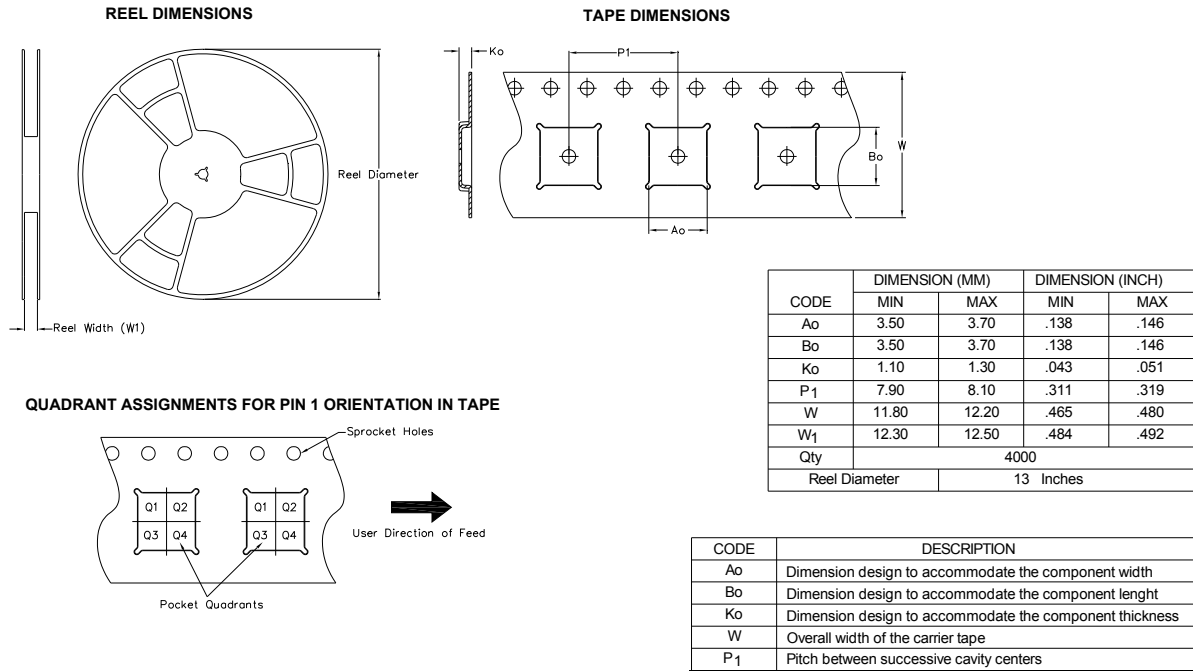
SYMBOL	COMMON			
	MM		INCH	
	MIN.	MAX.	MIN.	MAX.
A	0.70	1.05	0.0276	0.0413
A1	0.12	0.39	0.0047	0.0154
b	0.25	0.39	0.0098	0.0154
D	3.20	3.45	0.1260	0.1358
D1	3.00	3.20	0.1181	0.1417
D2	1.69	2.20	0.0665	0.0866
E	3.20	3.40	0.1260	0.1339
E1	3.00	3.20	0.1181	0.1417
E2	2.15	2.59	0.0846	0.1020
e	0.65 BSC		0.0256 BSC	
L	0.15	0.55	0.0059	0.0217
M	0.59	—	0.0232	—
O	9Deg	12Deg	9Deg	12Deg

For more information on board mounting, including footprint and stencil recommendation, please refer to application note AN-1136: <http://www.irf.com/technical-info/appnotes/an-1136.pdf>

For more information on package inspection techniques, please refer to application note AN-1154: <http://www.irf.com/technical-info/appnotes/an-1154.pdf>

**PQFN 3.3 x 3.3 Part Marking**


Note: For the most current drawing please refer to IR website at <http://www.irf.com/package/>

**PQFN 3.3 x 3.3 Tape and Reel**


Note: For the most current drawing please refer to IR website at <http://www.irf.com/package/>



**Qualification Information<sup>†</sup>**

<b>Qualification Level</b>	Consumer <sup>††</sup> (per JEDEC JESD47F guidelines)	
<b>Moisture Sensitivity Level</b>	PQFN 3.3mm x 3.3mm	MSL1 (per JEDEC J-STD-020D <sup>††</sup> )
<b>RoHS Compliant</b>	Yes	

† Qualification standards can be found at International Rectifier's web site: <http://www.irf.com/product-info/reliability/>

†† Applicable version of JEDEC standard at the time of product release.

**Notes:**

- ① Starting  $T_J = 25^{\circ}\text{C}$ ,  $L = 1.872\text{mH}$ ,  $R_G = 50\Omega$ ,  $I_{AS} = -9\text{A}$ .
- ② Pulse width  $\leq 400\mu\text{s}$ ; duty cycle  $\leq 2\%$ .
- ③  $R_{\theta}$  is measured at  $T_J$  of approximately  $90^{\circ}\text{C}$ .
- ④ When mounted on 1 inch square PCB (FR-4). Please refer to AN-994 for more details:  
<http://www.irf.com/technical-info/appnotes/an-994.pdf>
- ⑤ Calculated continuous current based on maximum allowable junction temperature.
- ⑥ Current is limited by source bonding technology.

**Revision History**

<b>Date</b>	<b>Comments</b>
7/1/14	<ul style="list-style-type: none"> <li>• Remove "SAWN" package outline on page 7.</li> <li>• Updated part marking on page 7.</li> <li>• Updated tape and reel on page 8.</li> </ul>

International  
 Rectifier

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To contact International Rectifier, please visit <http://www.irf.com/whoto-call/>