

HVD399C

Variable Capacitance Diode for VCO

REJ03G0051-0200 Rev.2.00 Mar 30, 2006

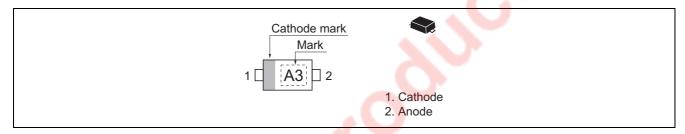
Features

- High capacitance ratio. (n = 2.30 to 2.46)
- Low series resistance. (rs = $0.40 \Omega \text{ max}$)
- Super small Flat Lead Package (SFP) is suitable for surface mount design.

Ordering Information

Type No.	Laser Mark	Package Name	Package Code
HVD399C	A3	SFP	PUSF0002ZB-A

Pin Arrangement



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

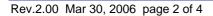
Item	Symbol	Value	Unit
Reverse voltage	V _R	10	V
Junction temperature	Tj	125	°C
Storage temperature	Tstg	-55 to +125	°C

Electrical Characteristics

 $(Ta = 25^{\circ}C)$

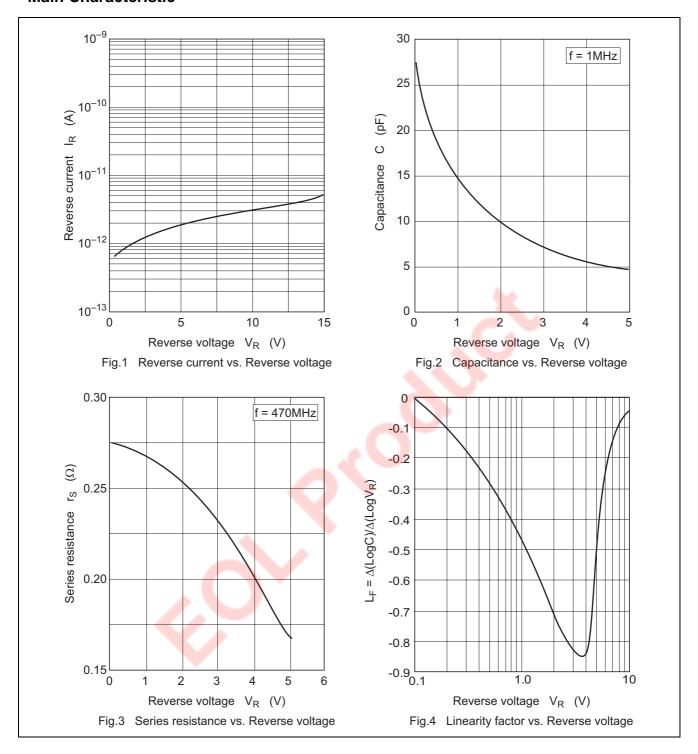
Item	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse current	I _{R1}	_	_	10	nA	V _R = 10 V
	I _{R2}	_	_	50		V _R = 10 V, Ta = 60°C
Capacitance	C _{0.5}	18.5	_	20.0	pF	$V_R = 0.5 \text{ V}, f = 1 \text{ MHz}$
	C _{2.5}	7.30	_	8.60		$V_R = 2.5 \text{ V}, f = 1 \text{ MHz}$
Capacitance ratio	n	2.30	_	2.46	_	C _{0.5} /C _{2.5}
Series resistance	rs	_	_	0.40	Ω	V _R = 1 V, f = 470 MHz

Note: For SFP package, the material of lead is exposed for cutting plane. There for, soldering nature of lead tip part is considered as unquestioned. Please kindly consider soldering nature.

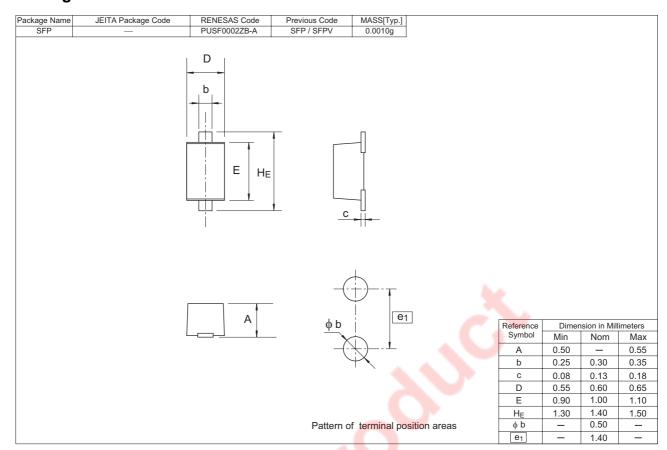




Main Characteristic



Package Dimensions



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April 1st, 2010 Renesas Electronics Corporation

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