

HVD380B

Variable Capacitance Diode for VCO

HITACHI

ADE-208-1168 (Z)
Rev. 0
Apr. 2001

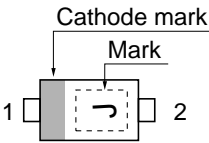
Features

- High capacitance ratio. ($n = 1.70$ min)
- Low series resistance. ($r_s = 0.80 \, \Omega$ max)
- Super small Flat Package (SFP) is suitable for surface mount design.

Ordering Information

Type No.	Laser Mark	Package Code
HVD380B	J	SFP

Outline



1. Cathode
2. Anode

Absolute Maximum Ratings (Ta = 25°C)

Item	Symbol	Value	Unit
Reverse voltage	V _R	15	V
Junction temperature	T _j	125	°C
Storage temperature	T _{stg}	−55 to +125	°C

Electrical Characteristics (Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse current	I _{R1}	—	—	10.0	nA	V _R = 15 V
	I _{R2}	—	—	100		V _R = 15 V, Ta = 60°C
Capacitance	C ₁	2.880	—	3.120	pF	V _R = 1 V, f = 1 MHz
	C ₃	1.660	—	1.795		V _R = 3 V, f = 1 MHz
	C ₄	1.360	—	1.471		V _R = 4 V, f = 1 MHz
Capacitance ratio	n ₁	1.70	—	1.84	—	C ₁ / C ₃
	n ₂	2.08	—	2.25		C ₁ / C ₄
Series resistance	r _s	—	—	0.80	Ω	V _R = 1 V, f = 470 MHz

Note: Please do not use the soldering iron due to avoid high stress to the SFP package.

Main Characteristic

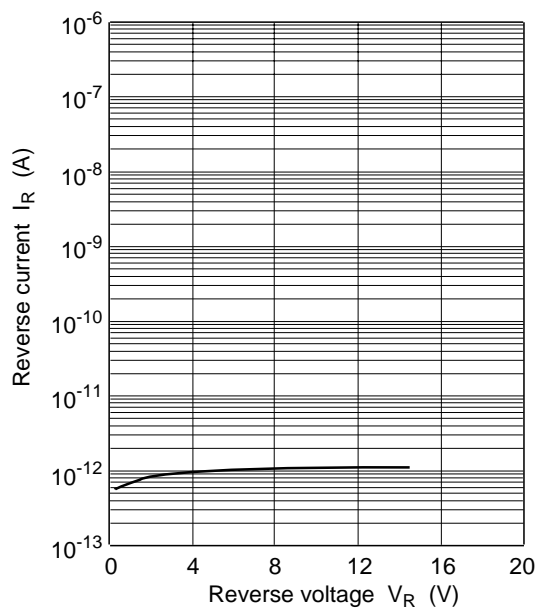


Fig.1 Reverse current vs. Reverse voltage

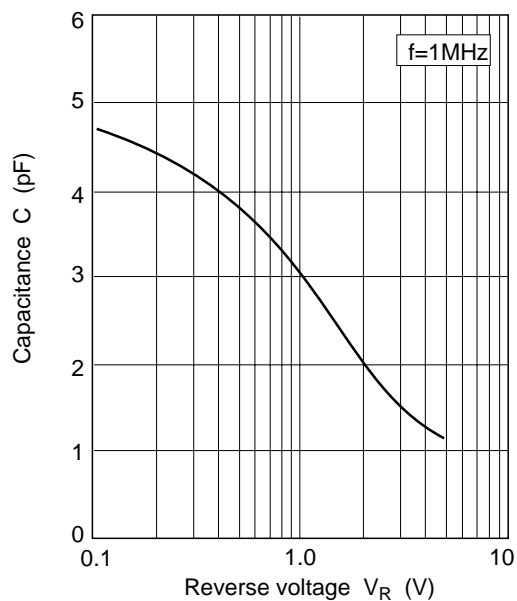


Fig.2 Capacitance vs. Reverse voltage

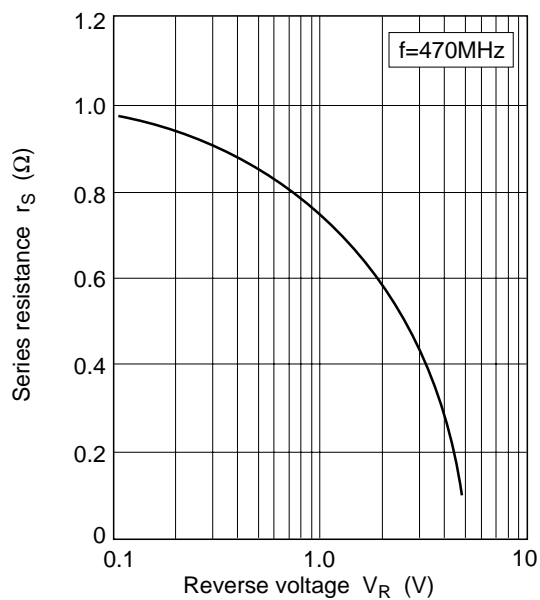
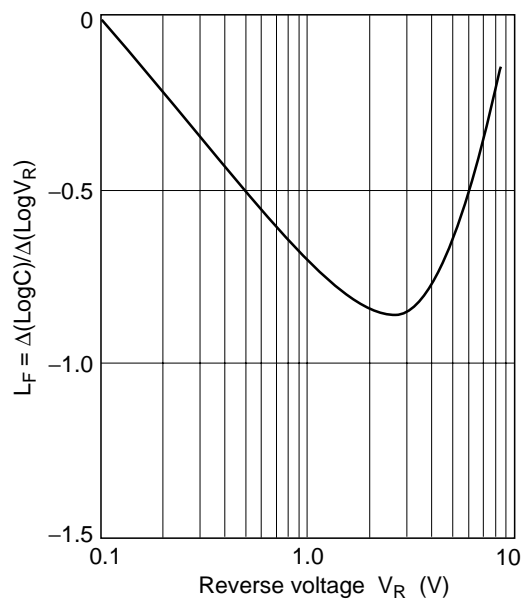
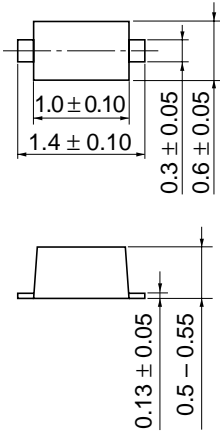


Fig.3 Series resistance vs. Reverse voltage

Fig.4 L_F vs. Reverse voltage

Package Dimensions

As of January, 2001
Unit: mm



Hitachi Code	SFP
JEDEC	—
EIAJ	—
Mass (reference value)	0.0010 g

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HITACHI

Hitachi, Ltd.

Semiconductor & Integrated Circuits.
Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan
Tel: Tokyo (03) 3270-2111 Fax: (03) 3270-5109

URL	NorthAmerica	:	http://semiconductor.hitachi.com/
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For further information write to:

Hitachi Semiconductor (America) Inc. 179 East Tasman Drive, San Jose, CA 95134 Tel: <1> (408) 433-1990 Fax: <1> (408) 433-0223	Hitachi Europe Ltd. Electronic Components Group. Whitebrook Park Lower Cookham Road Maidenhead Berkshire SL6 8YA, United Kingdom Tel: <44> (1628) 585000 Fax: <44> (1628) 585200
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Hitachi Europe GmbH
Electronic Components Group
Dornacher Straße 3
D-85622 Feldkirchen, Munich
Germany
Tel: <49> (89) 9 9180-0
Fax: <49> (89) 9 29 30 00

Hitachi Asia Ltd.
Hitachi Tower
16 Collyer Quay #20-00,
Singapore 049318
Tel: <65>-538-6533/538-8577
Fax: <65>-538-6933/538-3877
URL: <http://www.hitachi.com.sg>

Hitachi Asia Ltd.
(Taipei Branch Office)
4/F, No. 167, Tun Hwa North Road,
Hung-Kuo Building,
Taipei (105), Taiwan
Tel: <886>-(2)-2718-3666
Fax: <886>-(2)-2718-8180
Telex: 23222 HAS-TP
URL: <http://www.hitachi.com.tw>

Hitachi Asia (Hong Kong) Ltd.
Group III (Electronic Components)
7/F., North Tower,
World Finance Centre,
Harbour City, Canton Road
Tsim Sha Tsui, Kowloon,
Hong Kong
Tel: <852>-(2)-735-9218
Fax: <852>-(2)-730-0281
URL: <http://semiconductor.hitachi.com.hk>

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