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TO: Road Well

Date: 2009/04/16

Customer Acceptance Specification

Model: HSD220MKW1-A00

 	
Accepted by:	
Signature	Date
Proposed by: Technical Service D	Pivision
Signature	Date

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Record of Revisions					
Rev.	Date	Sub-Model	Description of change		
1.0	Apr.,16, 2009	A00	Preliminary Product Specification was first issued.		
1					





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1.0 GENERAL DESCRIPTIONS

1.1 Introduction

HannStar Display model **HSD220MKW1-A** is a color active matrix thin film transistor (TFT) liquid crystal display (LCD) that uses amorphous silicon TFT as a switching device. This model is composed of a TFT LCD panel, the voltage reference, common voltage, DC-DC converter, column, and row driver circuit. This TFT LCD has a 22-inch diagonally measured active display area with WSXGA+ resolution (1050 vertical by 1680 horizontal pixel array).

1.2 Features

- 22" WSXGA+ TN mode TFT LCD panel
- High speed response time
- 4 CCFL Backlight system
- Supported WSXGA+ (V: 1050 lines, H: 1680 pixels) resolution
- Supported 75Hz refresh rate
- With LCD Timing Controller
- RoHS compatible

1.3 General information

General Information				
Item	Sp	Specification		
Outline dimension	493.7×320.1×16.5 (typ.)	mm	
Display area	473.76 (H) x296.16	(V)	mm	
Number of Pixel	1680(H) x 1050(V)		Pixels	
Pixel pitch	0.282(H) x 0.282(V)		mm	
Pixel arrangement	RGB Vertical stripe			
Display color	16.7M (6-bit+HiFRC	()		
Display mode	Normally white			
Surface treatment	Antiglare (3H)			
Weight	3050		G	
Back-light	4-CCFLs, Top & bo	ttom edge side		
Input signal	2-ch LVDS			
Down consumption	Logic system	4.2 @ v-color pattern	W	
Power consumption	B/L system	5.48	W	
		T I		

1.4 Applications

- Desktop and Multi-function monitors
- Display terminals for AV applications
- Monitors for industrial applications





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1.5 Mechanical Information

Item		Min.	Тур.	Max.	Unit
	Horizontal(H)	493.2	493.7	494.2	mm
Module Size	Vertical(V)	319.6	320.1	320.6	mm
	Depth(D)	16.0	16.5	17.0	mm
Weight (without inverter)		2850	3050	3250	g
Torque of custo	mer screw hole			3.0	Kgf*Cm





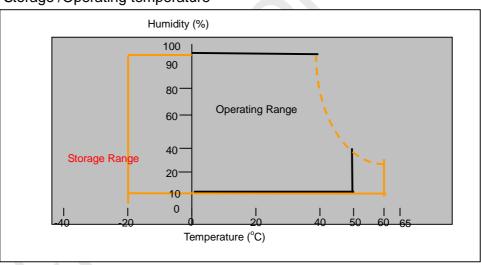
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2.0 ABSOLUTE MAXIMUM RATINGS

2.1 Absolute Rating of Environment

Item	Symbol	Min.	Max.	Unit	Note
Storage temperature	T _{STG}	-20	60	°C	
Operating temperature	T _{OPR}	0	50	°C	(1)
Vibration (non-operating)	V _{NOP}		1.5	G	(2)
Shock (non-operating)	S _{NOP}		70	G	(3)
Storage humidity	H _{STG}	10	90	%RH	(3)
Operating humidity	H _{OP}	10	80	%RH	(4)
Low pressure (operating)	P _{LOP}	697		HPa	(5)
Low pressure (non-operating)	P _{LNOP}	116		HPa	(6)

Note (1) Storage / Operating temperature



- (2) 10-500Hz sine wave, X, Y, Z each directions, 30min/cycle.
- (3) 11ms, ±X, ±Y, ±Z direction, one time each. For this shock test, It is necessary to fill the silicon rubber between the shock jigs as buffer.
- (4) Max wet bulb temp. =39°C
- (5) 2 hrs. (10000 feet)
- (6) 24hrs. (50000 feet)

2.2 Electrical Absolute Rating:

2.2.1 TFT LCD Module:

Item	Symbol	Min.	Max.	<u>Unit.</u>	<u>Note</u>
Power supply Voltage	VDD	<u>-0.3</u>	<u>6.0</u>	V(DC)	(1)(2)

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2.2.2 Back Light Unit:

Item	Symbol	Min.	Max.	Unit	Note
Lamp current	I_{L}	3.0	9.0	mA	(1)(2)(3)
Lamp frequency	f_{L}	40	80	KHz	(1)(2)(3)

Note: (1) Permanent damage may occur to the LCD module if beyond this specification.

Functional operation should be restricted to the conditions described under Normal Operating Conditions.

- (2) To exceed 7.5mA, life time accelerate drop down and if to exceed 9.0 mA has safety problem. If current lower than 3.5 mA, CCFL would be unstable or damaged.
- (3) Within Ta=25±2℃





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3.0 OPTICAL CHARACTERISTICS

3.1 Optical specification

Item		Symbol	Condition	Min.	Тур.	Max.	Unit	Note
Contrast		CR		700	1000			(1)(2)
Response time	Rising	TR			1.5	3	meac	(1)(2)
Response time	Falling	TF			3.5	7	msec	(1)(3)
White luminance (center of screen)		Y_L	⊖=0°	240	300	~ <u>C</u>	cd/m ²	(1)(4)(7) (IL=7.5m A)
	Red	Rx	φ=0°	0.615	0.645	0.675		
	Reu	Ry	Normal viewing	0.310	0.340	0.370		
Color chromaticity (CIE1931)	Gree	Gx	angle	0.270	0.300	0.330		
	n	Gy		0.600	0.630	0.660		(1)(5)
	Blue	Bx		0.110	0.140	0.170		(1)(5)
	Diue	Ву		0.035	0.065	0.095		
	White	Wx		0.280	0.310	0.340		
	VVIIILE	Wy		0.300	0.330	0.360		
	Hor.	θι		80	85			
Viewing angle	1101.	Θ_{R}	CR>10	80	85			
viewing angle	Ver.	Өн) 011210	75	80			
	VOI	Θ_{L}		75	80			
	Hor.	θι		80	85			
Viewing angle	1101.	Θ_{R}	CR>5	80	85			
viewing angle	Ver.	Өн	01020	80	85			
	V 01.	θι		80	85			
Brightness uniformity		B _{UNI}	⊖=0° φ=0°	75			%	(6)

3.2 Measuring Condition

Measuring surrounding: dark room

■ Lamp current I_{BL}: (7.5)±0.1mA, lamp freq. F_L= 50KHz, Inverter: TDK TBD332LR

■ V_{DD1} =5.0V, f_V =60Hz, f_{DCLK} =59.61MHz

■ Surrounding temperature: 25±2°C

■ 30min. Warm-up time.



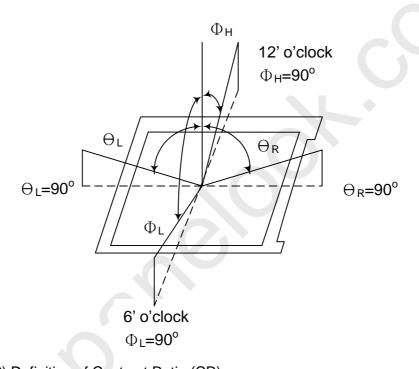


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3.3 Measuring Equipment

- FPM520 of Westar Display technologics, INC., which utilized SR-3 for Chromaticity and BM-5A for other optical characteristics.
- Measuring spot size: 20~21mm

Note (1) Definition of Viewing Angle:



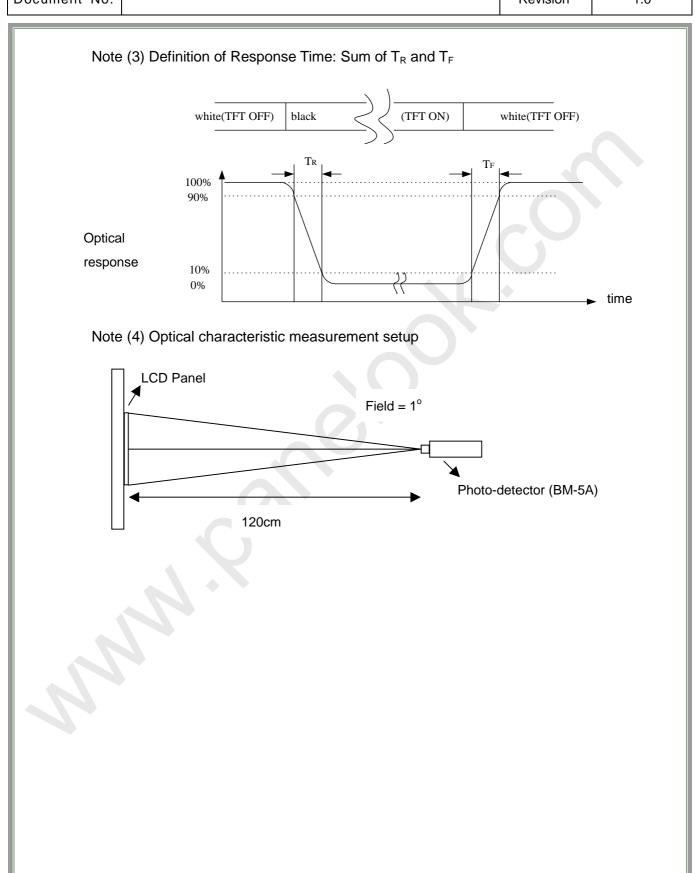
Note (2) Definition of Contrast Ratio (CR): measured at the center point of panel





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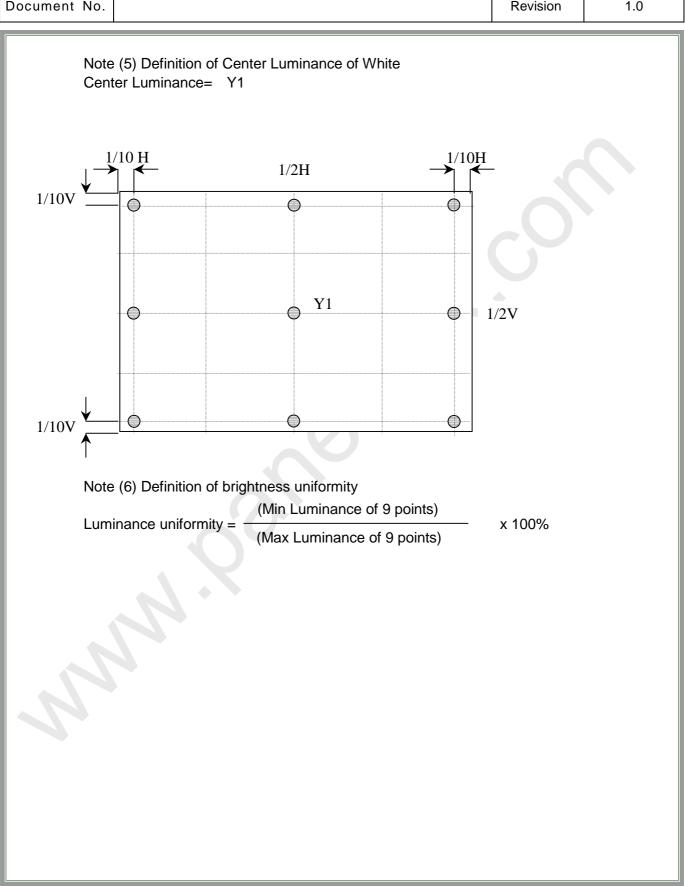
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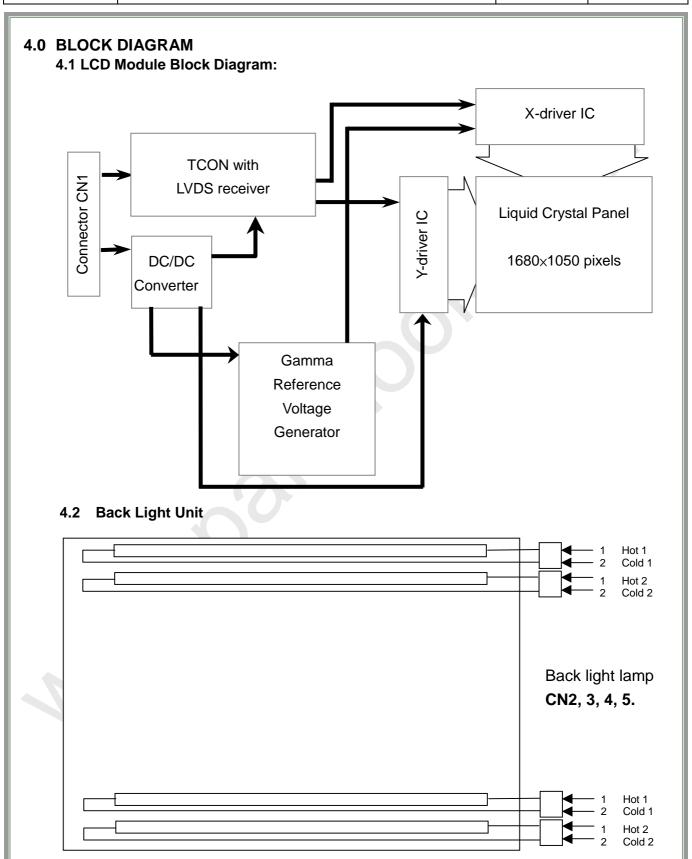
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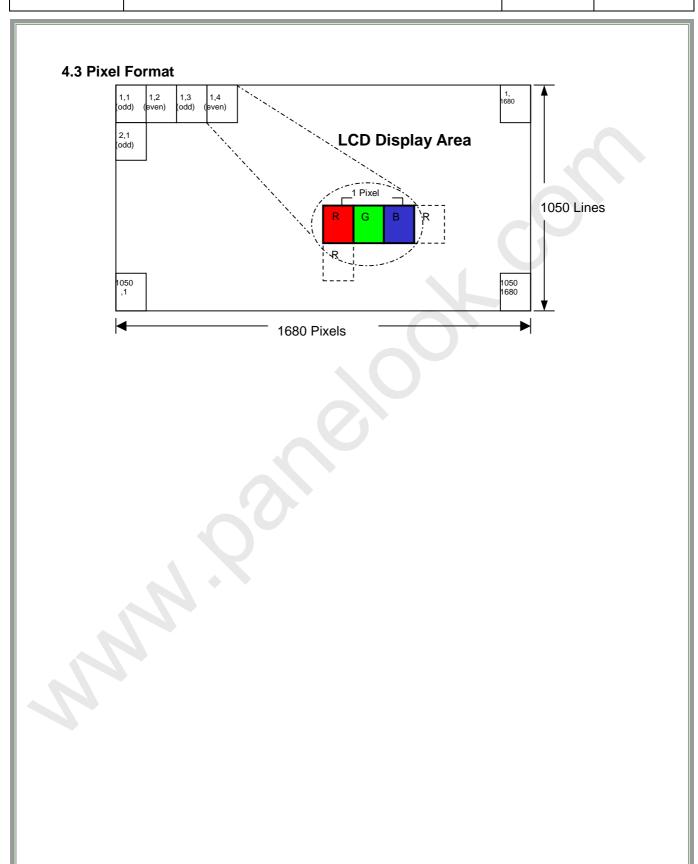


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4.4 Relationship Between Displayed Color and Input

		MSE	3				L	SB	MS	SB			-		L	SB	MS	βB					L	SB	Gray scale
	Display	R7 R	6 R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	В7	В6	В5	В4	ВЗ	В2	В1	В0	Level
	Black	LL	_ L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	-
	Blue	LL	L	L	L	L	L	L	L	L	L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	Н	Н	-
	Green	LL	_ L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	Н	Н	L	L	L	L	L	L	L	L	
Basic	Light Blue	LL	_ L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	- 1
color	Red	НН	1 Н	Н	Н	Н	Н	Н	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	-
	Purple	НЕ	<u> </u>	Н	Н	Н	Н	Н	L	L	L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	Н	Н	2
	Yellow	НН	<u> </u>	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	L	L	L	L	L	L	L	L	-
	White	ΗН	<u> </u>	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	-
	Black	LL	. L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L0
		LL	_ L	L	L	L	L	Н	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L1
	Dark	LL	<u> </u>	L	L	L	Н	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L2
Gray scale	1				:																:				L3…L251
of Red	\downarrow	НН	Н	Н	Н	Н	L	L	┙	L	L	L	L	L	L	L		L	L	L	L	L	L	L	L255
	Light	НН	1 Н	Н	Н	Н	L	Н	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L255
		НН	Н	Н	Н	Н	Н	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L255
	Red	НН	Н	Н	Н	Н	Н	Н	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	Red L255
	Black	LL	_ L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L0
		LL	<u> L</u>	L	L	L	L	L	L	L	L	L	L	L	L	Н	L	L	L	L	L	L	L	L	L1
	Dark	LL	_ L	L	L	L	L	L	L	L	L	L	L	L	Н	L	L	L	L	L	L	L	L	L	L2
Gray scale	1				:		. /														:				L3…L251
of Green	↓	LL	_ L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	L	L	L	L	L	L	L	L	L	L	L255
	Light	LL	. L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	L	Н	L	L	L	L	L	L	L	L	L255
		LL	_ L	L	L	L	L	L	Η	Н	Н	Н	Н	Н	Н	L	L	L	L	L	L	L	L	L	L255
	Green	LL	. L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	Н	Н	L	L	L	L	L	L	L	L	Green L255
	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L0
		LL	_ L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	Н	L1
	Dark	LL	L	_		L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	Н	L	L2
Gray scale	1																				:				L3…L251
of Blue	\downarrow	LL	. L	L	L	L	L	L	L	L	L	L	L	L	L	L	Η	Н	Н	Н	Н	Н	L	L	L255
	Light	LL	- L	L	L	L	L	L	ᆚ	L	L	L	L	L	L	L	${\tt T}$	Н	Н	Н	Н	Н	L	Τ	L255
		LL	. L	L	L	L	L	L	L	L	L	L	L	L	L	L	Η	Н	Н	Н	Н	Н	Н	L	L255
	Blue	LL	. L	L	L	L	L	L	L	L	L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	Н	Н	Blue L255
	Black	LL	. L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L0
		LL	. L	L			L		L				L				L	L	L	L	L		L		L1
	Dark	LL	<u> L</u>	L	L	L	Н	L	L	L	L	L	L	L	Н	L	L	L	L	L	L	L	Н	L	L2
Gray scale of White &	1				:																:				L3…L251
Black	↓	НЬ	1 H	Н	Н	Н	L	L	Н	Н	Н	Н	Н	Н	L	L	Н	Н	Н	Н	Н	Н	L	L	L252
	Light	ΗН	Η	Н	Н	Н	L	Н	Н	Н	Н	Н	Н	Н	L	Н	Н	Н	Н	Н	Н	Н	L	Н	L253
		ΗН	Η	Н	Н	Н	Н	L	Н	Н	Н	Н	Н	Н	Н	L	Н	Н	Н	Н	Н	Н	Н	L	L254
	White	НН	1 Н	H	H	H	H	Н	Н	H	H	H	H	Н	Н	H	Н	Н	Н	H	Н	H	Н	H	White L255





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5.0 I/O CONNECTION PIN ASSIGNMENT

5.1 Interface Connector (30-pins) (HRS: MDF76GW-30S-1H or equivalent)

Pin No.	Signal	Description
1	RinO0-	Receiver Signal (-)
2	RinO0+	Receiver Signal (+)
3	RinO1-	Receiver Signal (-)
4	RinO1+	Receiver Signal (+)
5	RinO2-	Receiver Signal (-)
6	RinO2+	Receiver Signal (+)
7	VSS	Ground
8	RinOC-	Clock Signal (-)
9	RinOC+	Clock Signal (+)
10	RinO3-	Receiver Signal (-)
11	RinO3+	Receiver Signal (+)
12	RinE0-	Receiver Signal (-)
13	RinE0+	Receiver Signal (+)
14	VSS	Receiver Signal (+)
15	RinE1-	Receiver Signal (-)
16	RinE1+	Receiver Signal (+)
17	VSS	Ground
18	RinE2-	Receiver Signal (-)
19	RinE2+	Receiver Signal (+)
20	RinEC-	Clock Signal (-)
21	RinEC+	Clock Signal (+)
22	RinE3-	Receiver Signal (-)
23	RinE3+	Receiver Signal (+)
24	VSS	Ground
25	NC	NC
26	NC	GND
27	NC	GND
28	VDD+5V	Power Supply, 5V (Typical)
29	VDD+5V	Power Supply, 5V (Typical)
30	VDD+5V	Power Supply, 5V (Typical)





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5.2 Back Light Unit (CCFL) Connectors:

CN2, 3, 4, 5: CCFL Power Source (Yeonho 35001H5-02 or equivalent)

Pin No.	Symbol	Color	Function
1	Hot1	Pink	CCFL power supply (High voltage)
2	Cold1	White	Ground





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6.0 ELECTRICAL CHARACTERISTICS

6.1 TFT LCD Module:

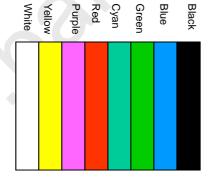
Item	Symbol	Min.	Тур.	Max.	Unit	Note
Voltage of power supply	V_{DD}	4.5	5.0	5.5	V	
	I _{DD0}	595	695	795		(1)
Current of power supply	I _{DD1}	720	870	1020	mA	(1)
	I _{DD2}	960	1160	1360	mA	(1)
Vsync frequency	f _V	56	60	75	Hz	(2)
Hsync frequency	f _H	60.5	64.8	82.4	KHz	
Frequency	f _{DCLK}	55.64	59.61	82	MHz	
Input rush current	I _{RUSH}		1	2	Α	(3)

Note (1)

(a) White: I_{DD0}



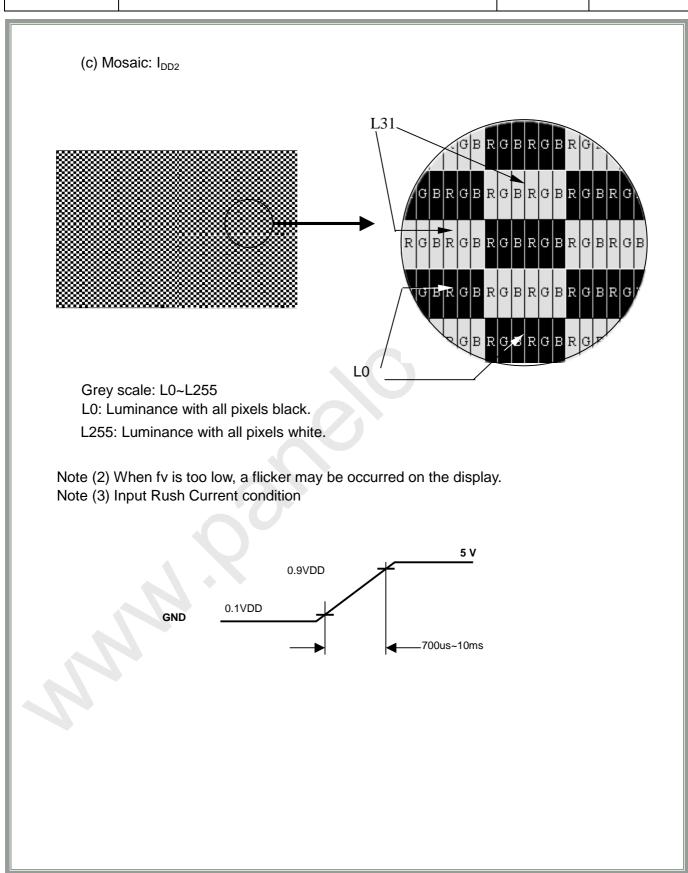
(b) V- Color: I_{DD1}







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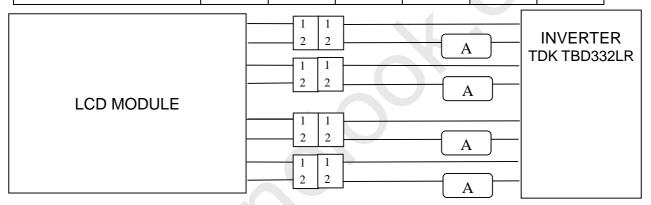


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6.2 Back-Light Unit

The back- light system is an direct-lighting type with 4CCFLs (Cold Cathode Fluorescent Lamp). The characteristics of the lamp are shown in the following tables.

Item	Symbol	Min.	Тур.	Max.	Unit	Note
Lamp current	IL	3.0	7.5	9.0	mA(rms)	(1)
Lamp voltage	VL	-	730	-	V(rms)	I _L =7.5mA
Frequency	fL	40	50	80	KHz	(2)
Operating Lifetime	Hr	40,000			Hour	7.5mA(3)
Startup voltage	Vs	1650			V(rms)	at 25°C
	VS	1850				at 0°C



Note (1)

Lamp current is measured with current meter for high frequency as shown below. Specified values are for a single lamp. To exceed 7.5 mA, life time accelerate drop down and if to exceed 9.0 mA has safety problem. If current lower than 3.5 mA, CCFL would be unstable or damaged.

Note (2)

Lamp frequency may produce interference with horizontal synchronous frequency and this may cause ripple noise on the display. Therefore lamp frequency shall be kept away from the horizontal synchronous frequency and its harmonics as far as possible in order to avoid interference.

Note (3)

Lamp life time (Hr) can be defined as the time in which it continues to operate under the condition: Ta=25±3°C, Typical IL value indicated in the above table and fL=48 kHz until the brightness becomes less than 50%

Note (4)

CCFL inverter should be able to provide a voltage over specified value (Vs) in the above table. Lamp units need at least Vs value shown above to ignition.



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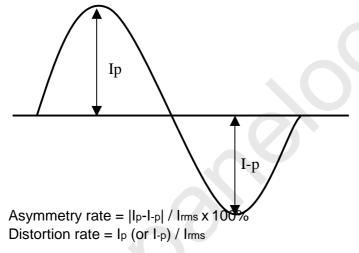
Note (5)

The voltage over specified value (Vs) should be applied to the lamp more than 1 second after startup. Otherwise, the lamp may not be turned on. The used lamp current is the lamp typical current.

Note (6)

The output voltage waveform and current waveform of the inverter must be symmetrical (Unsymmetrical ratio is less than 10%). Please do not use the inverter which has unsymmetrical voltage and current waveform, and spike waveform. The inverter design which can provide the best optical performance, power efficiency, and lamp life should under the following conditions.

- a. The asymmetry rate of the inverter waveform should be less than 10%.
- b. The distortion tae of the waveform should be within $\sqrt{2\pm10\%}$.
- c. The inverter output waveform should be better similar to the ideal sine wave.







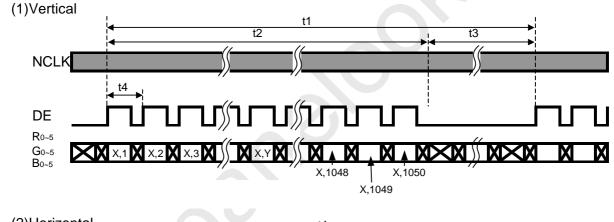
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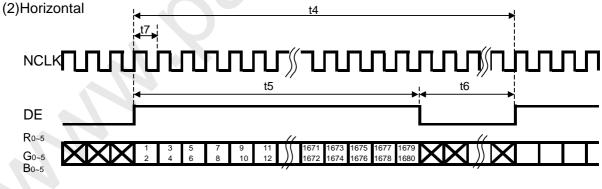
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6.3 Interface Timing (DE mode)

Item	Symbol	Min.	Тур.	Max.	Unit
Frame Rate		48	60	75	Hz
Frame Period	t1	1068	1080	1099	line
Vertical Display Time	t2	1050	1050	1050	line
Vertical Blanking Time	t3	15	30	49	line
1 Line Scanning Time	t4	870	920	1136	clock
Horizontal Display Time	t5	840	840	840	clock
Horizontal Blanking Time	t6	30	80	296	clock
Clock Rate	t7	48	59.61	85	MHz

Timing Diagram of Interface Signal (DE mode)

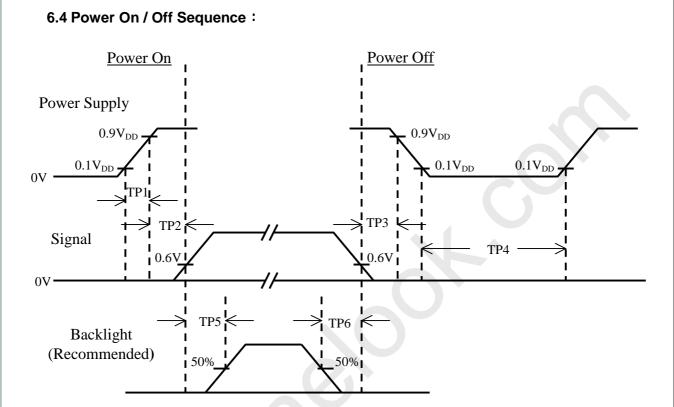








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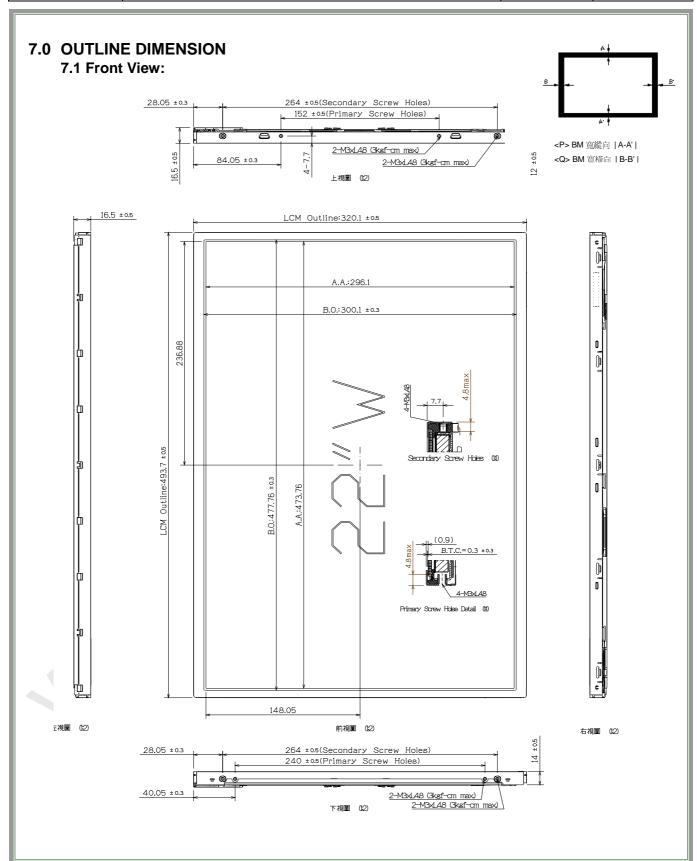
Item	Min.	Тур.	Max.	Unit	Remark
TP1	0.5		10	msec	
TP2	0		50	msec	
TP3	0		50	msec	
TP4	500			msec	
TP5	200			msec	
TP6	200			msec	

Note : (1) The supply voltage of the external system for the module input should be the same as the definition of V_{DD} .

- (2) Apply the lamp volatge within the LCD operation range. When the back-light turns on before the LCD operation or the LCD truns off before the back-light turns off, the display may momentarily become white.
- (3) In case of VDD = off level, please keep the level of input signal on the low or keep a high impedance.
- (4) TP4 should be measured after the module has been fully discharged between power off and on period.
- (5) Interface signal shall not be kept at high impedance when the power is on.



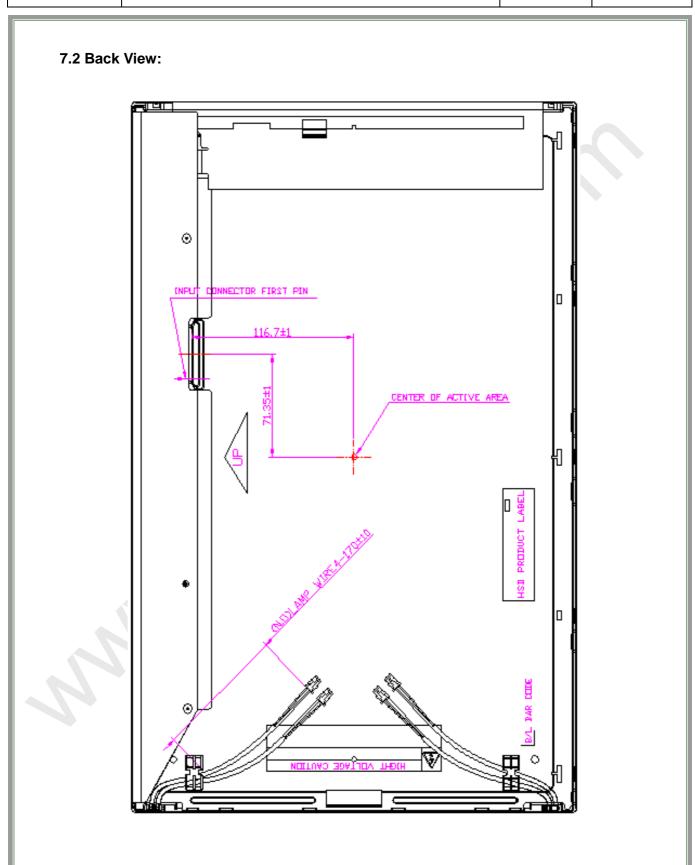
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8.0 LOT MARK

8.1 **Lot Mark**



code 1,2,3,4,5,6: HannStar internal flow control code.

code 7: production location.

code 8: production year. code 9: production month.

code 10,11,12,13,14,15: serial number.

Note (1) Production Year: Code 8 is defined by the last number of the year. For example:

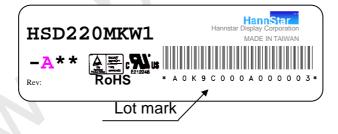
Year	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010
Mark	1	2	3	4	5	6	7	8	9	0

Note (2) Production Month

Month	Jan.	Feb.	Mar.	Apr.	May.	Jun.	Jul.	Aug.	Sep.	Oct	Nov.	Dec.
Mark	1	2	3	4	5	6	7	8	9	Α	В	С

8.2 Location of Lot Mark

- (1) The label is attached to the backside of the LCD module.
- (2) This is subject to change without prior notice.







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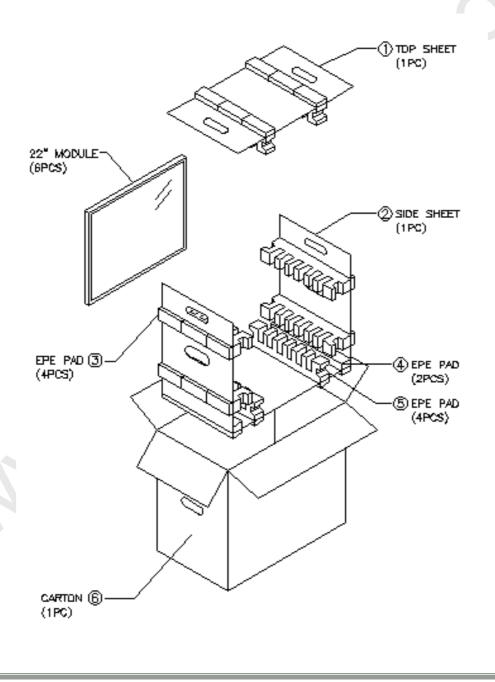
9.0 PACKAGE SPECIFICATION

9.1 Packing form

(1) Package quantity in one carton: 6 pieces

(2) Carton size: 600 x 267 x 440_H (mm)

9.2 Packing assembly drawings







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10.0 GENERAL PRECAUTION

10.1 Use Restriction

This product is not authorized for use in life supporting systems, aircraft navigation control systems, military systems and any other application where performance failure could be life-threatening or otherwise catastrophic.

10.2 Disassembling or Modification

Do not disassemble or modify the module. It may damage sensitive parts inside LCD module, and may cause scratches or dust on the display. HannStar does not warrant the module, if customers disassemble or modify the module.

10.3 Breakage of LCD Panel

- 10.3.1 If LCD panel is broken and liquid crystal spills out, do not ingest or inhale liquid crystal, and do not contact liquid crystal with skin.
- 10.3.2 If liquid crystal contacts mouth or eyes, rinse out with water immediately.
- 10.3.3 If liquid crystal contacts skin or cloths, wash it off immediately with alcohol and rinse thoroughly with water.
- 10.3.4 Handle carefully with chips of glass that may cause injury, when the glass is broken.

10.4 Electric Shock

- 10.4.1 Disconnect power supply before handling LCD module.
- 10.4.2 Do not pull or fold the CCFL cable.
- 10.4.3 Do not touch the parts inside LCD modules and the fluorescent lamp's connector or cables in order to prevent electric shock.

10.5 Absolute Maximum Ratings and Power Protection Circuit

- 10.5.1 Do not exceed the absolute maximum rating values, such as the supply voltage variation, input voltage variation, variation in parts' parameters, environmental temperature, etc., otherwise LCD module may be damaged.
- 10.5.2 Please do not leave LCD module in the environment of high humidity and high temperature for a long time.
- 10.5.3 It's recommended employing protection circuit for power supply.





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10.6 Operation

- 10.6.1 Do not touch, push or rub the polarizer with anything harder than HB pencil lead. Use fingerstalls of soft gloves in order to keep clean display quality, when Persons handle the LCD module for incoming inspection or assembly.
- 10.6.2 When the surface is dusty, please wipe gently with absorbent cotton or other soft material.
- 10.6.3 Wipe off saliva or water drops as soon as possible. If saliva or water drops contact with polarizer for a long time, they may causes deformation or color
- 10.6.4 When cleaning the adhesives, please use absorbent cotton wetted with a little petroleum benzine or other adequate solvent.

10.7 Mechanism

Please mount LCD module by using mounting holes arranged in four corners tightly.

10.8 **Static Electricity**

- 10.8.1 Protection film must remove very slowly from the surface of LCD module to prevent from electrostatic occurrence.
- 10.8.2 Because LCD module uses CMOS-IC on circuit board and TFT-LCD panel, it is very weak to electrostatic discharge. Please be careful with electrostatic discharge.
- 10.8.3 Persons who handle the module should be grounded through adequate methods.

Strong Light Exposure 10.9

The module shall not be exposed under strong light such as direct sunlight. Otherwise, display characteristics may be changed.

10.10 Disposal

When disposing LCD module, obey the local environmental regulations.