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# Model : HSD220KW1 - A00

Note: 1. Please contact HannStar Display Corp. before designing your product based on this module specification.

2. The information contained herein is presented merely to indicate the characteristics and performance of our products. No responsibility is assumed by HannStar for any intellectual property claims or other problems that may result from application based on the module described herein.

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Record of Revisions				
Rev. 1.0	Updated No. A00		Description of change HSD220MKW1-A product information was first issued.	

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### **1.0 GENERAL DESCRIPTIONS**

#### 1.1 Introduction

HannStar Display model HSD220MKW1-A is a color active matrix thin film transistor (TFT) liquid crystal display (LCD) that uses amorphous silicon TFT as a switching device. This model is composed of a TFT LCD panel, the voltage reference, common voltage, DC-DC converter, column, and row driver circuit. This TFT LCD has a 22-inch diagonally measured active display area with WSXGA+ resolution (1050 vertical by 1680 horizontal pixel array).

#### 1.2 Features

- 22" WSXGA+ TN(Twisted Nematic) mode TFT LCD panel
- High speed response time
- 4 CCFLs Backlight system
- Supported WSXGA+ (V:1050 lines, H:1680 pixels) resolution
- Supported 75Hz refresh rate
- With LCD Timing Controller
- RoHS compatible

#### 1.3 General information

General mormation				
Item		Specification		
Outline dimension	493.7×320.1×16.	5 (typ.)	mm	
Display area	473.76 (H) x296.	16 (V)	mm	
Number of Pixel	1680(H) x 1050(\	/)	Pixels	
Pixel pitch	0.282(H) x 0.282	(V)	mm	
Pixel arrangement	RGB Vertical strip	be		
Display color	16.7M (6-bit+HiF	16.7M (6-bit+HiFRC)		
Display mode	Normally white	Normally white		
Surface treatment	Antiglare, Hard-C	Coating (3H)		
Weight	(2600)	(2600)		
Back-light	4-CCFLs, Top & I	bottom edge side		
Input signal	2-ch LVDS			
	Logic system	TBD	W	
Power consumption	B/L system	TBD	W	

#### 1.4 Applications

- Desktop and Multi-function monitors
- Display terminals for AV applications
- Monitors for industrial applications

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#### 1.5 Mechanical Information

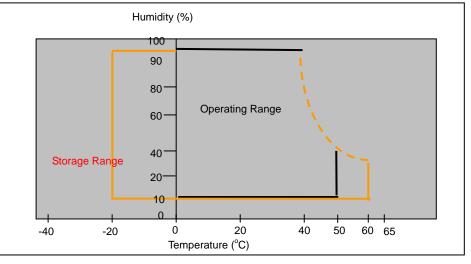
Item		Min.	Тур.	Max.	Unit
	Horizontal(H)		493.7		mm
Module Size	Vertical(V)		320.1		mm
	Depth(D)		16.5		mm
Weight (without inverter)			(2600)		g
Torque of customer screw hole				3.0	Kgf*Cm

### 2.0 ABSOLUTE MAXIMUM RATINGS

#### 2.1 Absolute Rating of Environment

Item	Symbol	Min.	Max.	Unit	Note
Storage temperature	T <sub>STG</sub>	-20	60	°C	
Operating temperature	T <sub>OPR</sub>	0	50	°C	(1)
Vibration(non-operating)	V <sub>NOP</sub>		1.04	G	(2)
Shock(non-operating)	S <sub>NOP</sub>		50	G	(3)
Storage humidity	H <sub>STG</sub>	10	90	%RH	(3)
Operating humidity	H <sub>OP</sub>	10	80	%RH	(4)
Low pressure(operating)	P <sub>LOP</sub>	697		HPa	(5)
Low pressure(non-operating)	PLNOP	116		HPa	(6)

#### Note (1)Storage /Operating temperature



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- (2) 10-500Hz sine wave, X,Y,Z each directions, 30min/cycle.
- (3) 11ms,  $\pm X$ ,  $\pm Y$ ,  $\pm Z$  direction, one time each. For this shock test, It is necessary to fill the silicon rubber between the shock jigs as buffer.
- (4) Max wet bulb temp. =39°C
- (5) 2 hrs. (10000 feet)
- (6) 24hrs. (50000 feet)

### 2.2 Electrical Absolute Rating:

#### 2.2.1 TFT LCD Module:

Item	Symbol	Min.	Max.	Unit.	Note
Power supply Voltage	VDD	-0.3	6.0	V(DC)	(1)(2)

#### 2.2.2 Back Light Unit:

ltem	Symbol	Min.	Max.	Unit	Note
Lamp current	ΙL	3.0	9.0	mA	(1)(2)(3)
Lamp frequency	f <sub>L</sub>	40	80	KHz	(1)(2)(3)

Note: (1) Permanent damage may occur to the LCD module if beyond this specification. Functional operation should be restricted to the conditions described under Normal Operating Conditions.

(2) To exceed 7.5mA, life time accelerate drop down and if to exceed 9.0 mA has safety problem. If current lower than 3.5 mA, CCFL would be unstable or damaged.

(3) Within Ta=25±2°C



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.0 OP 3.1	TICAL CHARA Optical specifi		STICS						
	Item		Symbol	Condition	Min.	Тур.	Max.	Unit	Note
	Contrast		CR			1000			(1)(2)
	Response time	Rising	TR			5			(1)(2)
	White luminance (center of screen)	Falling	TF			(Tr+Tf)		msec	(1)(3)
			YL	⊖=0°		300		cd/m <sup>2</sup>	(1)(4)(7) (IL=7.5mA)
		Red	Rx	φ=0°		TBD			
		Reu	Ry	Normal		TBD			
		Gree	Gx	viewing angle		TBD		]	
	Color chromaticity (CIE1931)	n	Gy	angle		TBD		]	(1)(5)
		Blue	Bx			TBD		]	(1)(3)
		Diue	Ву			TBD			
		White	Wx			0.310			
		VVIIICE	Wy			0.330			
		Hor.	θL			80			
	Viewing angle	1101.	$\Theta_{R}$	CR>10		80			
		Ver.	θн	011/210		80			
			θL			80			
		Hor.	θL			(88)			
	Viewing angle	1101.	θR	CR>5		(88)			
		Ver.	θн	05/0		(88)		]	
		vci.	θL			(88)			
	Brightness unifo	rmity	B <sub>UNI</sub>	$\Theta = 0^{\circ}$ $\phi = 0^{\circ}$	75			%	(6)

#### 3.2 Measuring Condition

- Measuring surrounding: dark room
- Lamp current I<sub>BL</sub>: (7.5)±0.1mA, lamp freq. F<sub>L</sub>= 50KHz, Inverter: TDK TBD332LR
- V<sub>DD1</sub>=5.0V, f<sub>V</sub>=60Hz, f<sub>DCLK</sub>=59.61MHz
- Surrounding temperature : 25±2°C
- 30min. Warm-up time.

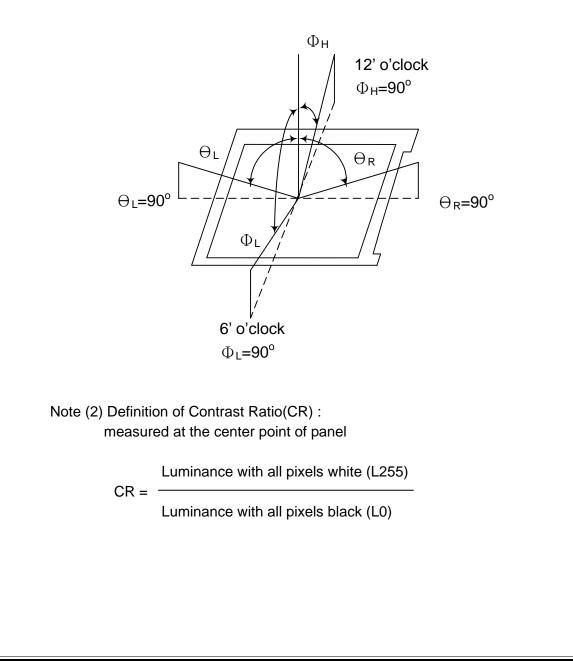
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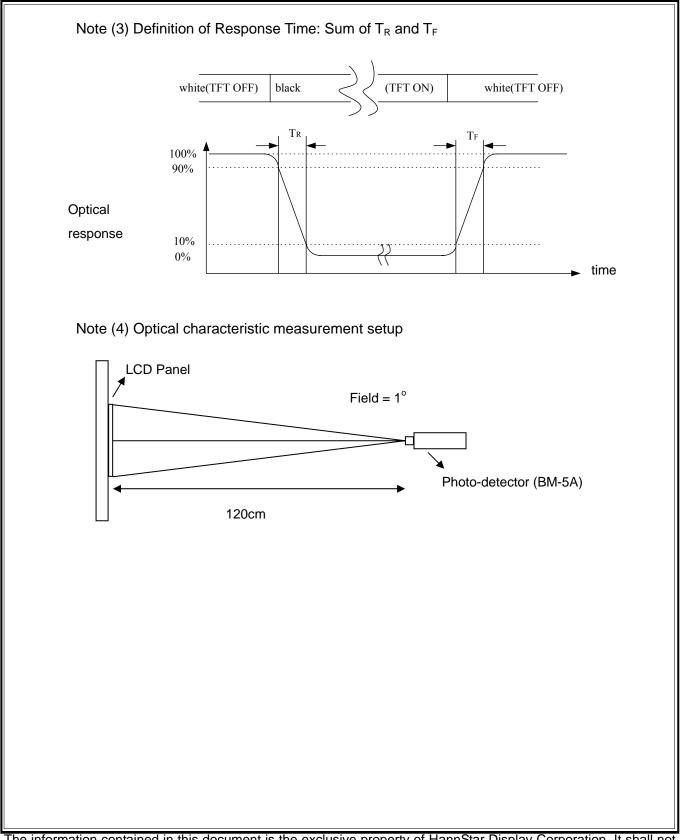
#### 3.3 Measuring Equipment

- FPM520 of Westar Display technologics, INC., which utilized SR-3 for Chromaticity and BM-5A for other optical characteristics.
- Measuring spot size : 20~21mm

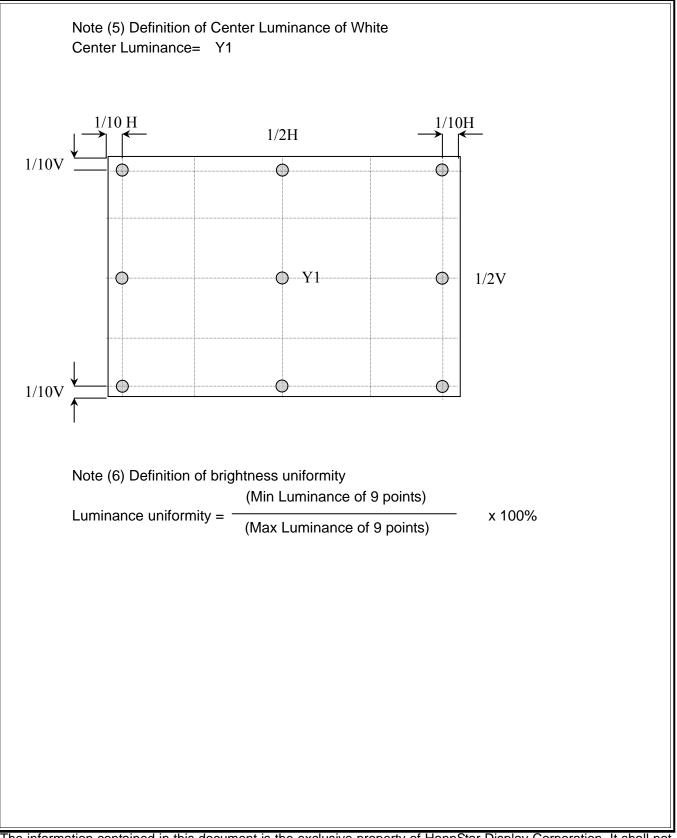
Note (1) Definition of Viewing Angle:



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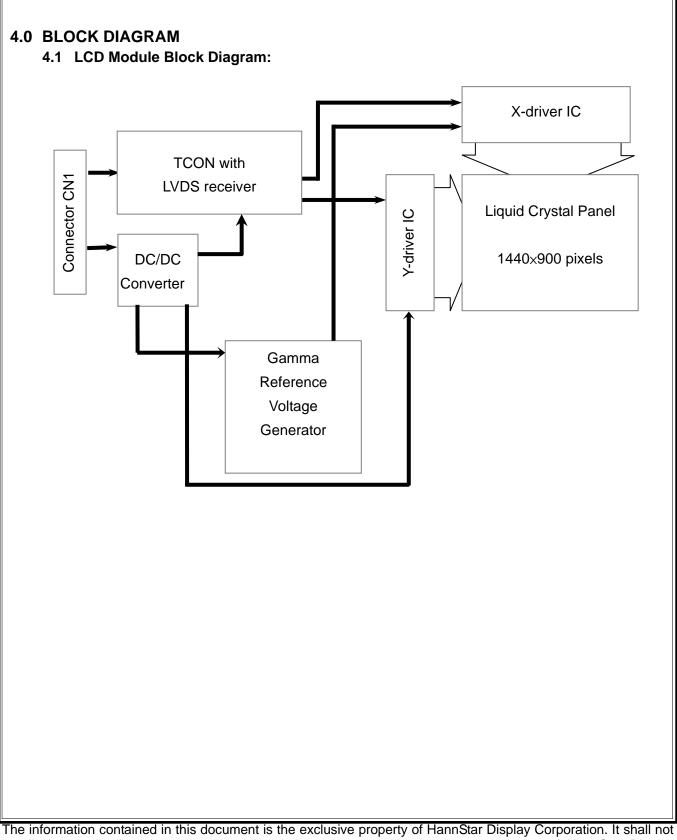
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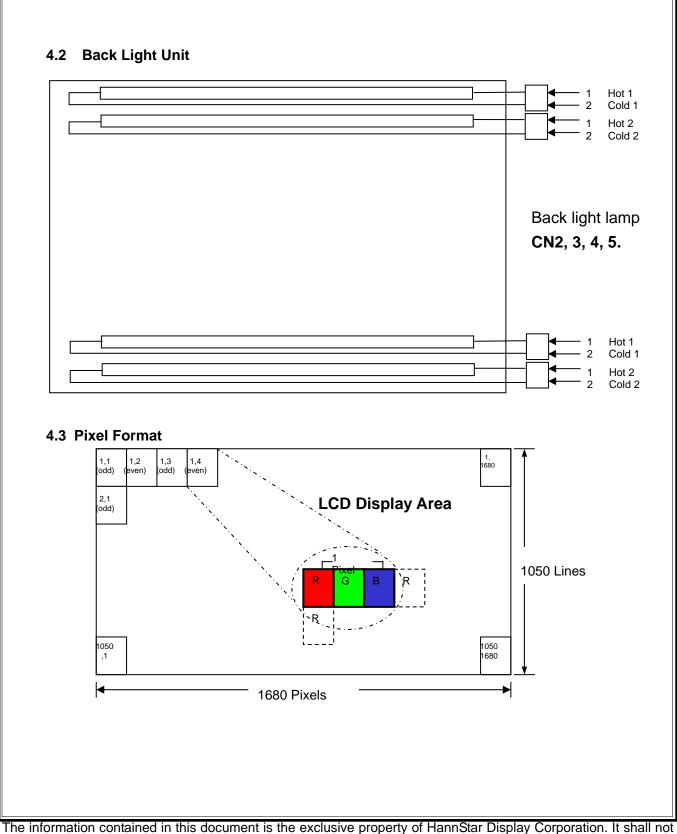
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		MS	SB					L	SB	MS	SΒ					L	SB	MS	SB					L	SB	Gray scale
	Display	R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	В5	B4	В3	B2	B1	В0	Level
	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	-
	Blue	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	Н	Н	-
	Green	L	L	L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	Н	Н	L	L	L	L	L	L	L	L	-
Basic	Light Blue	L	L	L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	-
color	Red	Н	Η	Η	Η	Η	Η	Н	Η	∟	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	-
	Purple	Н	Η	Η	Η	Η	Η	Η	Η	L	L	L	L	L	L	L	L	Н	Η	Η	Η	Η	Η	Н	Н	-
	Yellow	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	L	L	L	L	L	L	L	L	-
	White	Н	Η	Η	Η	Η	Η	Η	Η	Н	Η	Η	Н	Η	Η	Η	Н	Н	Η	Н	Η	Η	Η	Н	Н	-
	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	LO
		L	L	L	L	L	L	L	Н	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L1
	Dark	L	L	L	L	L	L	Н	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L2
Gray scale	↑		_	_			_	_	_		_	_			_	_	_		_	_	_	:	_	_		L3…L251
of Red	$\downarrow$	Н	Н	Н	Н	Н	Н	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L255
	Light	Н	Н	Н	Н	Н	Н	L	Н	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L255
	-	Н	Н	Н	Н	Н	Н	Н	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L255
	Red	Н	Н	Н	Н	Н	Н	Н	Н	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	Red L255
	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L0
		L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	Н	L	L	L	L	L	L	L	L	L1
	Dark	L	L	L	L	L	L	L	L	L	L	L	L	L	L	Н	L	L	L	L	L	L	L	L	L	L2
Gray scale	1					:								:								:				L3…L251
of Green	$\downarrow$	L	L	L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	L	L	L	L	L	L	L	L	L	L	L255
	Light	L	L	L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	L	Н	L	L	L	L	L	L	L	L	L255
	Ū	L	L	L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	Н	L	L	L	L	L	L	L	L	L	L255
	Green	L	L	L	L	L	L	L	L	Н						Н		L	L	L	L	L	L	L	L	Green L25
	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L0
		L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	Н	L1
	Dark	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	Н	L	L2
Gray scale	1					:								:								:				L3…L251
of Blue	Ļ	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	н	Н	Н	Н	Н	Н	L	L	L255
	Light	L	L	L	L	L	L	L	L		L						L	Н								L255
	g				L													Н								L255
	Blue	L			L				L	L						L									H	Blue L255
	Black	L			L				L		L	_		_	_	L	_				L				L	LO
		L	L	L	L	L	L	L		L	L	L	L			L		L			L	L	L		H	 L1
	Dark	L	L	Ē	L	L	L	H	L	L	L	L	L	L		H		L	L	L	L	L	L		L	L2
Gray scale		F	-	-	-	-	-		-		-	-	-	:	-		-	<u> </u>	-	_	_	:	_		-	 L3…L251
of White &		н	н	н			н	L	1	н	н	н			н	1	1	н	н	н	н	н	н	I	1	L252
Black	Light							L				H				L									H	L252
	Light							H								Н					H					L253
	White																									White L254



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### 5.0 I/O CONNECTION PIN ASSIGNMENT

#### 5.1 Interface Connector (30-pins) (HRS: MDF76GW-30S-1H or equivalent)

Pin No.	Signal	Description
1	RinO0-	Receiver Signal (-)
2	RinO0+	Receiver Signal (+)
3	RinO1-	Receiver Signal (-)
4	RinO1+	Receiver Signal (+)
5	RinO2-	Receiver Signal (-)
6	RinO2+	Receiver Signal (+)
7	VSS	Ground
8	RinOC-	Clock Signal (-)
9	RinOC+	Clock Signal (+)
10	RinO3-	Receiver Signal (-)
11	RinO3+	Receiver Signal (+)
12	RinE0-	Receiver Signal (-)
13	RinE0+	Receiver Signal (+)
14	VSS	Receiver Signal (+)
15	RinE1-	Receiver Signal (-)
16	RinE1+	Receiver Signal (+)
17	VSS	Ground
18	RinE2-	Receiver Signal (-)
19	RinE2+	Receiver Signal (+)
20	RinEC-	Clock Signal (-)
21	RinEC+	Clock Signal (+)
22	RinE3-	Receiver Signal (-)
23	RinE3+	Receiver Signal (+)
24	VSS	Ground
25	NC	NC
26	NC	GND
27	NC	GND
28	VDD+5V	Power Supply, 5V (Typical)
29	VDD+5V	Power Supply, 5V (Typical)
30	VDD+5V	Power Supply, 5V (Typical)

#### 5.2 Back Light Unit (CCFL) Connectors:

CN2, 3, 4, 5: CCFL Power Source (Yeonho 35001H5-02 or equivalent)

Pin No.	Symbol	Color	Function
1	Hot1	Pink	CCFL power supply (High voltage)
2	Cold1	White	Ground



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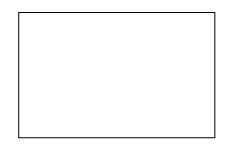
## 6.0 ELECTRICAL CHARACTERISTICS

#### 6.1 TFT LCD Module:

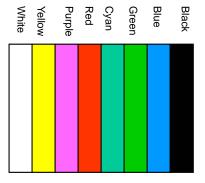
			1	1		
ltem	Symbol	Min.	Тур.	Max.	Unit	Note
Voltage of power supply	V <sub>DD</sub>	4.5	5.0	5.5	V	
Current of power supply	I <sub>DD1</sub>	-	TBD	-	mA	(1)
	I <sub>DD2</sub>	-	TBD	-	mA	(1)
Vsync frequency	f <sub>V</sub>	60	60	75	Hz	(2)
Hsync frequency	f <sub>H</sub>	64.8	64.8	81	KHz	
Frequency	f <sub>DCLK</sub>	59.61	59.61	74.52	MHz	
Input rush current	I <sub>RUSH</sub>			2	А	(3)

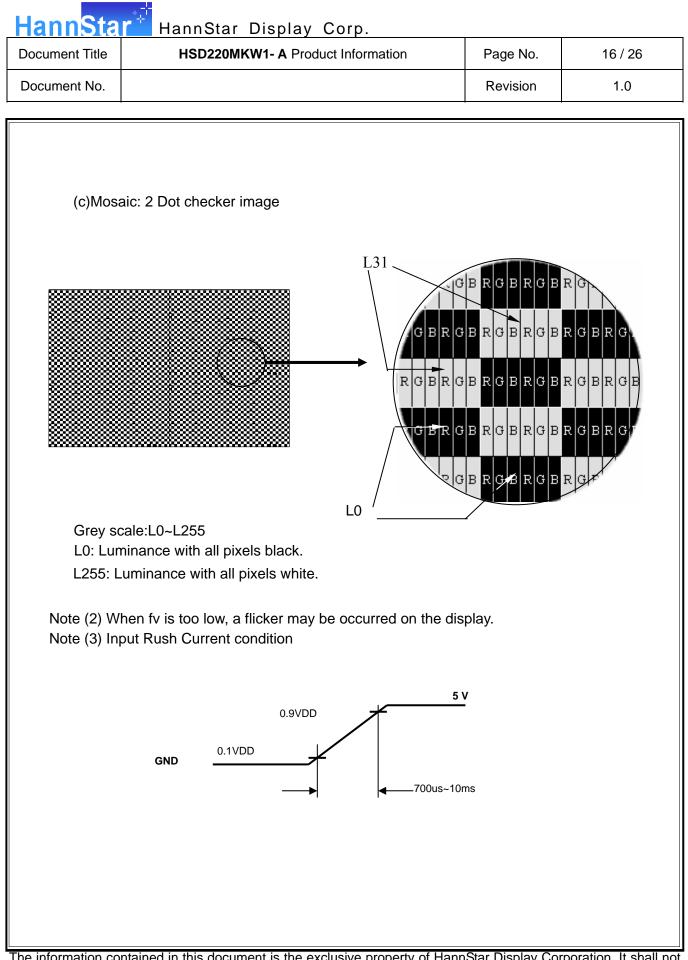
### Note (1)

(a)White:



(b).V-Color :





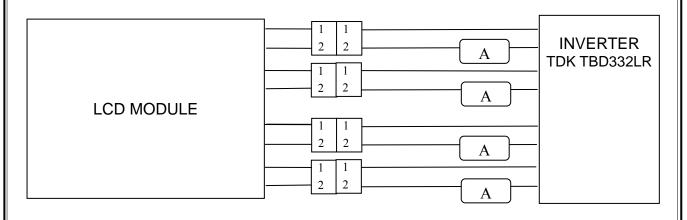
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#### 6.2 Back-Light Unit

The back-light system is an direct-lighting type with 4CCFLs (Cold Cathode Fluorescent Lamp). The characteristics of the lamp are shown in the following tables.

Item	Symbol	Min.	Тур.	Max.	Unit	Note
Lamp current	IL	3.0	7.5	9.0	mA(rms)	(1)
Lamp voltage	VL	-	TBD	-	V(rms)	I <sub>L</sub> =7.5mA
Frequency	fL	40	50	80	KHz	(2)
Operating Lifetime	Hr	50,000			Hour	6.5mA(3)
	Hr	40,000			Hour	7.5mA(3)
Startup voltage	Vs	TBD			V(rms)	at 25°C
Startup voltage	VS	TBD			v (1115)	at 0°C



#### Note (1)

Lamp current is measured with current meter for high frequency as shown below. Specified values are for a single lamp. To exceed 7.5 mA, life time accelerate drop down and if to exceed 9.0 mA has safety problem. If current lower than 3.5 mA, CCFL would be unstable or damaged.

#### Note (2)

Lamp frequency may produce interference with horizontal synchronous frequency and this may cause ripple noise on the display. Therefore lamp frequency shall be kept away from the horizontal synchronous frequency and its harmonics as far as possible in order to avoid interference.

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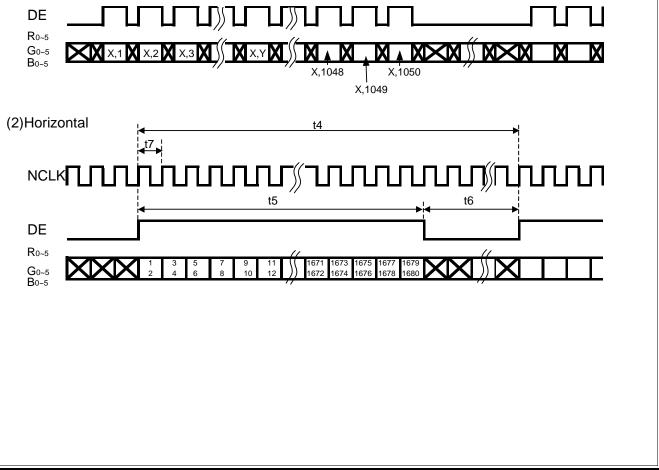
Lamp life time (Hr) can be defined as the time in which it continues to operate under the condition : Ta=25±3°C, Typical IL value indicated in the above table and fL=48 kHz until the brightness becomes less than 50% Note (4) CCFL inverter should be able to provide a voltage over specified value (Vs) in the above table. Lamp units need at least Vs value shown above to ignition. Note (5) The voltage over specified value (Vs) should be applied to the lamp more than 1 second after startup. Otherwise, the lamp may not be turned on. The used lamp current is the lamp typical current. Note (6) The output voltage waveform and current waveform of the inverter must be symmetrical (Unsymmetrical ratio is less than 10%). Please do not use the inverter which has unsymmetrical voltage and current waveform, and spike waveform. The inverter design which can provide the best optical performance, power efficiency, and lamp life should under the following conditions. a. The asymmetry rate of the inverter waveform should be less than 10%. b. The distortion tae of the waveform should be within  $\sqrt{2\pm10\%}$ . c. The inverter output waveform should be better similar to the ideal sine wave. Ip I-p Asymmetry rate =  $|I_p - I_{-p}| / I_{rms} \times 100\%$ Distortion rate =  $I_p$  (or  $I_{-p}$ ) / Irms

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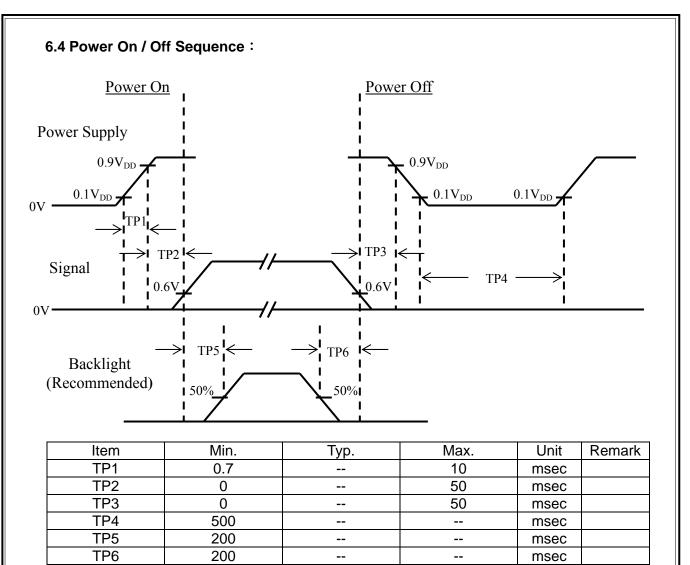
#### 6.3 Interface Timing (DE mode)

Item	Symbol	Min.	Тур.	Max.	Unit
Frame Rate		60	60	75	Hz
Frame Period	t1	-	1080	-	line
Vertical Display Time	t2	-	1050	-	line
Vertical Blanking Time	t3	-	30	-	line
1 Line Scanning Time	t4	-	1840	-	clock
Horizontal Display Time	t5	-	1680	-	clock
Horizontal Blanking Time	t6	-	160	-	clock
Clock Rate	t7	59.61	59.61	74.52	MHz

Timing Diagram of Interface Signal (DE mode)



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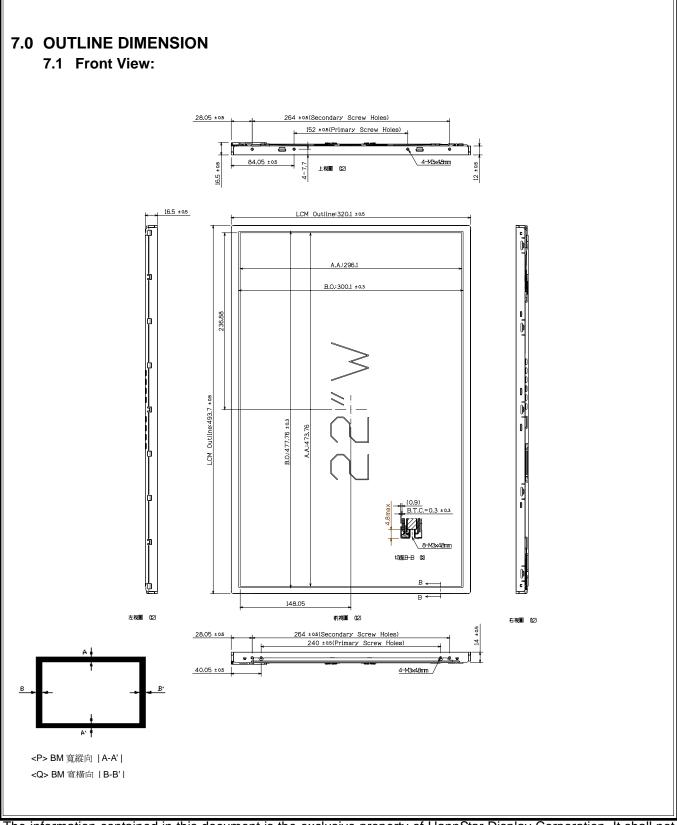
Note : (1) The supply voltage of the external system for the module input should be the same as the definition of  $V_{DD}$ .

- (2) Apply the lamp volatge within the LCD operation range. When the back-light turns on before the LCD operation or the LCD truns off before the back-light turns off, the display may momentarily become white.
- (3) In case of VDD = off level, please keep the level of input signal on the low or keep a high impedance.
- (4) TP4 should be measured after the module has been fully discharged between power off and on period.
- (5) Interface signal shall not be kept at high impedance when the power is on.

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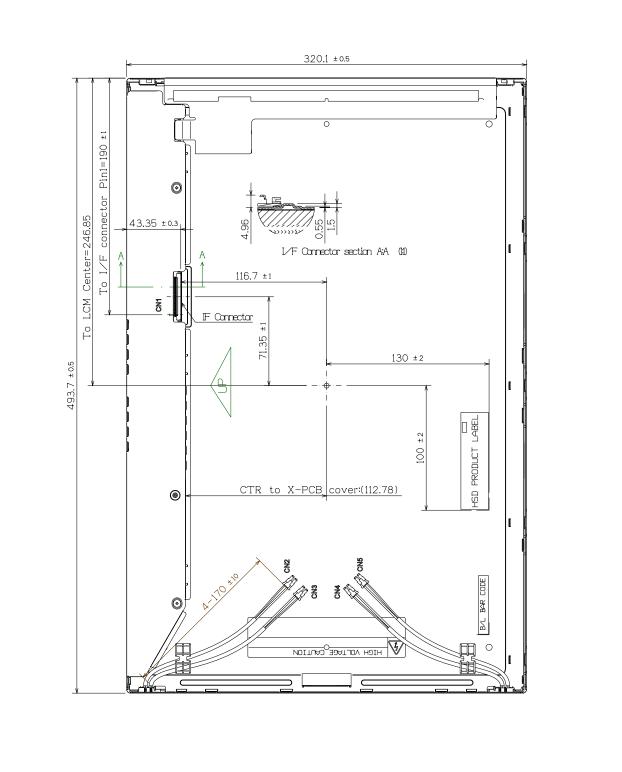


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#### 7.2 Back View:



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### 8.0 LOT MARK

#### 8.1 Lot Mark



code 1,2,3,4,5,6: HannStar internal flow control code.

code 7: production location.

code 8: production year.

code 9: production month.

code 10,11,12,13,14,15: serial number.

Note (1) Production Year

Year	1999	2000	2001	2002	2003	2004	2005	2006	2007	2008
Mark	9	0	1	2	3	4	5	6	7	8

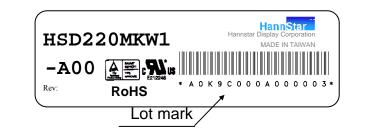
Note (2) Production Month

Month	Jan.	Feb.	Mar.	Apr.	May.	Jun.	Jul.	Aug.	Sep.	Oct	Nov.	Dec.
Mark	1	2	3	4	5	6	7	8	9	А	В	С

#### 8.2 Location of Lot Mark

(1) The label is attached to the backside of the LCD module.

(2) This is subject to change without prior notice.





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### 9.0 PACKAGE SPECIFICATION

#### 9.1 Packing form

- (1) package quantity in one carton: 6 pieces
- (2) carton size: TBD
- (3) for domestic transportation only.

#### 9.2 Packing assembly drawings

TBD

#### 9.3 Pallet transportation specification(TBD)



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#### 10.0 **GENERAL PRECAUTION**

#### 10.1 **Use Restriction**

This product is not authorized for use in life supporting systems, aircraft navigation control systems, military systems and any other application where performance failure could be life threatening or otherwise catastrophic.

#### 10.2 **Disassembling or Modification**

Do not disassemble or modify the module. It may damage sensitive parts inside LCD module, and may cause scratches or dust on the display. HannStar does not warrant the module, if customers disassemble or modify the module.

#### 10.3 Breakage of LCD Panel

- 10.3.1 If LCD panel is broken and liquid crystal spills out, do not ingest or inhale liquid crystal, and do not contact liquid crystal with skin.
- 10.3.2 If liquid crystal contacts mouth or eyes, rinse out with water immediately.
- If liquid crystal contacts skin or cloths, wash it off immediately with alcohol and 10.3.3 rinse thoroughly with water.
- 10.3.4 Handle carefully with chips of glass that may cause injury, when the glass is broken.

#### 10.4 **Electric Shock**

- 10.4.1 Disconnect power supply before handling LCD module.
- 10.4.2 Do not pull or fold the CCFL cable.
- 10.4.3 Do not touch the parts inside LCD modules and the fluorescent lamp's connector or cables in order to prevent electric shock.

#### **Absolute Maximum Ratings and Power Protection Circuit** 10.5

- 10.5.1 Do not exceed the absolute maximum rating values, such as the supply voltage variation, input voltage variation, variation in parts' parameters, environmental temperature, etc., otherwise LCD module may be damaged.
- 10.5.2 Please do not leave LCD module in the environment of high humidity and high temperature for a long time.
- 10.5.3 It's recommended employing protection circuit for power supply.

#### 10.6 Operation

Do not touch, push or rub the polarizer with anything harder than HB pencil 10.6.1 lead.

Use fingerstalls of soft gloves in order to keep clean display quality, when persons Handle the LCD module for incoming inspection or assembly.

- 10.6.2 When the surface is dusty, please wipe gently with absorbent cotton or other soft material.
- 10.6.3 Wipe off saliva or water drops as soon as possible. If saliva or water drops contact with polarizer for a long time, they may causes deformation or color fading.
- 10.6.4 When cleaning the adhesives, please use absorbent cotton wetted with a little petroleum benzene or other adequate solvent.

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#### 10.7 Mechanism

Please mount LCD module by using mounting holes arranged in four corners tightly.

#### 10.8 Static Electricity

- 10.8.1 Protection film must remove very slowly from the surface of LCD module to prevent from electrostatic occurrence.
- 10.8.2 Because LCD module uses CMOS-IC on circuit board and TFT-LCD panel, it is very weak to electrostatic discharge. Please be careful with electrostatic discharge.
- 10.8.3 Persons who handle the module should be grounded through adequate methods.

#### 10.9 Strong Light Exposure

The module shall not be exposed under strong light such as direct sunlight. Otherwise, display characteristics may be changed.

#### 10.10 Disposal

When disposing LCD module, obey the local environmental regulations.