### 30V Nch+Nch Power MOSFET

$V_{DSS}$	30V
D (May)	5.0mΩ
$R_{DS(on)}(Max.)$	5.01112
$I_D$	14A
P <sub>D</sub>	3W

### Features

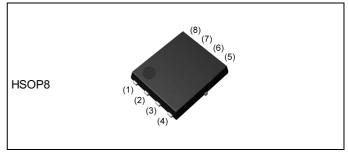
- 1) Low on resistance.
- 2) Pb-free lead plating; RoHS compliant.
- 3) Halogen Free.

## Application

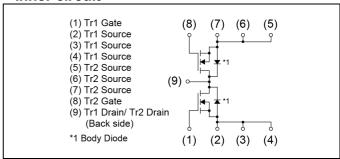
Load Switch

LiB charging and discharging switch

#### Outline



### ●Inner circuit



Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	330
Туре	Tape width (mm)	12
	Quantity (pcs)	2500
	Taping code	ТВ
	Marking	HP8KA1

### ● **Absolute maximum ratings** (T<sub>a</sub> = 25°C ,unless otherwise specified) < Tr1 and Tr2>

Parameter	Symbol	Value	Unit
Drain - Source voltage	$V_{DSS}$	30	V
Continuous drain current	I <sub>D</sub> *1	14	А
Pulsed drain current	I <sub>DP</sub> *2	28	Α
Gate - Source voltage	$V_{GSS}$	±20	V
Power dissipation	P <sub>D</sub> *3	3	W
Junction temperature	T <sub>j</sub>	150	°C
Operating junction and storage temperature range	T <sub>stg</sub>	-55 to +150	°C

### ●Thermal resistance

Parameter	Currele el	Values			l le:4
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - ambient	R <sub>thJA</sub> *3	1	41	1	°C/W

# ullet Electrical characteristics (T<sub>a</sub> = 25°C) <It is the same characteristics for the Tr1 and Tr2>

Parameter	Symbol	Conditions	Values			Unit	
Parameter	Symbol Conditions —		Min.	Тур.	Max.	UTIIL	
Drain - Source breakdown voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 1mA	30	-	-	V	
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_{j}}$	I <sub>D</sub> = 1mA referenced to	-	21	-	mV/°C	
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> = 24V, V <sub>GS</sub> = 0V	-	-	1	μΑ	
Gate - Source leakage current	I <sub>GSS</sub>	$V_{GS}$ = ±20V, $V_{DS}$ = 0V	1	-	±100	nA	
Gate threshold voltage	$V_{GS(th)}$	V <sub>DS</sub> = 10V, I <sub>D</sub> = 10mA	1.0	-	2.5	V	
Gate threshold voltage temperature coefficient	$\frac{\DeltaV_{GS(th)}}{\DeltaT_j}$	I <sub>D</sub> = 1mA referenced to	-	-3	-	mV/°C	
Static drain - source	R <sub>DS(on)</sub> *4	V <sub>GS</sub> = 10V, I <sub>D</sub> = 14A	1	3.5	5.0	mO.	
on - state resistance	NDS(on)	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 14A	-	5.0	7.0	mΩ	
Forward Transfer Admittance	Y <sub>fs</sub>  *4	V <sub>DS</sub> = 5V, I <sub>D</sub> = 14A	14	-	-	S	

<sup>\*1</sup> Limited only by maximum temperature allowed.

<sup>\*2</sup> Pw ≤ 10µs, Duty cycle ≤ 1%

<sup>\*3</sup> Mounted on 40mm×40mm Cu BOARD

<sup>\*4</sup> Pulsed

# ●Electrical characteristics (T<sub>a</sub> = 25°C) <Tr1 and Tr2>

Davamatav	Cymah al	Conditions	Values			Unit	
Parameter	Symbol	ymbol Conditions -		Тур.	Max.	Offic	
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	2550	-		
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 15V	-	330	-	pF	
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	1	270	-		
Turn - on delay time	t <sub>d(on)</sub> *4	V <sub>DD</sub> ≈ 15V,V <sub>GS</sub> = 10V	1	25	-		
Rise time	t <sub>r</sub> *4	I <sub>D</sub> = 7A	-	30	-	no	
Turn - off delay time	t <sub>d(off)</sub> *4	$R_L \simeq 2.1\Omega$	-	85	-	ns	
Fall time	t <sub>f</sub> *4	$R_G = 10\Omega$	-	40	-		

# ● Gate charge characteristics (T<sub>a</sub> = 25°C) < Tr1 and Tr2>

Danamatan	Ols al	O	Values			l lait
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Total gate charge	$Q_g^{*4}$		-	24	-	
Gate - Source charge	Q <sub>gs</sub> *4	$V_{DD} \approx 15V, I_{D} = 14A$ $V_{GS} = 4.5V$	-	7.5	-	nC
Gate - Drain charge	Q <sub>gd</sub> *4	1.00	-	9.0	-	

## ● Body diode electrical characteristics (Source-Drain) (T<sub>a</sub> = 25°C)

### <Tr1 and Tr2>

Parameter	Symbol	Conditions	Values			Unit	
raianetei	Symbol	Conditions	Min.	Тур.	Max.	Offit	
Continuous forward current	I <sub>S</sub> *1	T - 25°C	-	-	2.5	Α	
Pulse forward current	l <sub>SP</sub> *2	T <sub>a</sub> = 25℃	-	-	28	Α	
Forward voltage	V <sub>SD</sub> *4	$V_{GS} = 0V, I_{S} = 2.5A$	-	-	1.2	V	

### • Electrical characteristics curves < Tr1 and Tr2>

Fig.1 Power Dissipation Derating Curve

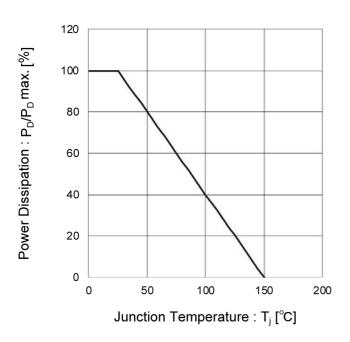
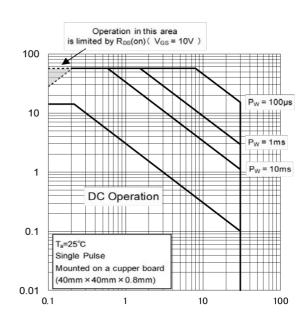


Fig.2 Maximum Safe Operating Area



Drain Current : I<sub>D</sub> [A]

Drain - Source Voltage: V<sub>DS</sub>[V]

Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

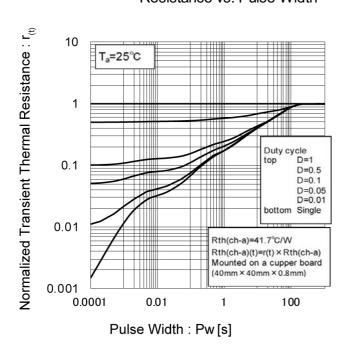
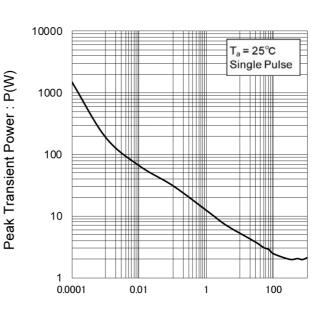


Fig.4 Single Pulse Maximum Power dissipation



Pulse Width: Pw[s]

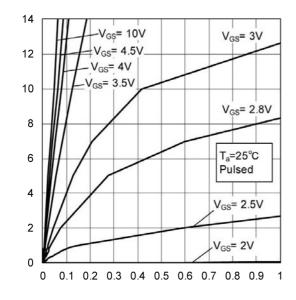
Drain Current : I<sub>D</sub> [A]

Drain-Source Breakdown Voltage: V<sub>(BR)DSS</sub> [V]

### • Electrical characteristics curves < It is the same characteristics for the Tr1 and Tr2>

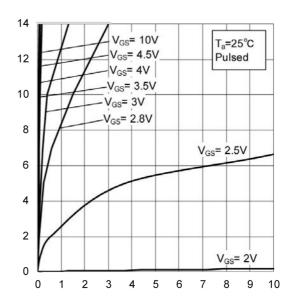
Drain Current : I<sub>D</sub> [A]

Fig.5 Typical Output Characteristics(I)



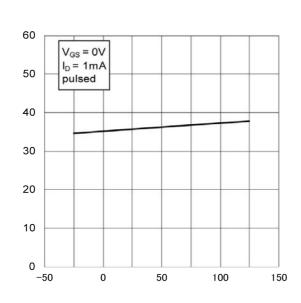
Drain - Source Voltage : V<sub>DS</sub> [V]

Fig.6 Typical Output Characteristics(II)



Drain - Source Voltage : V<sub>DS</sub> [V]

Fig.7 Breakdown Voltage vs. Junction Temperature



Junction Temperature : T<sub>i</sub> [°C]

Gate Threshold Voltage : V<sub>GS(th)</sub> [V]

Fig.8 Typical Transfer Characteristics

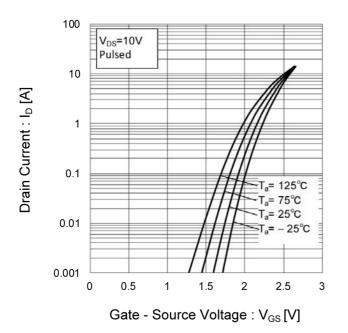
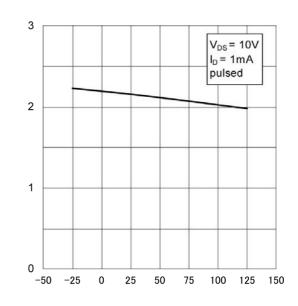


Fig.9 Gate Threshold Voltage vs. Junction Temperature



Junction Temperature : T<sub>j</sub> [°C]

Fig.10 Tranceconductance vs. Drain Current

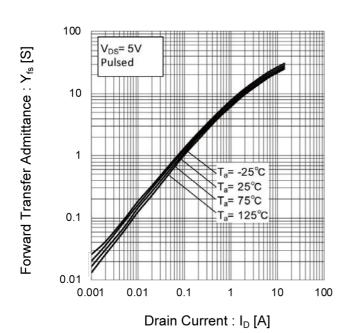


Fig.11 Drain Current Derating Curve

120
100
100
(%) 80
60
20
20
0 25 50 75 100 125 150

Junction Temperature : T<sub>j</sub> [°C]

Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

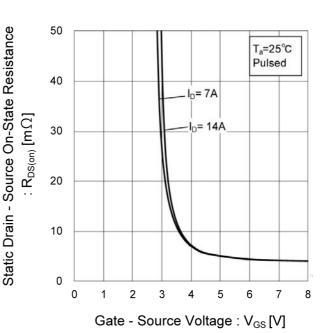
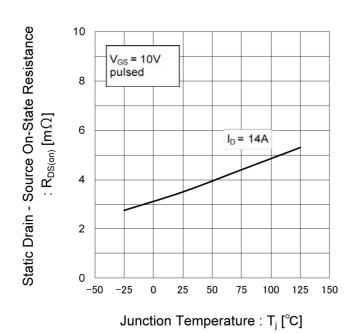


Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature



Static Drain - Source On-State Resistance

Fig.14 Static Drain - Source On - State Resistance vs. Drain Current(I)

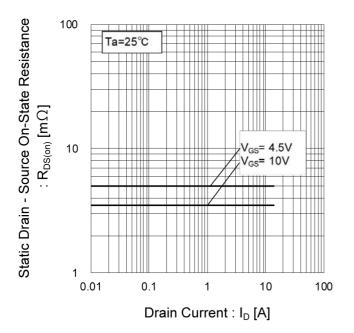


Fig.15 Static Drain - Source On - State Resistance vs. Drain Current(II)

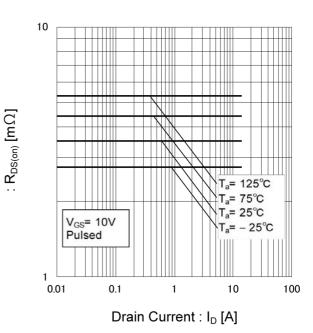


Fig.16 Static Drain - Source On - State Resistance vs. Drain Current(III)

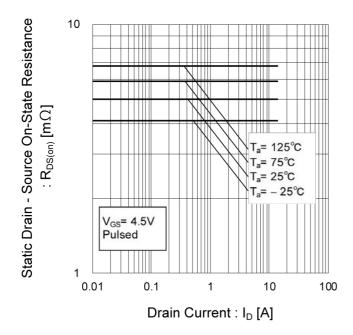


Fig.17 Typical Capacitance vs. Drain - Source Voltage

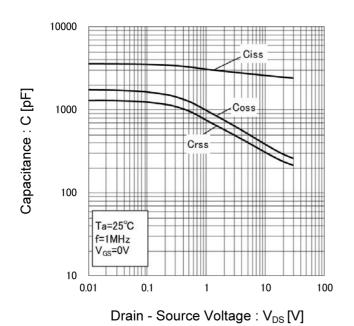


Fig.18 Switching Characteristics

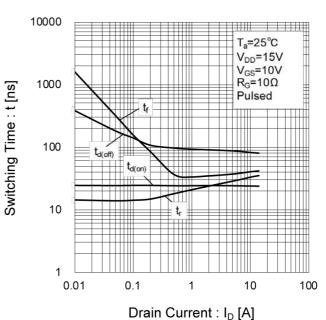


Fig.19 Dynamic Input Characteristics

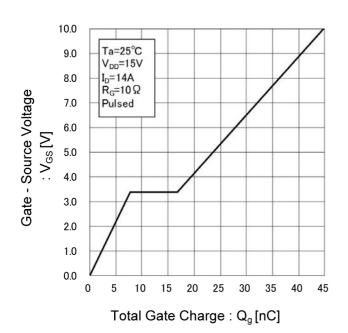
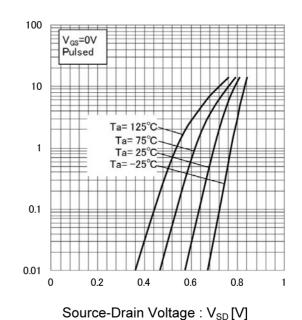


Fig.20 Source Current vs. Source Drain Voltage



Source Current : Is [A]

### Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

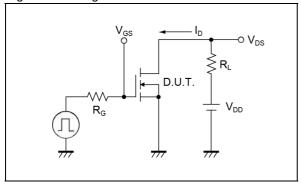


Fig.2-1 Gate Charge Measurement Circuit

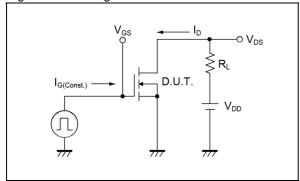


Fig.1-2 Switching Waveforms

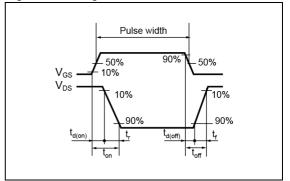
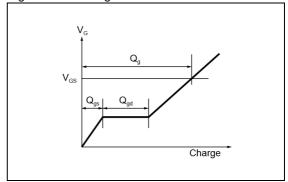
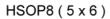
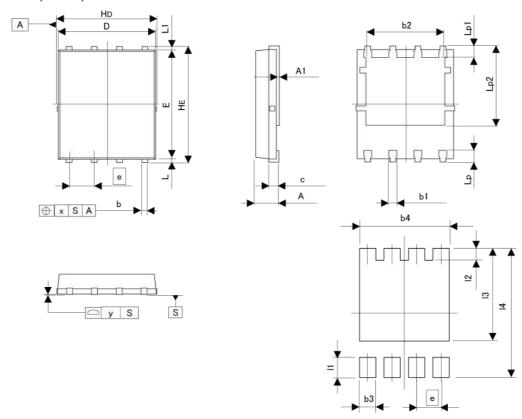


Fig.2-2 Gate Charge Waveform



### Dimensions





Pattern of terminal position areas [Not a pattern of soldering pads]

DIM	MILIME	TERS	INC	HES
DIIVI	MIN	MAX	MIN	MAX
Α	0.90	1.10	0.035	0.043
A1	0.00	0.05	0.000	0.002
b	0.24	0.42	0.009	0.017
b1	0.29	0.49	0.011	0.019
b2	3.81	4.21	0.150	0.166
С	0.20	0.30	0.008	0.012
D	4.80	5.00	0.189	0.197
E	5.60	5.80	0.220	0.228
е	1.	27	0.0	)50
HD	4.90	5.10	0.193	0.201
HE	5.90	6.10	0.232	0.240
L	0.07	0.25	0.003	0.010
L1	0.07	0.25	0.003	0.010
Lp	0.50	0.70	0.020	0.028
Lp1	0.52	0.72	0.020	0.028
Lp2	3.92	4.32	0.154	0.170
х	- 0	0.10		0.004
У	-	0.10	-	0.004

DIM	MILIME	MILIMETERS		HES
DIW	MIN	MAX	MIN	MAX
b3	15	0.59	P(#)	0.023
b4	¥	4.21	(4)	0,166
11	2	0.80	(12)	0.031
12		0.82	-	0.032
13	*	4.32	0+1	0.170
14	2	6.10	7.0	0.240

Dimension in mm/inches



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JÁPAN	USA	EU	CHINA
CLASSⅢ	CL ACCIII	CLASS II b	CL ACCIII
CLASSIV	CLASSⅢ	CLASSⅢ	CLASSⅢ

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  - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
  - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
  - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

#### Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

#### **Precaution for Storage / Transportation**

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
  - [a] the Products are exposed to sea winds or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
  may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
  exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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