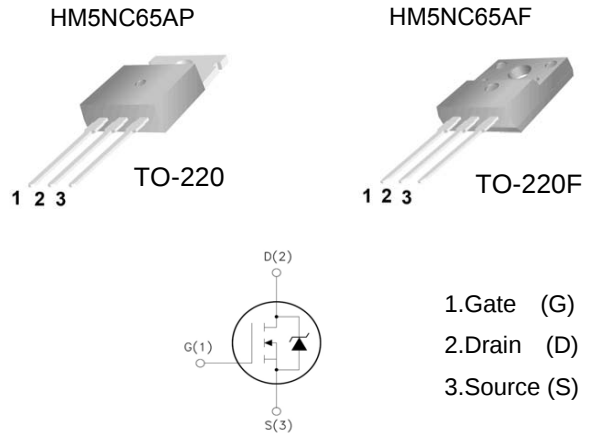


HM5NC65AP-HM5NC65AF

Features:

- ☐ Low Intrinsic Capacitances.
- ☐ Excellent Switching Characteristics.
- ☐ Extended Safe Operating Area.
- ☐ Unrivalled Gate Charge :Qg= 10nC (Typ.).
- ☐ BVDSS=650V, ID=5A
- ☐ RDS(on) : 0.9Ω (Max) @VG=10V
- ☐ 100% Avalanche Tested



Absolute Maximum Ratings (T_C=25°C)

Parameter	Symbol	HM5NC65AP	HM5NC65AF	Unit
Drain-Source Voltage (V _{GS} =0V)	V _{DS}	650		V
Gate-Source Voltage (V _{DS} =0V)	V _{GS}	±30		V
Continuous Drain Current at T _C =25°C	I _{D (DC)}	5	5*	A
Continuous Drain Current at T _C =100°C	I _{D (DC)}	3	3*	A
Pulsed drain current (Note 1)	I _{DM (pluse)}	15	15*	A
Drain Source voltage slope, VDS = 480 V, ID =5 A, Tj = 125 °C	dv/dt	48		V/ns
Maximum Power Dissipation (T _C =25°C)	P _D	49	29	W
Derate above 25°C		0.39	0.23	W/°C
Single pulse avalanche energy (Note2)	E _{AS}	135		mJ
Avalanche current (Note 1)	I _{AR}	2.5		A
Repetitive Avalanche energy, t _{AR} limited by T _{jmax} (Note 1)	E _{AR}	0.4		mJ
Operating Junction and Storage Temperature Range	T _J , T _{STG}	-55...+150		°C

* limited by maximum junction temperature

Thermal Characteristic

Parameter	Symbol	HM5NC65AP	HM5NC65AF	Unit
Thermal Resistance, Junction-to-Case (Maximum)	R _{thJC}	2.55	4.3	°C /W
Thermal Resistance, Junction-to-Ambient (Maximum)	R _{thJA}	62	80	°C /W

Electrical Characteristics (TA=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
On/off states						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA	650			V
Zero Gate Voltage Drain Current(Tc=25℃)	I _{DSS}	V _{DS} =650V,V _{GS} =0V			1	μA
Zero Gate Voltage Drain Current(Tc=125℃)	I _{DSS}	V _{DS} =650V,V _{GS} =0V			50	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±30V,V _{DS} =0V			±100	nA
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =250μA	2.5	3	3.5	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =3A		780	900	mΩ
Dynamic Characteristics						
Forward Transconductance	g _{FS}	V _{DS} = 20V, I _D = 3A		4.8		S
Input Capacitance	C _{iss}	V _{DS} =50V,V _{GS} =0V, F=1.0MHz		460		PF
Output Capacitance	C _{OSS}			45		PF
Reverse Transfer Capacitance	C _{rss}			3.5		PF
Total Gate Charge	Q _g	V _{DS} =480V,I _D =5A, V _{GS} =10V		10	20	nC
Gate-Source Charge	Q _{gs}			1.6		nC
Gate-Drain Charge	Q _{gd}			4		nC
Intrinsic gate resistance	R _G	f = 1 MHz open drain		2.5		Ω
Switching times						
Turn-on Delay Time	t _{d(on)}	V _{DD} =380V,I _D =5A, R _G =18Ω,V _{GS} =10V		6		nS
Turn-on Rise Time	t _r			3		nS
Turn-Off Delay Time	t _{d(off)}			50	60	nS
Turn-Off Fall Time	t _f			9	15	nS
Source- Drain Diode Characteristics						
Source-drain current(Body Diode)	I _{SD}	T _C =25℃			5	A
Pulsed Source-drain current(Body Diode)	I _{SDM}				15	A
Forward On Voltage	V _{SD}	T _j =25℃,I _{SD} =5A,V _{GS} =0V		1	1.3	V
Reverse Recovery Time	t _{rr}	T _j =25℃,I _F =5A,di/dt=100A/μs		250		nS
Reverse Recovery Charge	Q _{rr}			2.2		uC
Peak reverse recovery current	I _{rrm}			15		A

Notes: 1.Repetitive Rating: Pulse width limited by maximum junction temperature

2. $T_j=25^\circ C, V_{DD}=50V, V_G=10V, R_G=25\Omega$

Typical Characteristics

Figure1. Safe operating area for TO-220

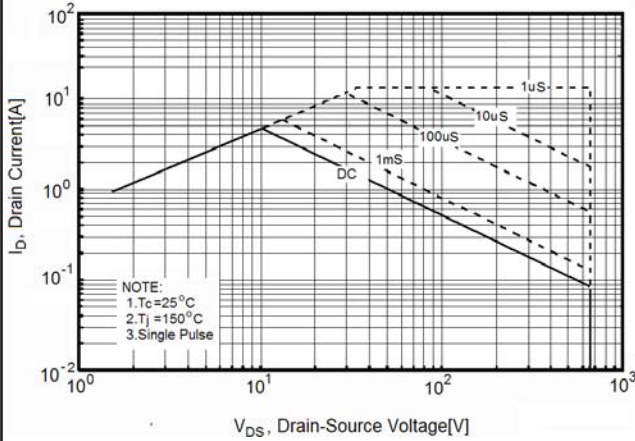


Figure2. Safe operating area for TO-220F

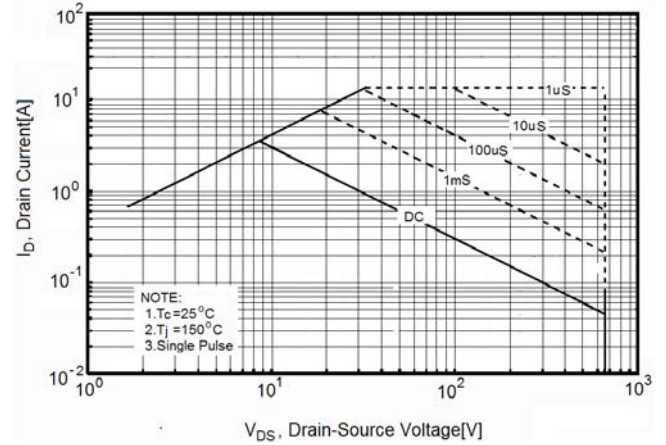


Figure3. Source-Drain Diode Forward Voltage

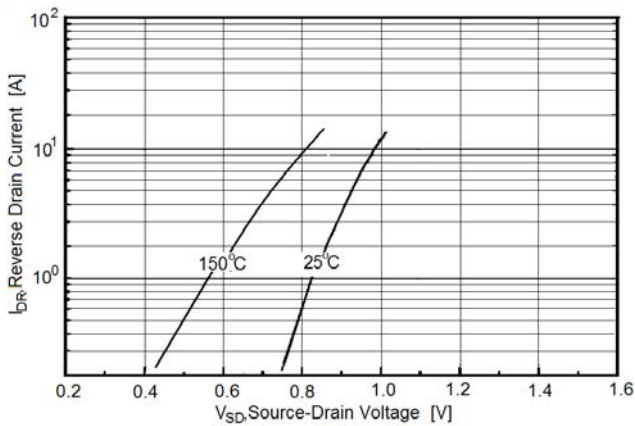


Figure4. Output characteristics

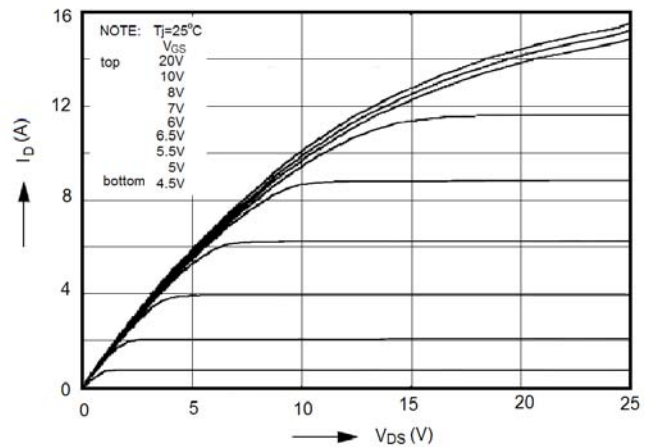


Figure5. Transfer characteristics

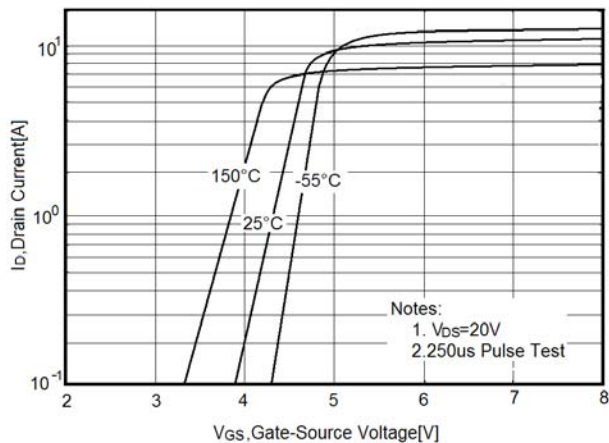
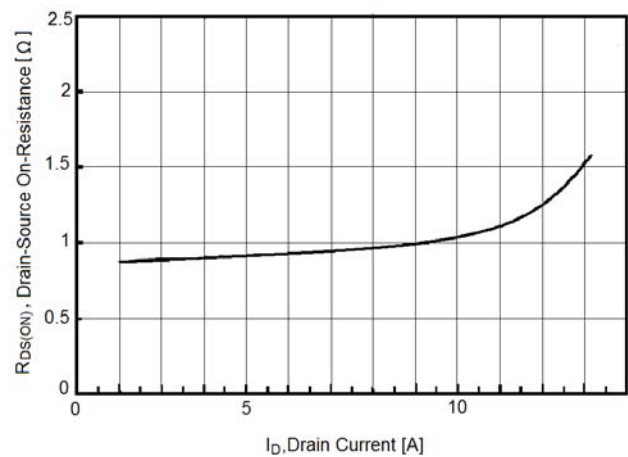


Figure6. Static drain-source on resistance



Typical Characteristics

Figure7. $R_{DS(ON)}$ vs Junction Temperature

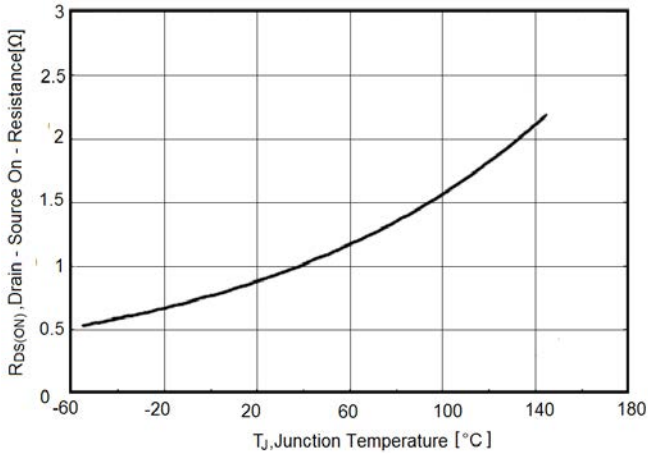


Figure8. BV_{DSS} vs Junction Temperature

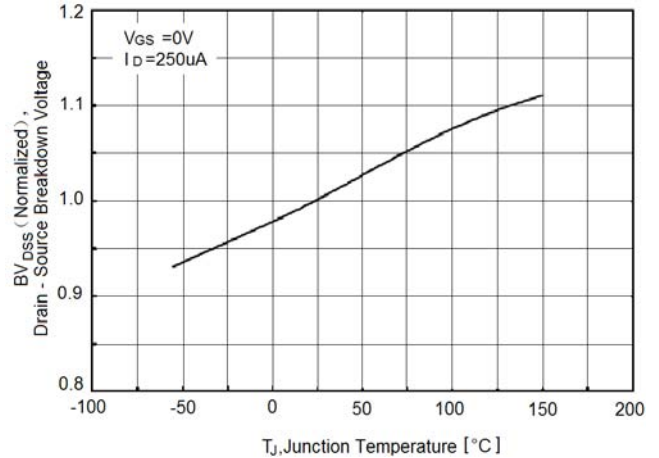


Figure9. Maximum I_D vs Junction Temperature

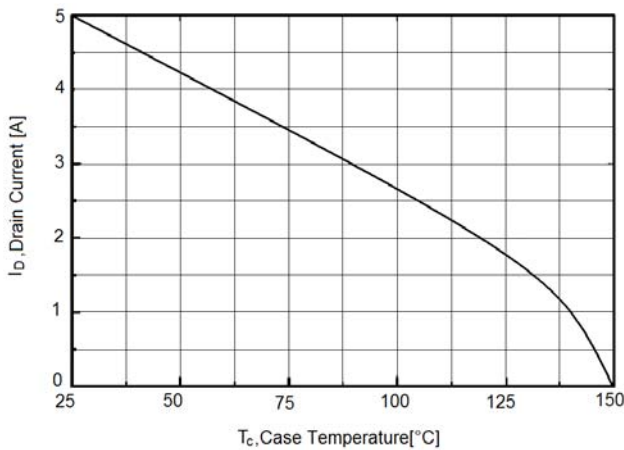


Figure10. Gate charge waveforms

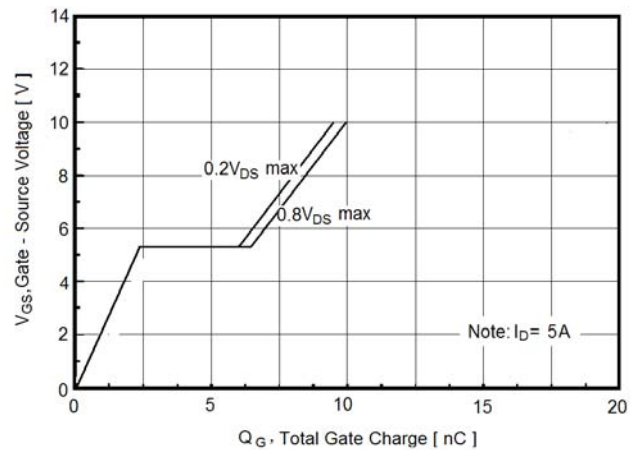


Figure11. Capacitance

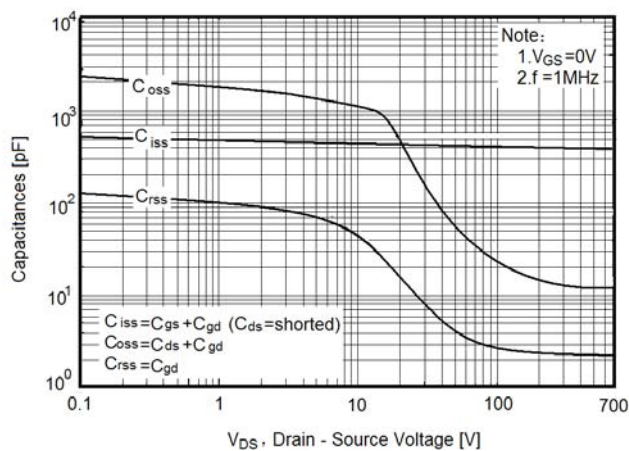
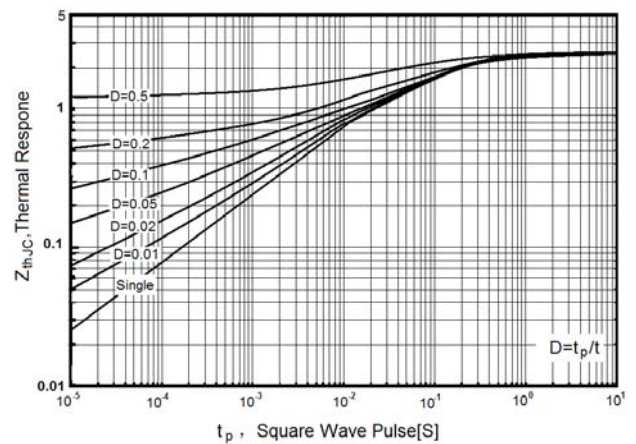
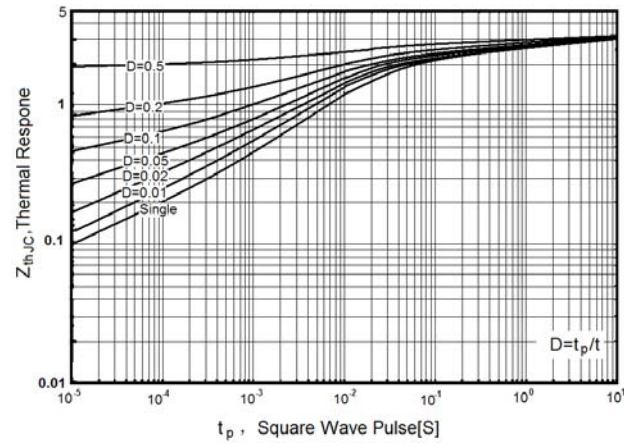


Figure12. Transient Thermal Impedance



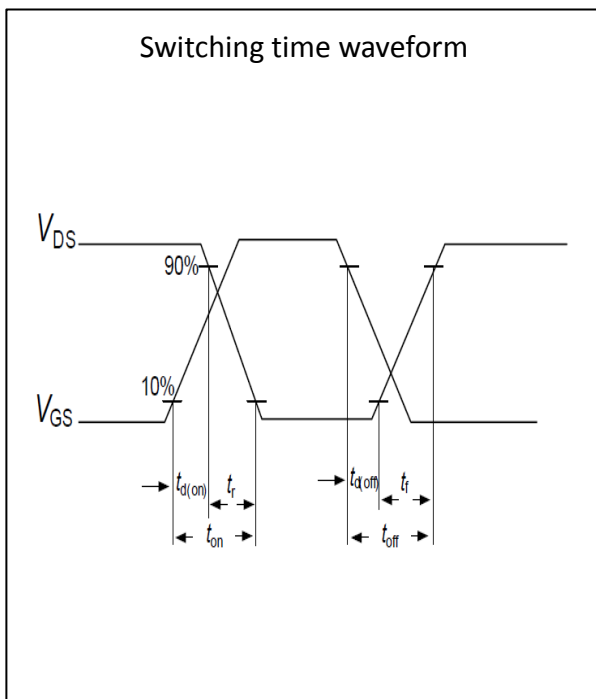
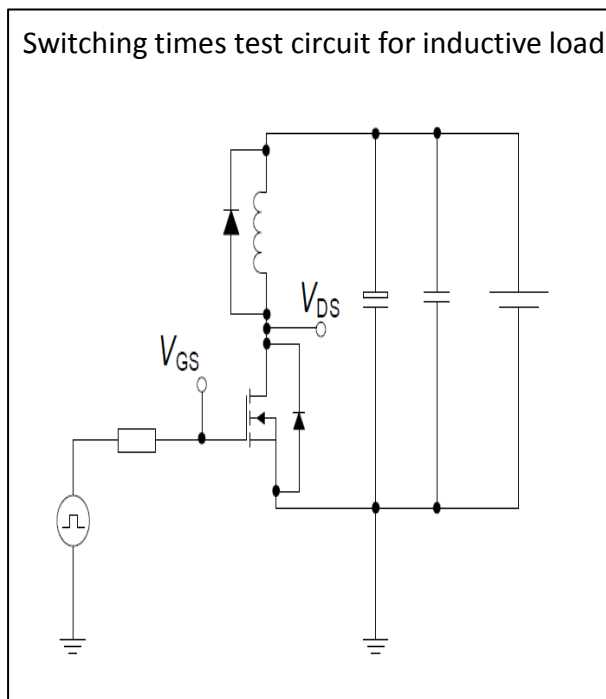
Typical Characteristics

Figure13. Transient Thermal Impedance for TO-220F

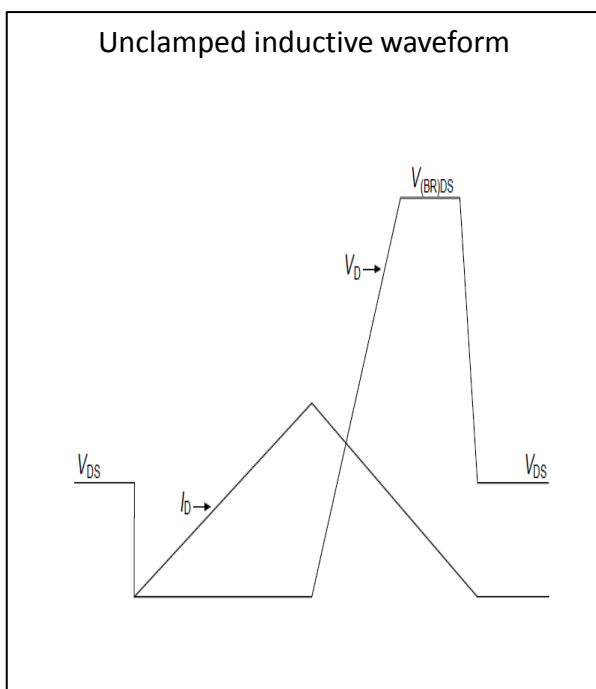
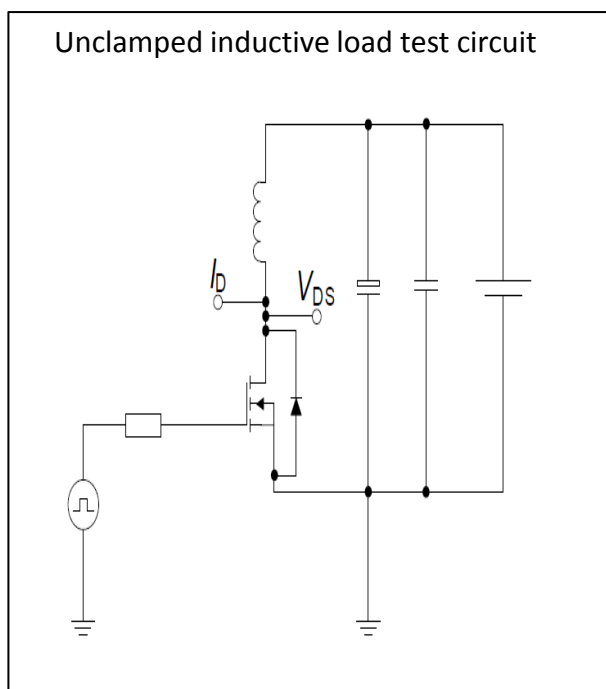


Test circuits

Switching times test circuit and waveform for inductive load

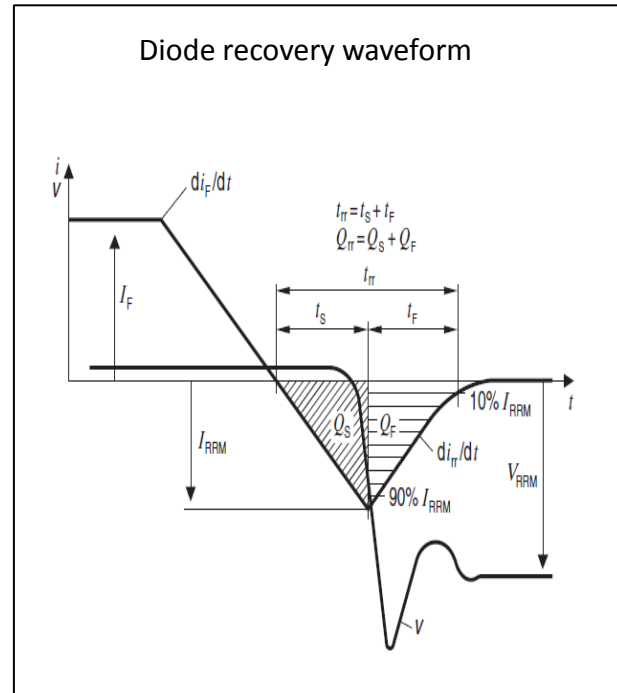
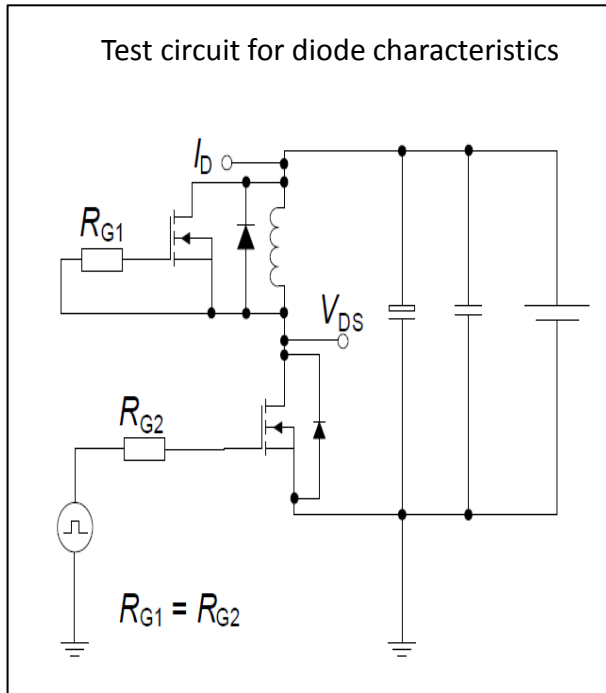


Unclamped inductive load test circuit and waveform



Test circuits

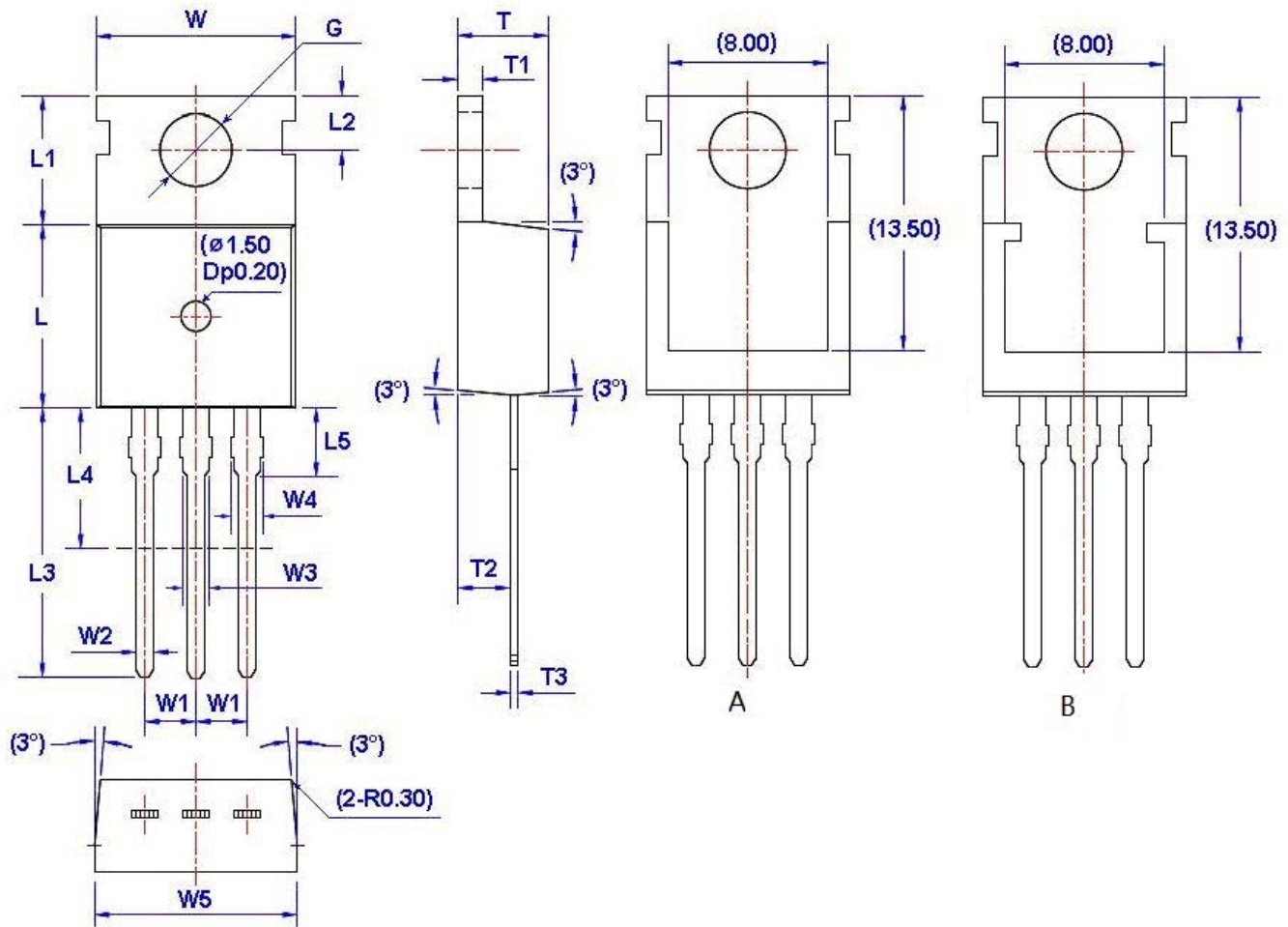
Test circuit and waveform for diode characteristics



Package Dimension

TO-220

Unit: mm

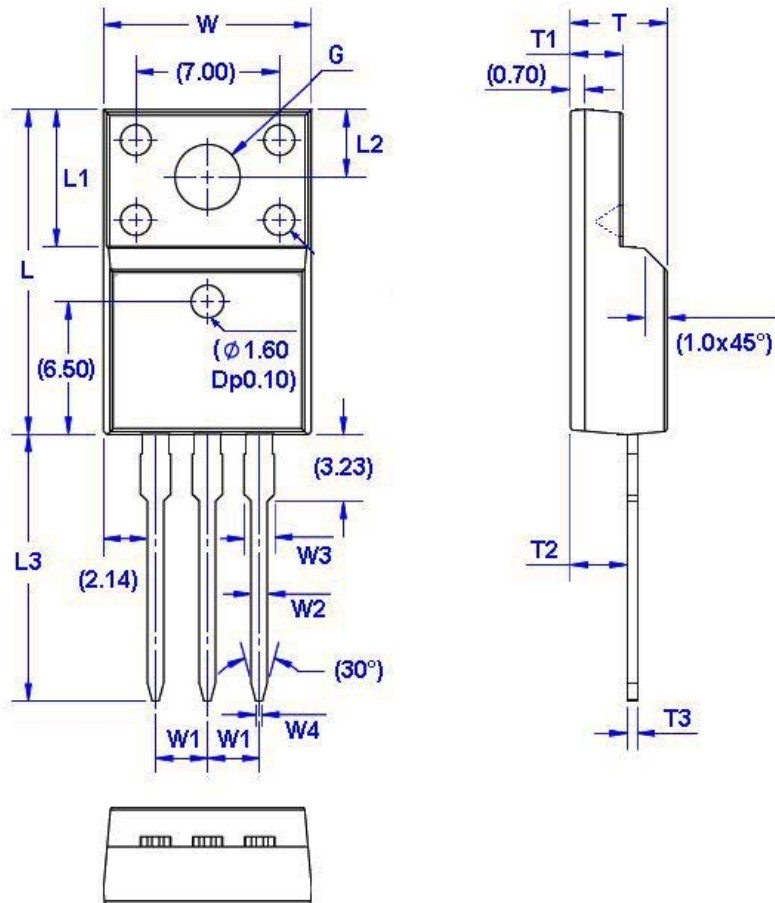


Symbol	Size		Symbol	Size		Symbol	Size		Symbol	Size	
	Min	Max		Min	Max		Min	Max		Min	Max
W	9.66	10.28	W5	9.80	10.20	L4**	6.20	6.60	T3	0.45	0.60
W1	2.54 (TYP)		L	9.00	9.40	L5	2.79	3.30	G(Φ)	3.50	3.70
W2	0.70	0.95	L1	6.40	6.80	T	4.30	4.70			
W3	1.17	1.37	L2	2.70	2.90	T1	1.15	1.40			
W4	1.32	1.72	L3	12.70	14.27	T2	2.20	2.60			

Package Dimension

TO-220F

Unit: mm



Symbol	Size		Symbol	Size		Symbol	Size		Symbol	Size	
	Min	Max		Min	Max		Min	Max		Min	Max
W	9.96	10.36	W4	0.25	0.45	L3	12.78	13.18	T3	0.45	0.60
W1	2.54 (TYP)		L	15.67	16.07	T	4.50	4.90	G(Φ)	3.08	3.28
W2	0.70	0.90	L1	6.48	6.88	T1	2.34	2.74			
W3	1.24	1.47	L2	3.20	3.40	T2	2.56	2.96			