

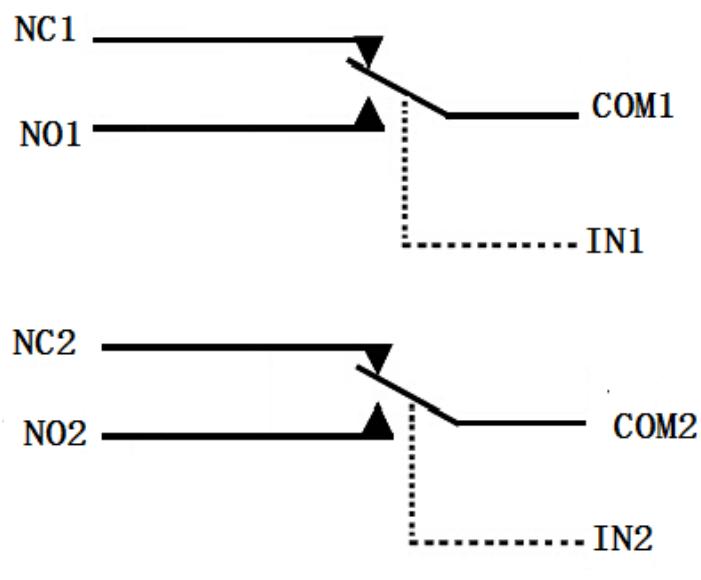
0.5 Ω Dual SPDT Negative Signal Handling Analog Switch

General Description

The HM5228 is an advanced CMOS analog switch fabricated in Sub-micron silicon gate CMOS technology. The part also features guaranteed Break Before Make (BBM) switching, assuring the switches never short the driver. The switches can handle negative signal down to -2V.

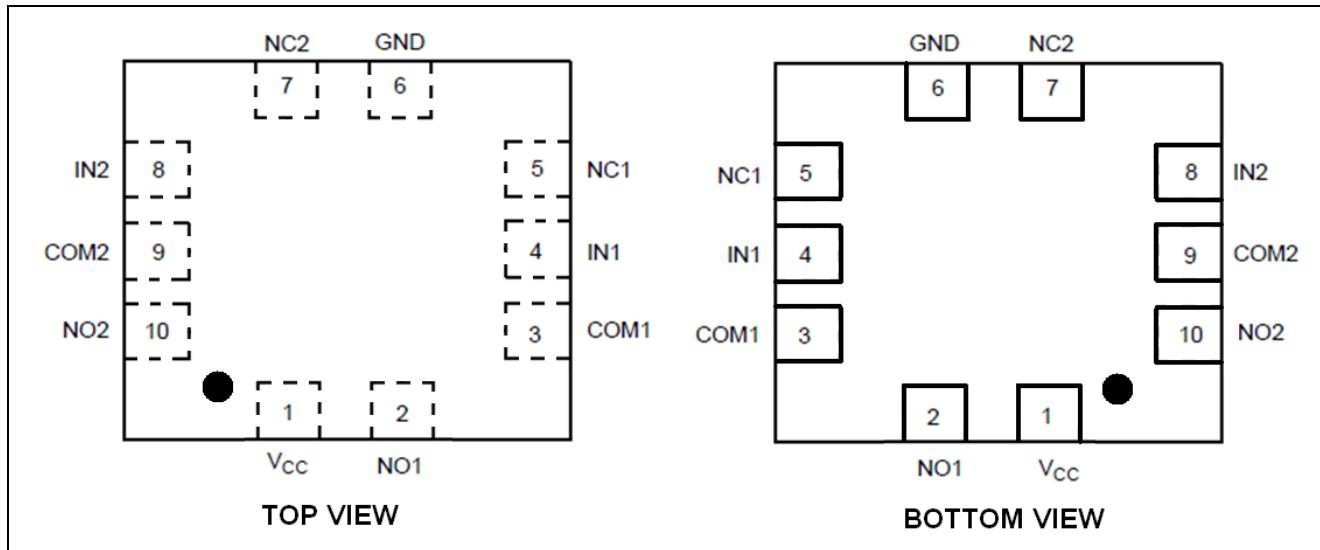
Features

- Low RON: typical 0.5Ω @ $V_{CC} = 5V$
- Single Supply Operation from 2.5V–5V
- Full -2V– V_{CC} Signal Handling Capability
- High Off-Channel Isolation
- Low Standby Current
- Low Distortion
- Break-Before-Make(BBM) switching
- High Continuous Current Capability: ± 300 mA Through Each Switch
- Applications in Cell Phone Audio Block/ Speaker and Earphone Switching Ring-Tone Chip / Amplifier Switching/Modems
- Package :QFN10L(1.8*1.4)



Analog Symbol

Pin Configuration



Pin Function

QFN PIN #	Symbol	FUNCTION
1	VCC	Power supply
2	NO1	Independent Channels
3	COM1	Common Channels
4	IN1	Controls
5	NC1	Independent Channels
6	GND	Ground (V)
7	NC2	Independent Channels
8	IN2	Controls
9	COM2	Common Channels
10	NO2	Independent Channels

Truth Table

IN1, 2	NO1, 2	NC1, 2
0	OFF	ON
1	ON	OFF

Absolute Maximum Ratings

Characteristic	Symbol	Value	Unit
Supply Voltage	V _{CC}	-0.5~+6.0	V
Analog Input Voltage	V _{IS}	-2.5~V _{CC} +0.3 V _{CC} -V _{IS} <6.5V	V
Digital Select Input Voltage	V _{IN}	-0.5~+6.0	V
Output Voltage	V _O	-2.5~V _{CC} +0.3 V _{CC} -V _O <6.5V	V
Continuous DC Current from COM to NC/NO	I _{an1}	±300	mA
Peak Current from COM to NC/NO, 10 duty cycle (Note 1)	I _{an1-pk1}	±500	mA
Continuous DC Current into COM/NO/NC with respect to V _{CC} or GND	I _{clmp}	±100	mA

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

Note1. Defined as 10% ON, 90% off duty cycle.

Characteristic	Symbol	Min	Max	Unit
DC Supply Voltag	V _{CC}	2.5	5	V
Digital Select Input Voltage	V _{IN}	GND	5	V
Analog Input Voltage	V _{IS}	-2	V _{CC}	V
Operating Temperature Range	T _A	-45	+85	°C
Input Rise or Fall Time, SELECT	tr,tf	0	20	ns/V

Electrical Characteristics

Symbol	Parameter	Test Conditions	V _{CC} ±10%	T _A =25°C			Unit
				Min	Typ	Max	
V _{IH}	High-Level Input Voltage, Select Inputs		2.5~5	1.3			V
V _{IL}	Low-Level Input Voltage, Select Inputs		2.5~5			0.5	V
I _{IN}	Maximum Input Leakage Current, Select Inputs	V _{IN} =V _{CC} or GND	5			±0.3	uA
I _{OFF}	Power Off Leakage Current	V _{IN} =V _{CC} or GND	0			±0.5	uA
I _{CC}	Maximum Quiescent Supply Current (Note 2)	SELECT, V _{IS} = V _{CC} or GND	3.6			1	uA
I _{COM(ON)}	COM ON leakage Current	V _{IN} =V _{IL} or V _{IH} ,	5	-20		20	nA

	(Note 3)	V _{NO} =0.3V or 4.7V V _{NC} floating V _{NC} =0.3V or 4.7V V _{No} floating V _{COM} =0.3V or 4.7V					
R _{ON}	On-Resistance (Note 2, Note 3)	V _{IS} =GND to VCC I _{IN} = 100mA	5		0.4	0.5	Ω
			3.3		0.6	0.7	
			2.0		1.5	1.7	
R _{FLAT}	On-Resistance Flatness (Note 2, Note 3, Note 5)	I _{COM} =100mA V _{IS} =GND to VCC	5			0.3	Ω
ΔR_{ON}	On-Resistance Match Between Channels (Note 2, Note 3, Note 4)	I _{COM} =100mA V _{IS} =1.5V	5		0.25		Ω
t _{ON}	Turn-On Time (Figure 1)	V _{IS} =1.5V	2.5~3.3		35	45	ns
			2.5~5.0		25	30	
t _{OFF}	Turn-Off Time (Figure 1)	V _{IS} =1.5V	2.5~3.3		17	20	ns
			3.6~5.0		15	20	
t _{BBM}	Break-Before-Make Time (Note 6) (Figure 2)	C _L =35pF, R _{IS} =50 Ω V _{IS} =1.5V	2.5~3.3	7	9		ns
			3.6~5.0	4	6		
BW	On-Channel -3dB Bandwidth or Frequency Response (Figure 4)	R _{IS} =50 Ω	2.5~5.0		50		MHz
V _{ISO}	Off-Channel Isolation (Figure 4)	F _{IS} = 100kHz, V _{IN} = GND to V _{CC} C _L =5pF, R _L = 50 Ω V _{IS} =1V RMS	2.5~5.0		-66		dB
Q	Charge Injection Select Input to Common I/O (Figure 3)	V _{IN} = 0 or V _{CC} R _{IS} =0 Ω , C _L =100pF R _L =1M Ω Q=C _L \times ΔV_{Out}	2.5~3.3		51		pC
			3.6~5.0		49		
THD	Total Harmonic Distortion THD +Noise	F _{IS} =20Hz to 20KHz R _L =600 Ω , C _L =50pF V _{IS} =2V RMS	3.6		0.08		%
V _{CT}	Channel-to-Channel Crosstalk (Figure 4)	F _{IS} = 100KHz, V _{IN} = GND to V _{CC} R _L = 50 Ω , C _L =5pF V _{IS} =1V RMS	3.6~5.0		-72		dB
C _{IN}	Control Pin Input Capacitance		3.6		3.5		pF
C _{NC/C_{NO}}	NC/NO Port Capacitance		3.6		50		pF
C _{COM}	COM Port Capacitance When Switch is Enabled		3.6		120		pF

Note 2. Guaranteed by design

3. Guaranteed by design. Resistance measurements do not include test circuit or package resistance
4. $\Delta R_{ON} = R_{ON(MAX)} - R_{ON(MIN)}$ between NC1 and NC2 or between NO1 and NO2.
5. Flatness is defined as the difference between the maximum and minimum value of on-resistance as measured over the specified analog signal ranges.
6. Guaranteed by design in -40°C .

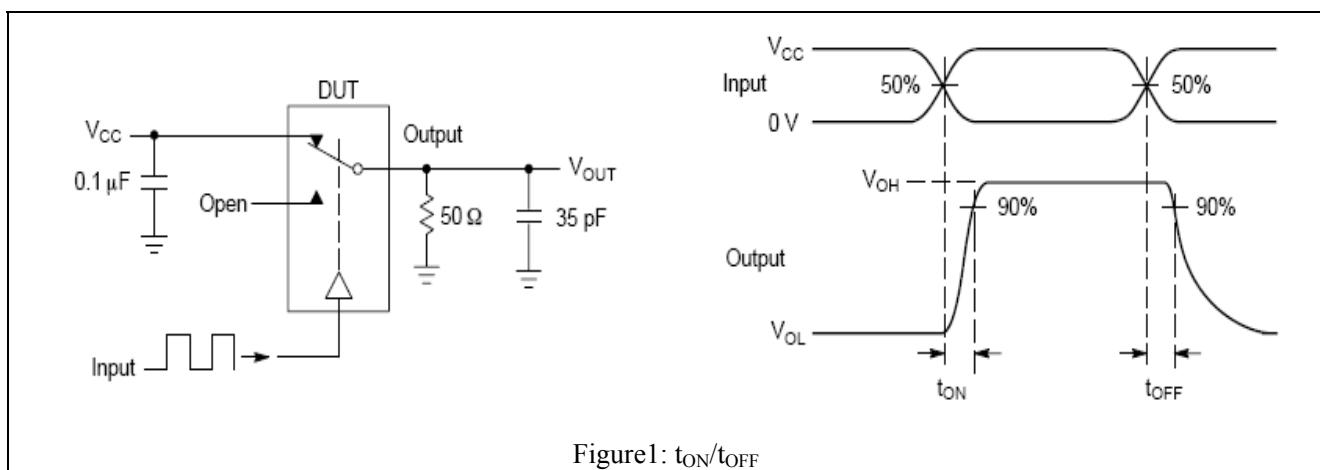
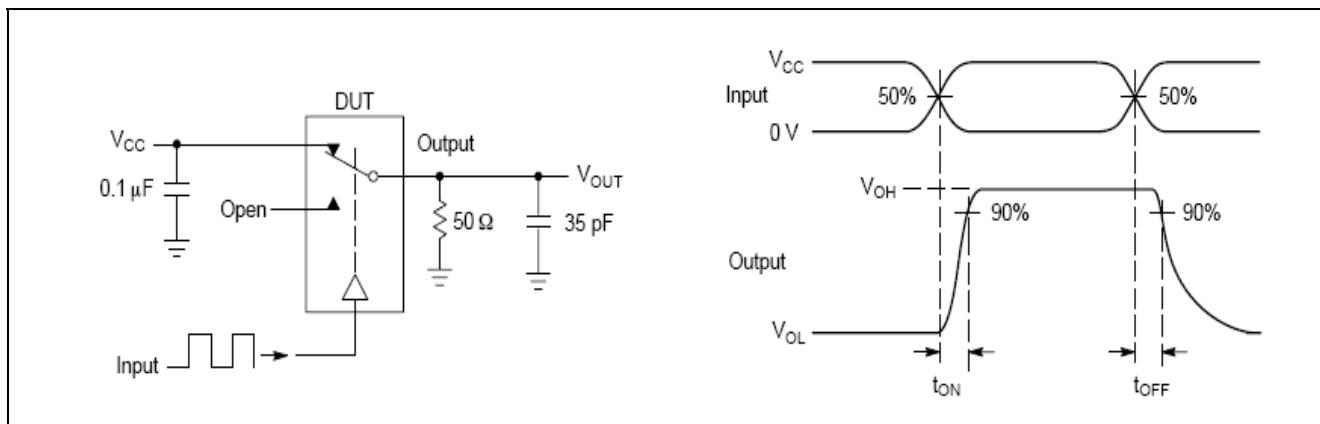


Figure1: t_{ON}/t_{OFF}

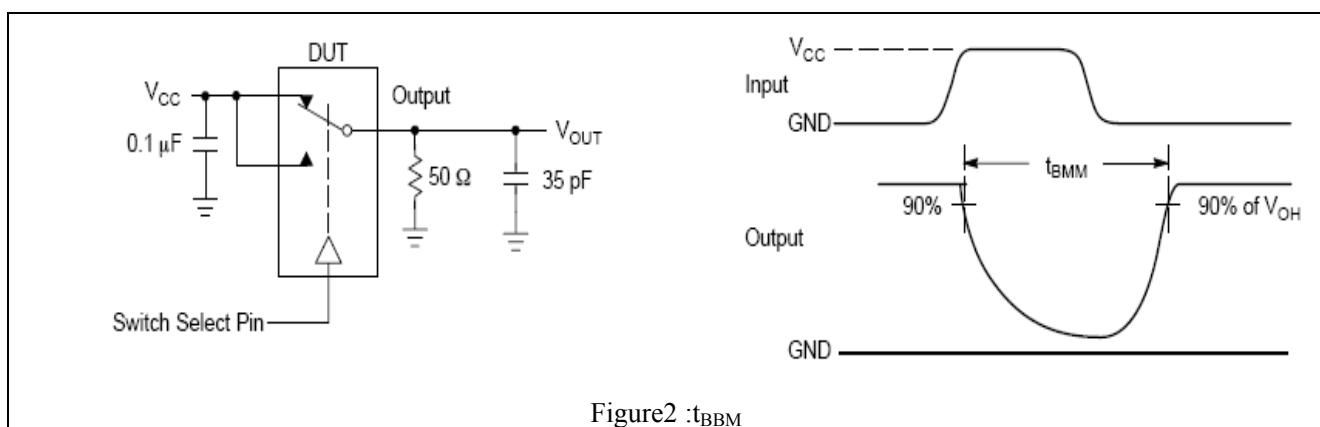


Figure2 : t_{BBM}

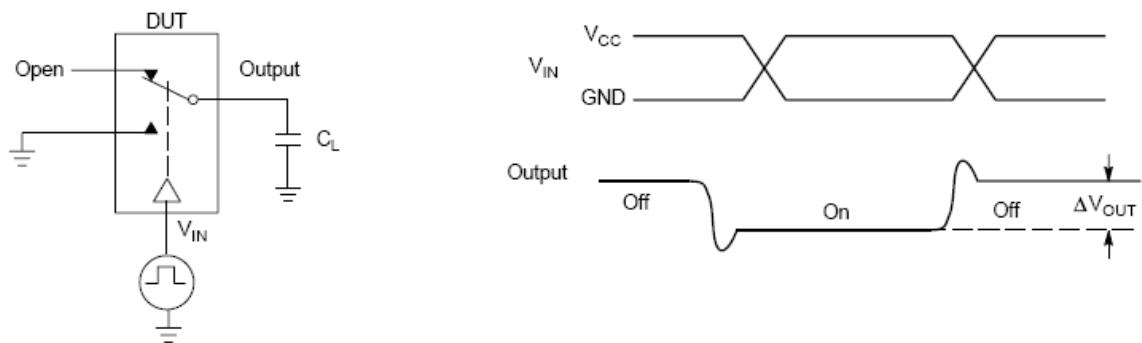
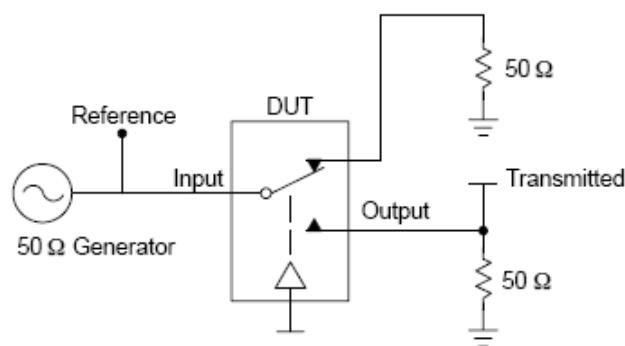


Figure3: Charge Injection



$$V_{ISO} = 20 \log \left(\frac{V_{OUT}}{V_{IN}} \right)$$

$$V_{ONL} = 20 \log \left(\frac{V_{OUT}}{V_{IN}} \right)$$

$$V_{CT} = 20 \log \left(\frac{V_{OUT}}{V_{IN}} \right)$$

BW = the frequency 3dB below V_{ONL}

Figure 4. -3dB Bandwidth/Off Channel Isolation/On Channel Loss(BW)/Crosstalk

(On Channel to Off Channel)/V_{ONL}

Package Dimension

QFN10L

