

N and P-Channel Enhancement Mode Power MOSFET

Description

The HM4618B uses advanced trench technology to provide excellent $R_{\mathrm{DS}(\mathrm{ON})}$ and low gate charge . The complementary MOSFETs may be used to form a level shifted high side switch, and for a host of other applications.

General Features

N-Channel

 $V_{DS} = 40V, I_{D} = 8A$

 $R_{DS(ON)}$ < 19m Ω @ V_{GS} =10V

 $R_{DS(ON)}$ < 29m Ω @ V_{GS} =4.5V

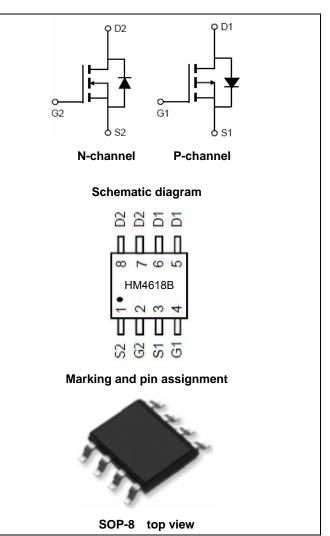
P-Channel

 $V_{DS} = -40V, I_{D} = -7A$

 $R_{DS(ON)}$ <35m Ω @ V_{GS} =-10V

 $R_{DS(ON)} < 45 m\Omega$ @ $V_{GS} = -4.5 V$

- High power and current handing capability
- Lead free product is acquired
- Surface mount package



Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
HM4618B	HM4618B	SOP-8	Ø330mm	12mm	2500 units

Absolute Maximum Ratings (T_A=25 ℃unless otherwise noted)

Parame	Symbol	N-Channel	annel P-Channel			
Drain-Source Voltage		V _{DS}	40	-40	V	
Gate-Source Voltage		V _{GS}	±20	±20	V	
Continuous Drain Current	T _A =25℃		8	-7	^	
Continuous Drain Current	T _A =70°C	I _D	6	-5.5	Α	
Pulsed Drain Current (Note 1)		I _{DM}	40	-30	Α	
Maximum Power Dissipation T _A =25℃		P _D	2.0	2.0	W	
Operating Junction and Storage Temperature Range		T_{J} , T_{STG}	-55 To 150	-55 To 150	$^{\circ}$	



Thermal Characteristic

Thermal Resistance,Junction-to-Ambient (Note2)	$R_{\theta JA}$	N-Ch	62.5	°C/W
Thermal Resistance,Junction-to-Ambient (Note2)	R _{eJA}	P-Ch	62.5	°C/W

N-CH Electrical Characteristics (T_A=25 °C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	-Source Breakdown Voltage BV _{DSS} V _{GS} =0V I _D =250µA		40	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =40V,V _{GS} =0V	-	-	1	μΑ
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)	<u>.</u>					•
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	1	1.5	2.0	V
Drain Course On State Desistance	Б	V _{GS} =10V, I _D =8A	-	14	19	mΩ
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =4.5V, I _D =4A	-	19	29	mΩ
Forward Transconductance	g fs	V_{DS} =5 V , I_{D} =8 A	33	-	-	S
Dynamic Characteristics (Note4)			•			•
Input Capacitance	C _{lss}	\/ 00\/\/ 0\/	-	415	-	PF
Output Capacitance	Coss	V_{DS} =20V, V_{GS} =0V,	-	112	-	PF
Reverse Transfer Capacitance	C _{rss}	F=1.0MHz	-	11	-	PF
Switching Characteristics (Note 4)			•			•
Turn-on Delay Time	t _{d(on)}		-	4	-	nS
Turn-on Rise Time	t _r	V_{DD} =20V, R_L =2.5 Ω	-	3	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =10 V , R_{GEN} =3 Ω	-	15	-	nS
Turn-Off Fall Time	t _f		-	2	-	nS
Total Gate Charge	Qg	\/ 00\/ L 0A	-	12	-	nC
Gate-Source Charge	Q _{gs}	V _{DS} =20V,I _D =8A,	-	3.2	-	nC
Gate-Drain Charge	Q_{gd}	V _{GS} =10V	-	3.1	-	nC
Drain-Source Diode Characteristics			•			•
Diode Forward Voltage (Note 3)	V_{SD}	V _{GS} =0V,I _S =8A	-	0.8	1.2	V



P-CH Electrical Characteristics (T_A=25 $^{\circ}$ C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics	<u> </u>					
Drain-Source Breakdown Voltage	Source Breakdown Voltage BV _{DSS} V _{GS} =0V I _D =-250µA		-40	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-40V,V _{GS} =0V	-	-	-1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)	<u> </u>					
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=-250\mu A$	-1.0	-1.5	-2.0	V
Drain-Source On-State Resistance	В	V _{GS} =-10V, I _D =-8A	-	29	35	mΩ
Diam-Source On-State Resistance	R _{DS(ON)}	V _{GS} =-4.5V, I _D =-4A	-	34	45	mΩ
Forward Transconductance	g FS	V _{DS} =-5V,I _D =-8A	20	-	-	S
Dynamic Characteristics (Note4)	<u> </u>					
Input Capacitance	C _{lss}	\/ 00\/\\ 0\/	-	520	-	PF
Output Capacitance	C _{oss}	V_{DS} =-20V, V_{GS} =0V, F=1.0MHz	-	100	-	PF
Reverse Transfer Capacitance	C _{rss}	r-1.0MHZ	-	65	-	PF
Switching Characteristics (Note 4)	<u> </u>					
Turn-on Delay Time	t _{d(on)}		-	7.5	-	nS
Turn-on Rise Time	t _r	V_{DD} =-20V, R_L =2.3 Ω	-	5.5	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =-10 V , R_{GEN} =6 Ω	-	19	-	nS
Turn-Off Fall Time	t _f		-	7	-	nS
Total Gate Charge	Qg	V - 20VI - 0A	-	13	-	nC
Gate-Source Charge	Q _{gs}	V_{DS} =-20V, I_{D} =-8A	-	3.8	-	nC
Gate-Drain Charge	Q_{gd}	V _{GS} =-10V	-	3.1	-	nC
Drain-Source Diode Characteristics			,	•		
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =-10A	-	-	-1.2	V
						•

Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- **2.** Surface Mounted on FR4 Board, $t \le 10$ sec.
- 3. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%.
- 4. Guaranteed by design, not subject to production



N- Channel Typical Electrical and Thermal Characteristics (Curves)

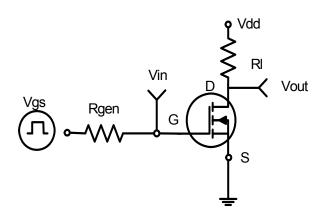


Figure 1:Switching Test Circuit

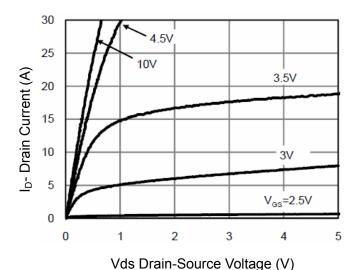


Figure 3 Output Characteristics

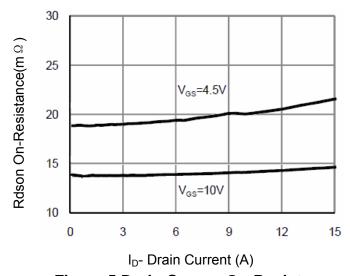


Figure 5 Drain-Source On-Resistance

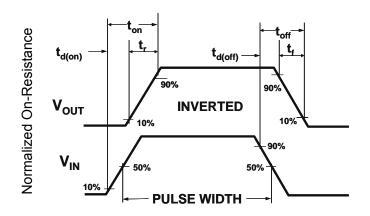


Figure 2:Switching Waveforms

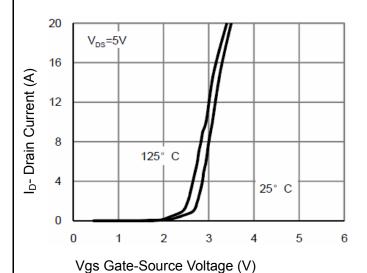


Figure 4 Transfer Characteristics

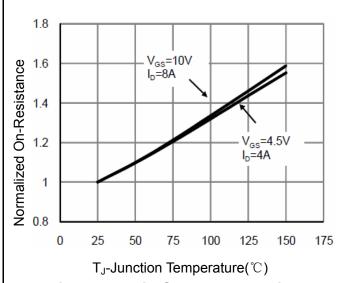


Figure 6 Drain-Source On-Resistance



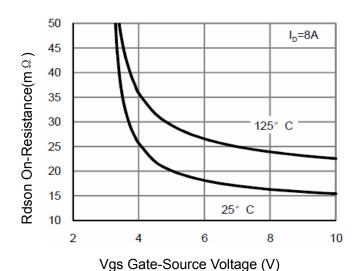


Figure7 Rdson vs Vgs

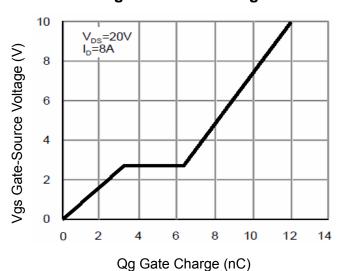


Figure 9 Gate Charge

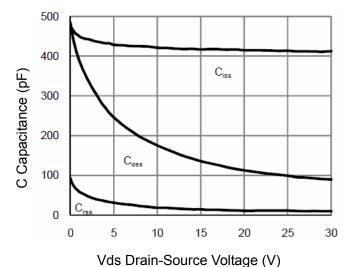


Figure 11 Capacitance vs Vds

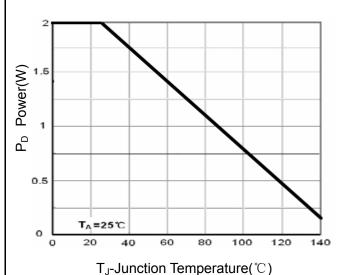


Figure 8 Power Dissipation

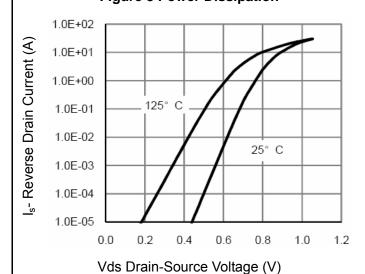
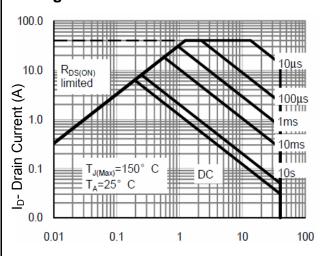


Figure 10 Source- Drain Diode Forward



Vds Drain-Source Voltage (V)
Figure 12 Safe Operation Area



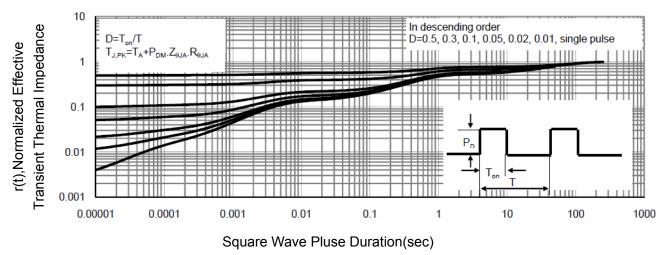
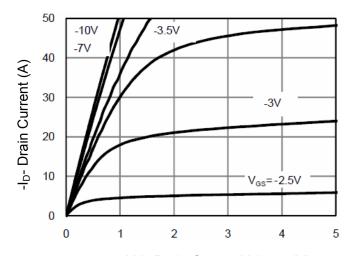


Figure 13 Normalized Maximum Transient Thermal Impedance



P- Channel Typical Electrical and Thermal Characteristics (Curves)



-Vds Drain-Source Voltage (V)

Figure 1 Output Characteristics

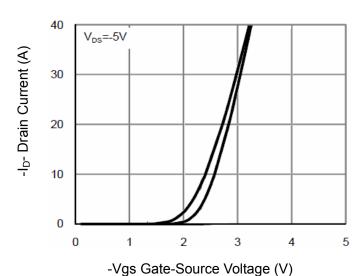


Figure 2 Transfer Characteristics

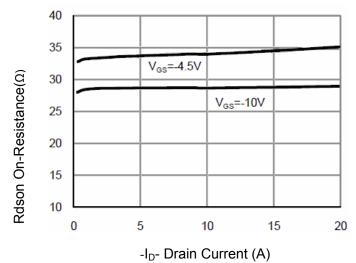


Figure 3 Rdson- Drain Current

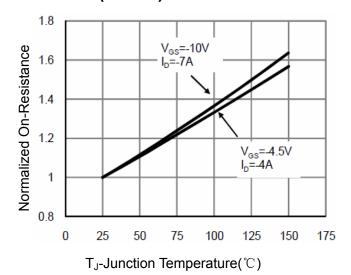


Figure 4 Rdson-Junction Temperature

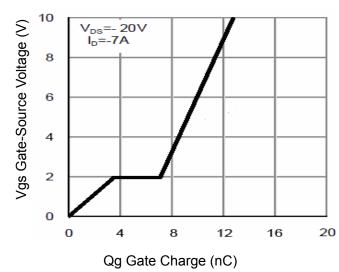


Figure 5 Gate Charge

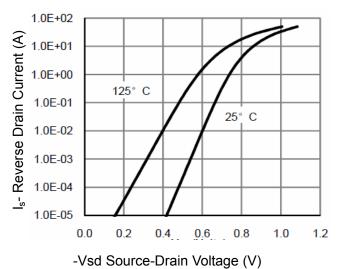


Figure 6 Source- Drain Diode Forward



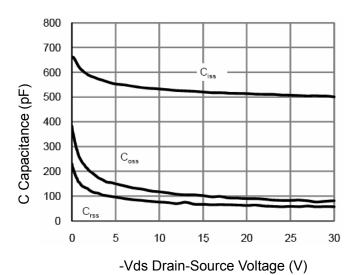


Figure 7 Capacitance vs Vds

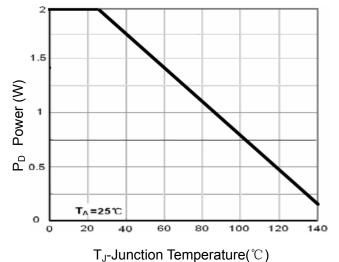


Figure 9 Power Dissipation

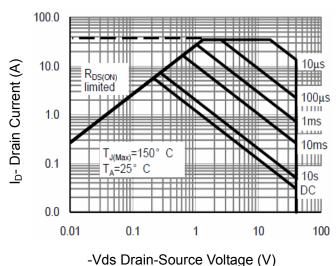


Figure 8 Safe Operation Area

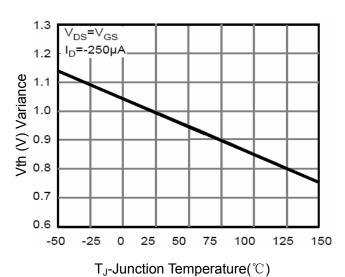


Figure 10 V_{GS(th)} vs Junction Temperature

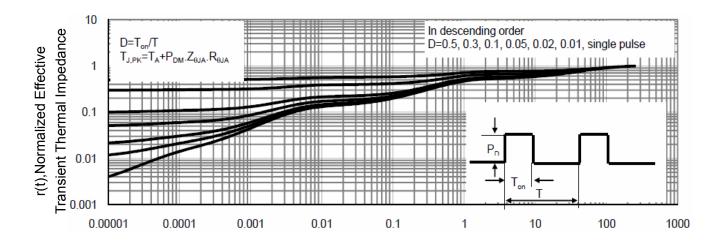
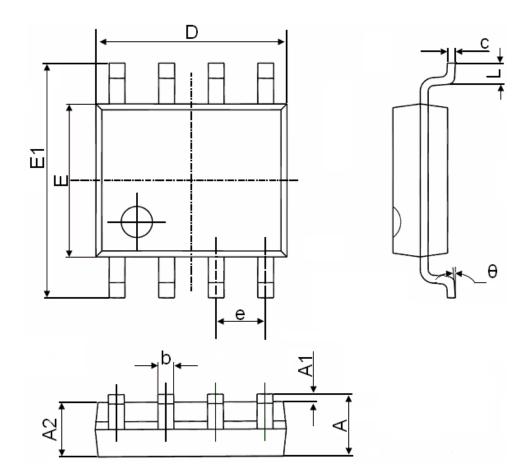


Figure 11 Normalized Maximum Transient Thermal Impedance

Square Wave Pluse Duration(sec)



SOP-8 Package Information



Symbol	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
Α	1.350	1.750	0.053	0.069	
A1	0.100	0.250	0.004	0.010	
A2	1.350	1.550	0.053	0.061	
b	0.330	0.510	0.013	0.020	
С	0.170	0.250	0.006	0.010	
D	4.700	5.100	0.185	0.200	
E	3.800	4.000	0.150	0.157	
E1	5.800	6.200	0.228	0.244	
е	1.270(BSC)		0.050(BSC)		
L	0.400	1.270	0.016	0.050	
θ	0°	8°	0°	8°	



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