

N and P-Channel Enhancement Mode Power MOSFET

Description

The HM4606A uses advanced trench technology to provide excellent $R_{DS(ON)}$ and low gate charge . The complementary MOSFETs may be used to form a level shifted high side switch, and for a host of other applications.

General Features

N-Channel

 V_{DS} = 30V, I_{D} =6.5A

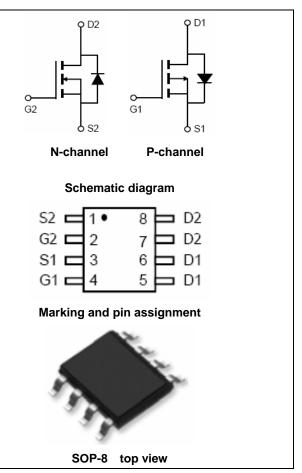
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\mathsf{R}_{\mathsf{DS}(\mathsf{ON})} < 30 \text{m}\Omega \textcircled{0} \mathsf{V}_\mathsf{GS} \texttt{=} 10 \mathsf{V}
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• P-Channel

V_{DS} = -30V,I_D = -7A

 $R_{DS(ON)} < 33m\Omega @ V_{GS}=-10V$

- High power and current handing capability
- Lead free product is acquired
- Surface mount package



Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity		
HM4606A	HM4606A	SOP-8	Ø330mm	12mm	2500 units		

Absolute Maximum Ratings (T_A=25℃ unless otherwise noted)

Parameter			P-Channel	Unit	
Drain-Source Voltage			-30	V	
Gate-Source Voltage			±20	V	
T _A =25℃		6.5	-7	٨	
T _A =70℃	ID ID	5.4	-5.8	A	
Pulsed Drain Current (Note 1)		30	-30	А	
T _A =25℃	PD	2.0	2.0	W	
Operating Junction and Storage Temperature Range			-55 To 150	°C	
	T _A =25°C T _A =70°C T _A =25°C	VDS VGS TA=25°C TA=70°C ID IDM TA=25°C	$\begin{tabular}{ c c c c c c } \hline & & & & & & & & & & & & & & & & & & $	$\begin{tabular}{ c c c c c c } \hline V_{DS} & 30 & -30 \\ \hline V_{DS} & ±20 & ±20 \\ \hline V_{GS} & ±20 & ±20 \\ \hline $T_A=25^{\circ}C$ & I_D & 6.5 & -7 \\ \hline $T_A=70^{\circ}C$ & I_D & 30 & -5.8 \\ \hline I_{DM} & 30 & -30 \\ \hline $T_A=25^{\circ}C$ & P_D & 2.0 & 2.0 \\ \hline \end{tabular}$	

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient (Note2)	R _{0JA}	N-Ch	62.5	°C/W
Thermal Resistance, Junction-to-Ambient (Note2)	$R_{ extsf{ heta}JA}$	P-Ch	62.5	°C/W



N-CH Electrical Characteristics (T_A=25[°]C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit		
Off Characteristics								
Drain-Source Breakdown Voltage	BV _{DSS}	oss V _{GS} =0V I _D =250μA		33	-	V		
Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} =30V, V_{GS} =0V	-	-	1	μA		
Gate-Body Leakage Current	I _{GSS}	V_{GS} =±20V, V_{DS} =0V	-	-	±100	nA		
On Characteristics (Note 3)								
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	1	1.6	3	V		
Drain-Source On-State Resistance	R _{DS(ON)}	V_{GS} =10V, I _D =6A	-	20	30	mΩ		
Forward Transconductance	g fs	V _{DS} =5V,I _D =6A	15	-	-	S		
Dynamic Characteristics (Note4)								
Input Capacitance	C _{lss}	V _{DS} =15V,V _{GS} =0V,	-	255	-	PF		
Output Capacitance	C _{oss}	v _{DS} =15v,v _{GS} =0v, F=1.0MHz	-	45	-	PF		
Reverse Transfer Capacitance	C _{rss}		-	35	-	PF		
Switching Characteristics (Note 4)								
Turn-on Delay Time	t _{d(on)}		-	4.5	-	nS		
Turn-on Rise Time	tr	V_{DD} =15V, R _L =2.5 Ω	-	2.5	-	nS		
Turn-Off Delay Time	t _{d(off)}	V_{GS} =10V, R_{GEN} =3 Ω	-	14.5	-	nS		
Turn-Off Fall Time	t _f		-	3.5	-	nS		
Total Gate Charge	Qg		-	13	-	nC		
Gate-Source Charge	Q _{gs}	V _{DS} =15V,I _D =6A, V _{GS} =10V	-	5.5	-	nC		
Gate-Drain Charge	Q _{gd}	V _{GS} =10V	-	3.5	_	nC		
Drain-Source Diode Characteristics			·					
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =6A	-	0.8	1.2	V		



P-CH Electrical Characteristics (T_A=25[°]C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics	·····					
Drain-Source Breakdown Voltage	BV _{DSS} V _{GS} =0V I _D =-250μA		-30	-33	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-30V,V _{GS} =0V	-	-	-1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V		-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=-250\mu A$	-1.5	-1.9	-2.5	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =-10V, I _D =-6.5A	-	28	33	mΩ
Forward Transconductance	g fs	V _{DS} =-5V,I _D =-6.5A	10	-	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C _{lss}	(-15)()(-0)(-	520	-	PF
Output Capacitance	C _{oss}	V _{DS} =-15V,V _{GS} =0V, F=1.0MHz	-	100	-	PF
Reverse Transfer Capacitance	C _{rss}	r = 1.0m12	-	65	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t _{d(on)}		-	7.5	-	nS
Turn-on Rise Time	tr	V_{DD} =-15V, R _L =2.3 Ω	-	5.5	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =-10V, R_{GEN} =6 Ω	-	19	-	nS
Turn-Off Fall Time	t _f		-	7	-	nS
Total Gate Charge	Qg	V _{DS} =-15V,I _D =-6.5A	-	9.2	-	nC
Gate-Source Charge	Q _{gs}	v _{DS} =-15V,I _D =-0.5A V _{GS} =-10V	-	1.6	-	nC
Gate-Drain Charge	Q _{gd}	V _{GS} =-10V	_	2.2	-	nC
Drain-Source Diode Characteristics	·		•		-	
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =-6.5A	-	-	-1.2	V

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.

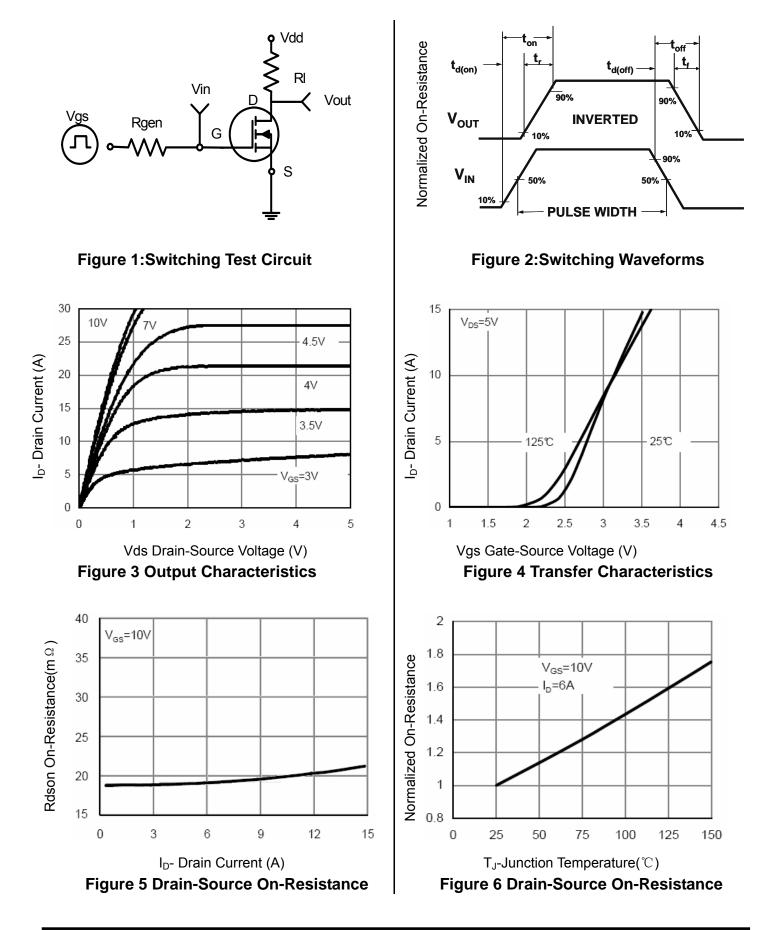
2. Surface Mounted on FR4 Board, $t \le 10$ sec.

3. Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%.

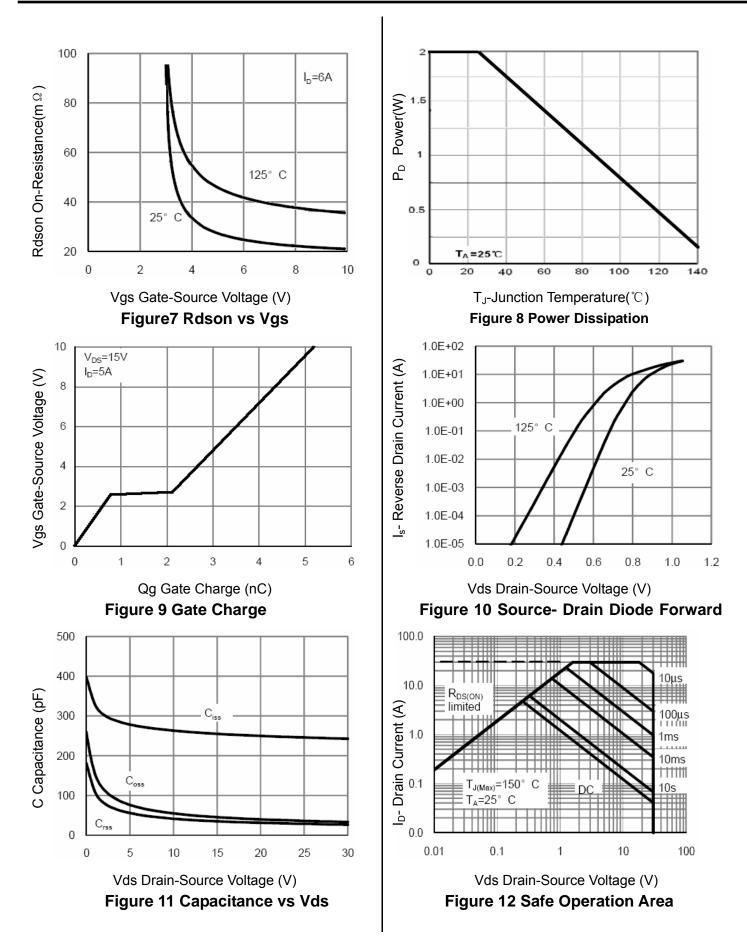
4. Guaranteed by design, not subject to production



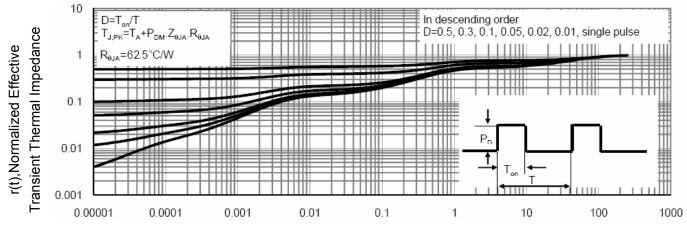
N- Channel Typical Electrical and Thermal Characteristics (Curves)







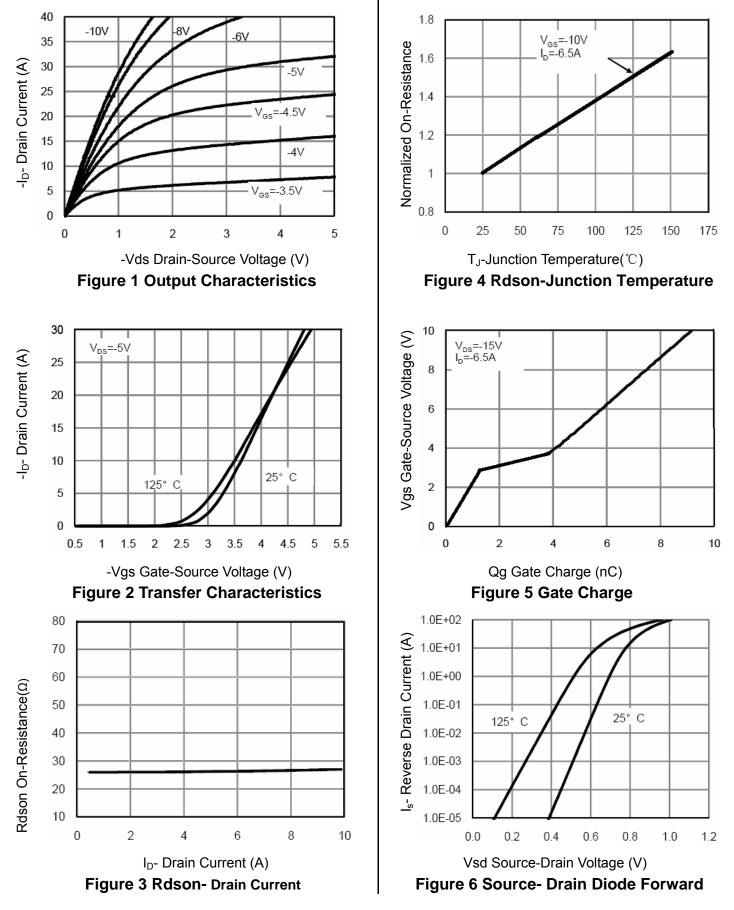




Square Wave Pluse Duration(sec) Figure 13 Normalized Maximum Transient Thermal Impedance



P- Channel Typical Electrical and Thermal Characteristics (Curves)





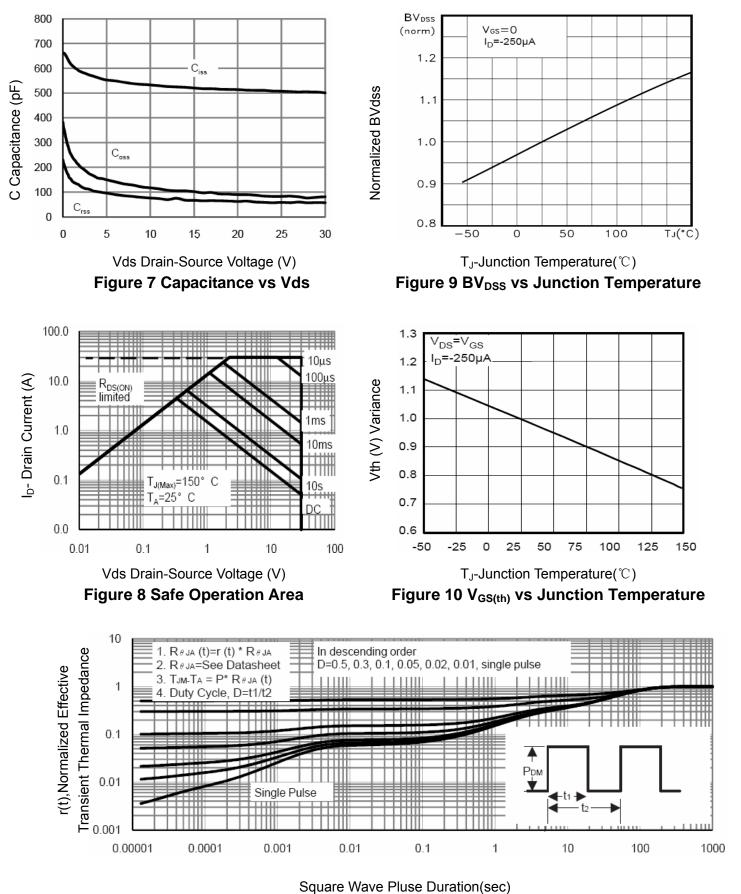
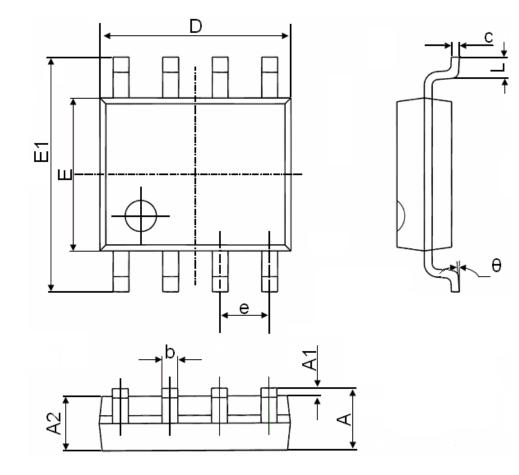


Figure 11 Normalized Maximum Transient Thermal Impedance



SOP-8 Package Information



Symbol	Dimensions I	n Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
A	1.350	1.750	0.053	0.069	
A1	0.100	0.250	0.004	0.010	
A2	1.350	1.550	0.053	0.061	
b	0.330	0.510	0.013	0.020	
с	0.170	0.250	0.006	0.010	
D	4.700	5.100	0.185	0.200	
E	3.800	4.000	0.150	0.157	
E1	5.800	6.200	0.228	0.244	
е	1.270	(BSC)	0.050(BSC)		
L	0.400	1.270	0.016	0.050	
θ	0°	8°	0°	8°	



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