

Features

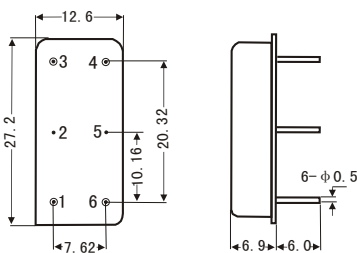
- Built-in buffer amplifier Low frequency pulling
- Dual output Flexible tuning design
- Perfect tuning linearity Thin film circuit High reliability
- DIP-18C package
- Operating temperature range: $-55^{\circ}\text{C} \sim +85^{\circ}\text{C}$

Specifications($T_A=25^{\circ}\text{C}, V_{CC}=+12\text{V}$)

Parameter	Symbol	Unit	Guaranteed	Typical	Test Condition
Frequency Range	$f_L \sim f_H$	MHz	25~50	—	$V_T: 0 \sim 15\text{V}$
Main Output	P_{O1}	dBm	≥ 12	—	$V_T=10\text{V}$
Aux Output	P_{O2}	dBm	—	0	$V_T=10\text{V}$
Power Output Variation	ΔP_O	dB	$\leq \pm 1.5$	± 1.0	$f_{L-H}: 25 \sim 50\text{MHz}$
Tuning Voltage	V_T	V	0~15	—	—
Pushing	K_{VC}	MHz/V	—	0.5	$V_{CC}=11 \sim 13\text{V}, V_T=10\text{V}$
Spurious	R_{fs}	dBc	≤ -70	—	$f_{L-H}: 25 \sim 50\text{MHz}$
Harmonics	R_{fn}	dBc	—	-10	$f_{L-H}: 25 \sim 50\text{MHz}$
SSB Phase Noise	S_{Φ}	dBc/Hz	—	-125	$V_T=10\text{V}, f_m=10\text{KHz}$
Frequency Drift	Δf	MHz	—	3	$V_T=10\text{V}, T_A: -55 \sim +85^{\circ}\text{C}$
Current	I_{CC}	mA	—	75	—
Tuning Port Capacitance	C_T	pF	—	1000	—

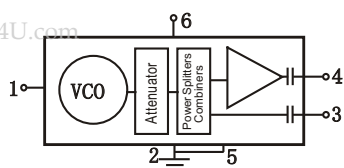
Absolute Ratings

Maximum DC Voltage : +15V
Maximum Tuning Voltage : +30V
Minimum Tuning Voltage : -0.7V
Maximum Storage Temp: +125°C



Application Notes

1. Pin 5 must be connected with 50 Ω Load
2. See assembly section for mounting information
3. ESD observe handling precautions



1. V_T
2. GND
3. P_{O2}
4. P_{O1}
5. GND
6. V_{CC}

Typical Performance

