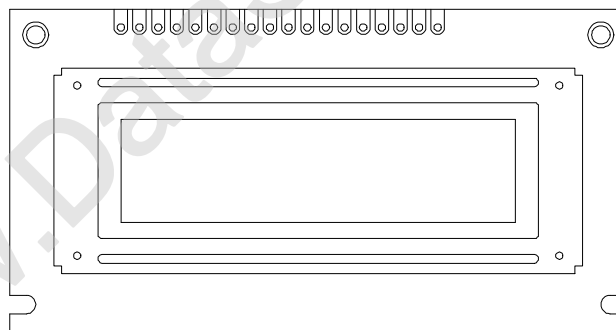


HANTRONIX

PRODUCT SPECIFICATION

HDM32GS12-B

122x32 GRAPHICS
LCD DISPLAY MODULE



HANTRONIX, INC. 10080 BUBB RD. CUPERTINO, CA 95014	Q.A.: JK	REV.: 1.0	HDM32GS12-B	SHEET 1 OF 14
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1.General specifications

1-1.General

The HDM32GS12-B dot matrix graphic LCD module consist of the liquid crystal display CMOS driver, a yellow or gray STN LCD.

1-2.Features

- A. Drive Method:1/32 Duty, 1/5 Bias.
- B. The Module Operating Voltage: 5.0V
- C. Viewing Direction: 6:00h or 12:00h
- D. Operating Temperature: 0℃~50℃ (Normal), -20℃~70℃ (Extended)
- E. Storage Temperature: -20℃~70℃(Normal), -30℃~80℃ (Extended)
- F. The Connect Method Between LCD and PCB: Zebra
- G. The LED Backlight Operating Voltage: 4.1v
- H. The LED Backlight Color: Yellow-Green or None

1-3.Mechanical Data:

- (1) Module Size ----- 84.0 w * 44.0 h mm
- (2) Viewing Area ----- 60.0 w * 18.5 h mm
- (3) Dot Size ----- 0.40 w * 0.40 h mm
- (4) Dot Quality----- 122 * 32

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1-4. Absolute maximum ratings

Item		Symbol	Min.	Max.	Unit	Note
Supply Voltage	Logic	Vdd	-0.3	7.0	V	1),2)
	LCD drive	Vdd - V0	Vdd-13.5	Vdd+0.3	V	1),2)
Input voltage		Vi	-0.3	Vdd+0.3	V	1),2)
Operating Temperature		Top	0	50	°C	
Storage Temperature		Tstg	-20	70	°C	
Humidity		—	—	90	%RH	

Note

- 1) The modules may be destroyed if they are used beyond absolute maximum ratings. In ordinary operation, it is desirable to use them within recommended operation conditions. Using the modules beyond these conditions may cause malfunction and poor reliability.
- 2) All voltage values are referenced to GND=0V.

1-5. Electrical characteristics

Electrical characteristics

Item		Symbol	Conditions	Min.	Typ.	Max.	Unit
Supply Voltage	Logic	Vdd		4.5	5.0	5.5	V
	LCD drive	Vdd-V0		—	5.0	—	
Input Voltage	"H" Level	Vih	A0,D0-D7,E,R/W TERMINALS	2.0	—	Vdd	
	"L" Level	Vil		0	—	0.8	
	"H" Level	Vih	RES TERMINALS	0.8xVdd	—	Vdd	
	"L" Level	Vil		0	—	0.2xVdd	
Frame Frequency		Ffm	Vdd=5V	60	65	70	Hz
Current Consumption	Logic	Idd	Vdd=5V Vdd-V0=5.0V	—	0.5	—	mA
	LCD drive	I0	Ffm=70Hz	—	0.5	—	
LCD Driving Voltage (Recommended Voltage)		Vdd-V0	Ta= 0°C φ=0°, θ=0°	—	—	—	V
			Ta= 25°C φ=0°, θ=0°	—	5.0	—	
			Ta= 50°C φ=0°, θ=0°	—	—	—	

Note:

<1> Duty =1/32

<2> All dots on static state

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1-6.Pin Connections:

Pin No.	Symbol	Level	Function									
1	Vss	---	0V(GND)									
2	Vdd	---	Power supply for logical circuit (5V).									
3	Vo	---	Power Supply For LCD Driving.									
4	A0	H/L	MPU control signal input. MPU inputs transfer data/instruction selection signal. ➢ D/I="H": data at the data bus <DB0:DB7> is display data. ➢ D/I="L": data at the data bus <DB0:DB7> is instruction data.									
5	E1	H/L	Master chip enable colock									
6	E2	H/L	Slave chip enable colock									
7	R/W	H/L	➢ When connect to the 68 type MPU. Connect to Read/Write control signal input terminal of 68 type MPU. <table border="1"><tr><td>R/W</td><td>H</td><td>L</td></tr><tr><td>Status</td><td>Read</td><td>Write</td></tr></table> ➢ When connect to the 80 type MPU. Connect to /WR signal input terminal of 80 type MPU. Active "L". The data on the data bus is fetch at the rising edge of this signal.	R/W	H	L	Status	Read	Write			
R/W	H	L										
Status	Read	Write										
8~15	DB0~DB7	H/L	Tri-state bilateral Data Bus. Transfers the data between MPU and module.									
16	/RES	H/L	Reset signal and interface type select terminal. The reset operation is performed by rise or fall edge of this signal. The input level after initialization selects the interface type of 68 or 80 type of MPU. <table border="1"><tr><td>MPU</td><td>Edge</td><td>Input level after initialization</td></tr><tr><td>68 Type</td><td>Rise</td><td>H</td></tr><tr><td>80 Type</td><td>Fall</td><td>L</td></tr></table>	MPU	Edge	Input level after initialization	68 Type	Rise	H	80 Type	Fall	L
MPU	Edge	Input level after initialization										
68 Type	Rise	H										
80 Type	Fall	L										
17~18	LED+,LED-		LED Backlight power supply									

1-7. Timing Characteristics:

1-7-1 Read / Write operation sequence

Timing parameters

VDD=5V±10%, VSS=0V, TA=0℃~+50℃

(68 Type MPU)

ITEM			SYMBOL	MIN	MAX	CONDITION	UNIT
Address Set Up time		A0, R/W	TAW6	20	—	—	ns
Address Hold Time			TAH6	10	—		
System Cycle Time			TCYC6	1000	—		
Enable Pulse Width	Read	E	TEW	100	—		
	Write			80	—		
Data Set Up Time		D0-D7	TDS6	80	—		
Data Hold Time			TDH6	10	—		
Access Time			TACC6	—	90	CL=100pF	
Output Disable Time			TCH6	10	60		

Note: Input signal rise time(tr)and fall time(tr) are less than 15ns.

(80 Type MPU)

ITEM		SYMBOL	MIN	MAX	CONDITION	UNIT
Address Set Up time	A0	TAW8	20	—		ns
Address Hold Time		TAH8	10	—		
System Cycle Time	/RD,/WR	TCYC8	1000	—		
Control Pulse Width		TCC	200	—		
Data Set Up Time	D0-D7	TDS8	80	—	CL=100pF	
Data Hold Time		TDH8	10	—		
Access Time		TACC8	—	90		
Output Disable Time		TCH8	10	60		

Note: Input signal rise time(tr)and fall time(tr) are less than 15ns.

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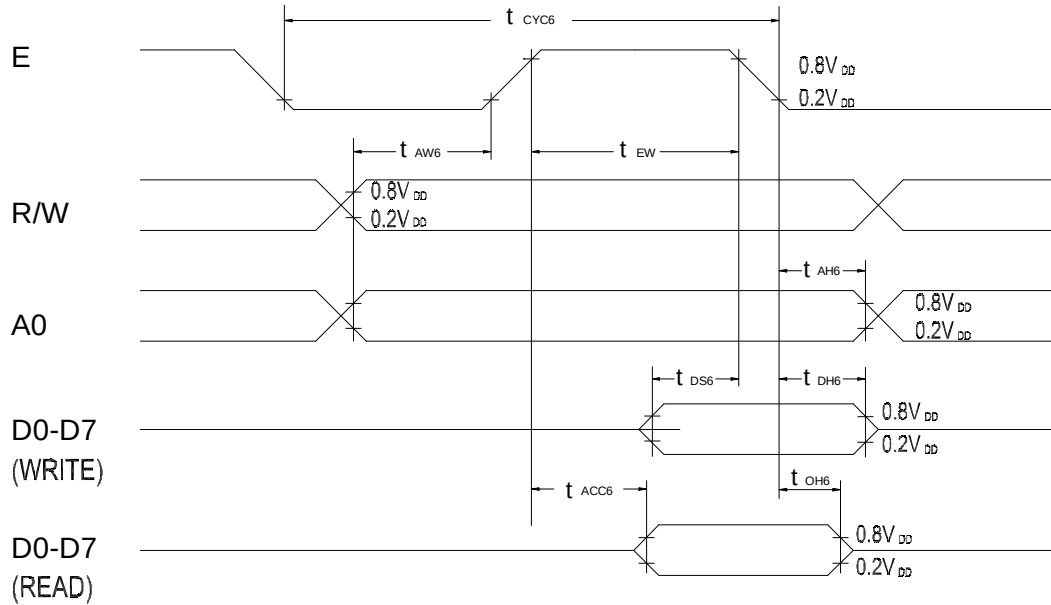
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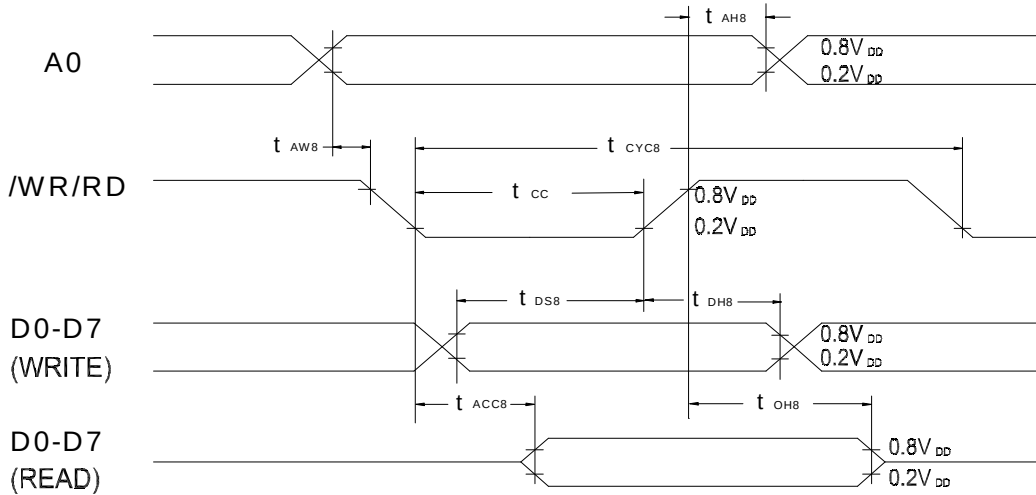
1-7-2 Timing Diagram

Bus Read/Write operation sequence(68 type MPU)



Bus Read / Write operation sequence

Bus Read/Write operation sequence(80 type MPU)



Bus Read / Write operation sequence

2.The characteristics and the reliability test

2-1.Electro-Optic Characteristics(LCM unit):

Condition:TEMP=(21±3)℃ HUM=(70±5)%RH

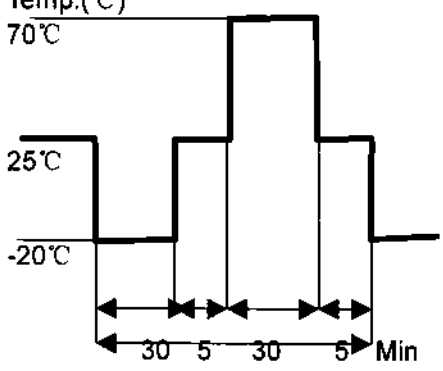
V_{DD}: 5.0V

NO	Item		Symbol	Min.	Typ.	Max.	Unit	Remarks
1	Operating Voltage		Vop		5.0		V	
2	Current Consumption		Is		1.08		mA	
3	Response Time		Ton		252		ms	
			Toff		63		ms	
4	Contrast		CR	3				
5	Viewing Angel (CR≥3.0)	12H	0 1		4		Deg.	
		6H	0 2		41			
		3H	0 3		45			
		9H	0 4		45			
6	Threshold Voltage		Vth		4.17		V	

2-2. Electro-Optic Characteristics(LED unit):

Ltem	Symbol	Min	Typ	Max	Unit
Forward Voltage	V _F	3.95	4.1		V
Power Dissipation	P _D		369		mW
Forward current	I _F		90		mA
Color		Yellow-green			

2-3. Reliability Test

No	Items	Test Condition	Equipmet	Test Result
1	High Temp Storage	Temp: $70 \pm 2^{\circ}\text{C}$ Time: 96h Restore: 24h	Tenny	Passed
2	Low Temp Storage	Temp: $-20 \pm 3^{\circ}\text{C}$ Time: 96h Restore: 24h	Tenny	Passed
3	High Temp Static drive	Temp: $50 \pm 2^{\circ}\text{C}$ VOP: 5V Time: 24h Restore: 24h	Tenny	Passed
4	Low Temp Static drive	Temp: $0 \pm 3^{\circ}\text{C}$ VOP: 5V Time: 24h Restore: 24h	Tenny	Passed
5	High Temp High Hum Storage	Temp: $40 \pm 2^{\circ}\text{C}$ Hum: 95%Rh Time: 96h Restore: 24h	Tenny	Passed
6	Thermal Shock	Temp: ($^{\circ}\text{C}$)  70$^{\circ}\text{C}$ 25$^{\circ}\text{C}$ -20$^{\circ}\text{C}$ 30 5 30 5 Min 5 Cycles Restore: 24h	Tenny	Passed

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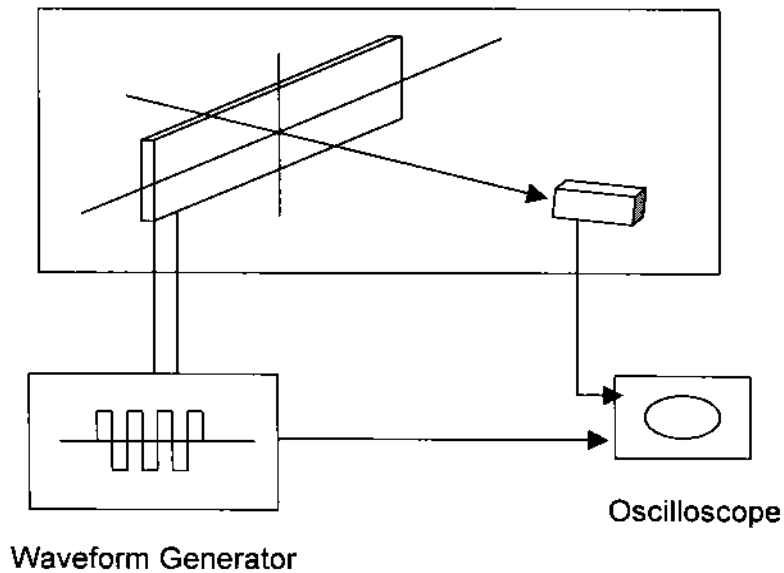
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3.The LCD measuring method and equipment

3-1.Threshold Voltage And Response Time Measuring

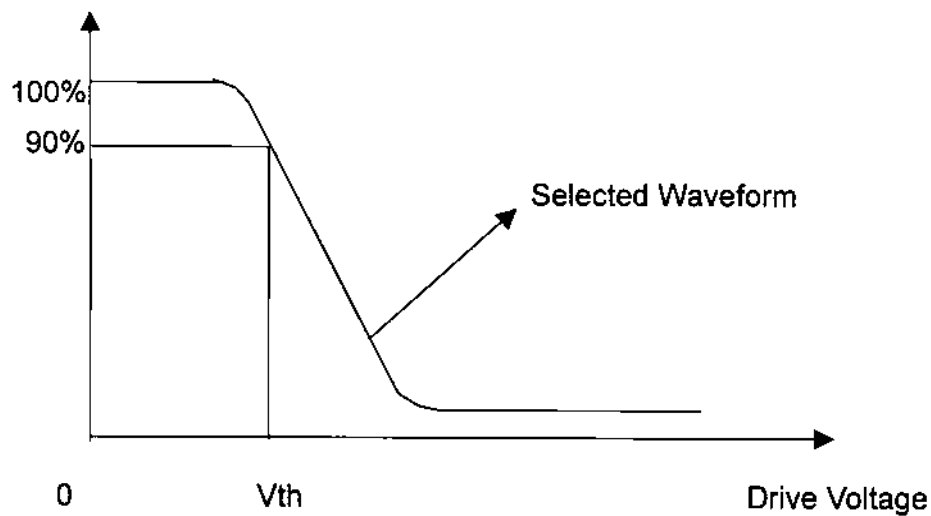
(1) Equipment



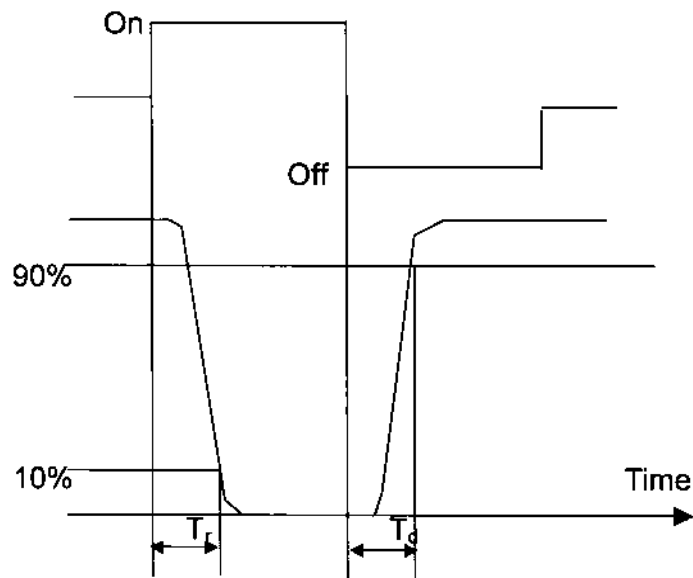
(2) Definition

A. Threshold Voltage (V_{th})

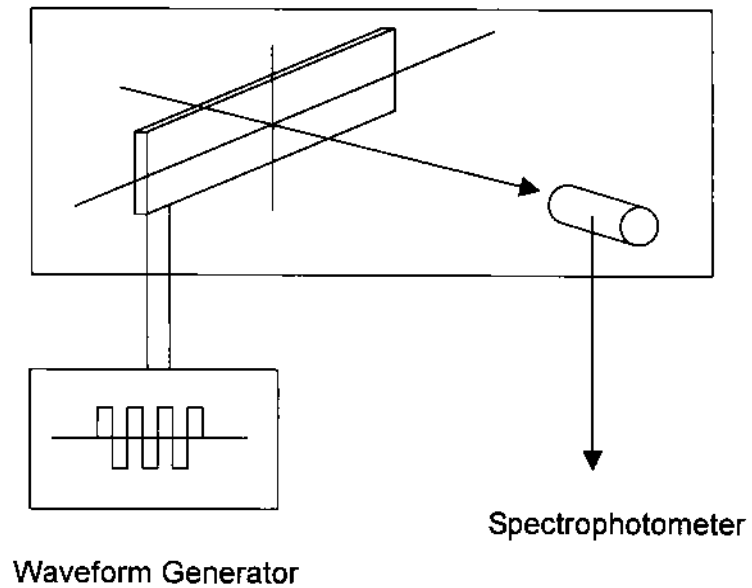
Brightness



B. Response Time



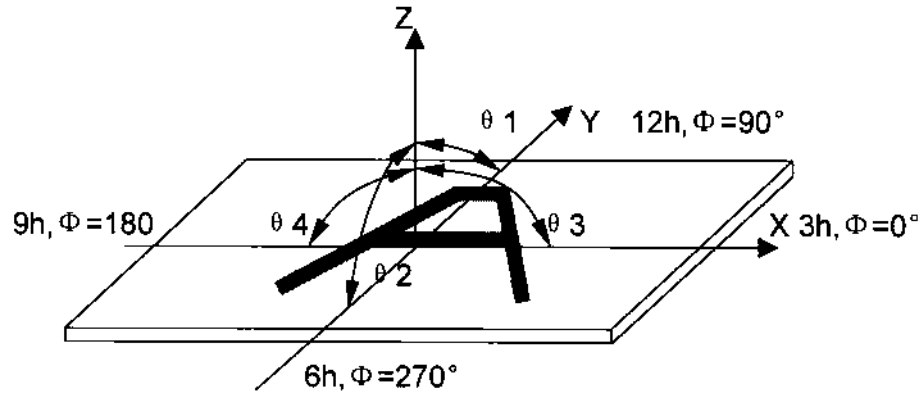
3-2. Contrast Measuring (1) Equipment



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(2)Definition:

A.Viewing Angle:



B. Contrast Ratio (Positive)

$$CR = \frac{\text{Brightness of non-selected wave-form}}{\text{Brightness of selected wave-form}}$$

3-3.Reliability Test:

Equipment : TENNY

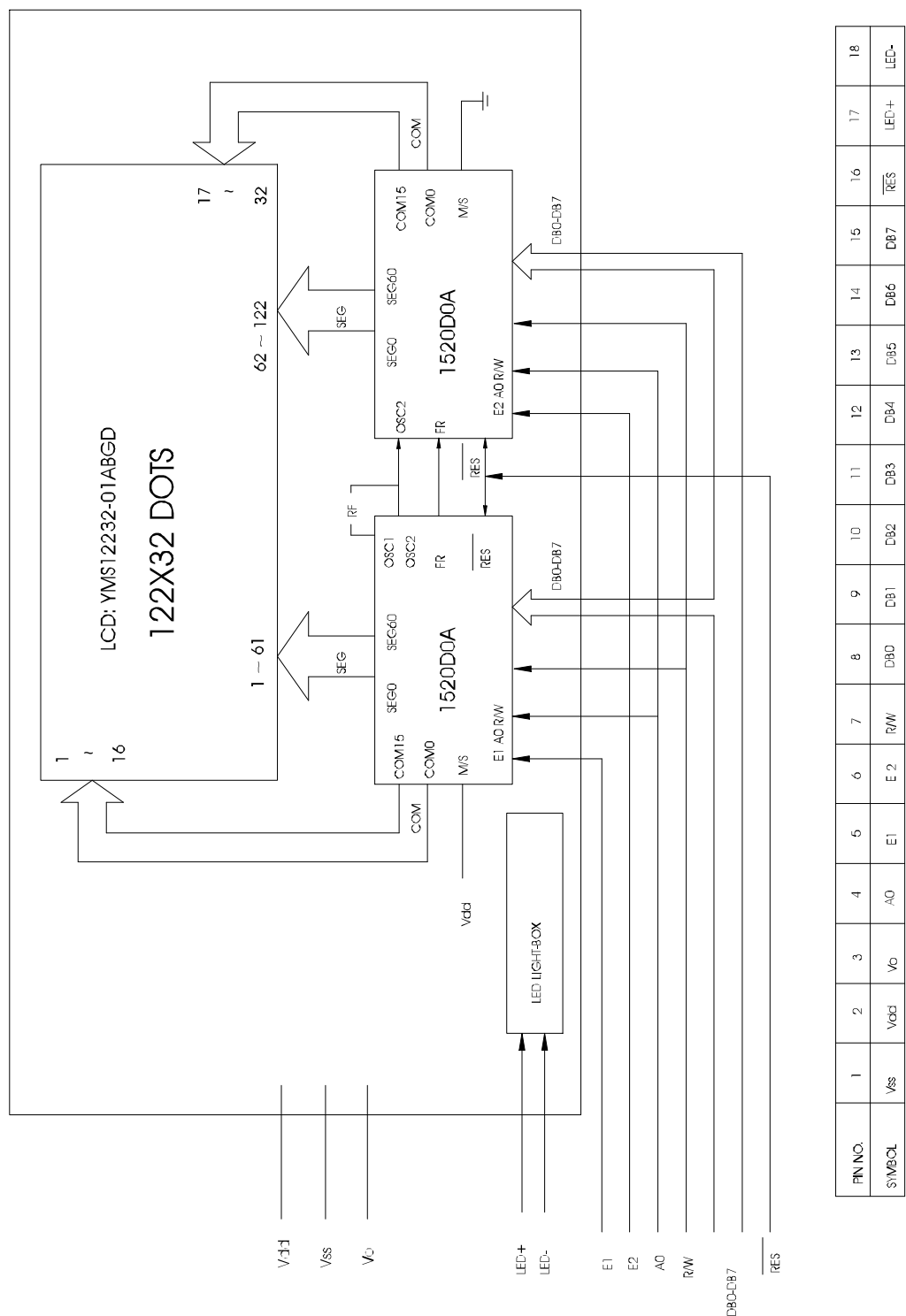
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4. Control Instruction

The following table is the instruction codes of the module.

Instruction	C o d e												Description
	A0	RD	WR	D7	D6	D5	D4	D3	D2	D1	D0		
Display On/Off	0	1	0	1	0	1	0	1	1	1	0/1	Whole Display On/Off. > 1:On > 0:Off	
Display Start Line	0	1	0	1	1	0	Display Start Address (1~31)					Determine the Display Line correspond to the COMn.	
Page Address Set	0	1	0	1	0	1	1	1	0	Page (0~3)		Set the Page of DDRAM to the Page Register.	
Column Address Set	0	1	0	0	Column Address (0~79)							Set the Column Address of Display Data RAM to the Column Register.	
Status Read	0	0	1	B U S Y	A D C	O N / O F F	R E S E T		0	0	0	0	Read the status.
Write Display Data	1	1	0	Write Data								Write the data to the Display Data RAM.	
Read Display Data	1	0	1	Read Data								Read the data from the Display Data RAM.	
ADC Select	0	1	0	1	0	1	0	0	0	0	0/1	Determine output direction of segment signal	
Static Drive On / Off	0	1	0	1	0	1	0	0	1	0	0/1	Select the Dynamic or Static Driving.	
Duty Ratio Select	0	1	0	1	0	1	0	1	0	0	0/1	Select the duty ratio.	
Read Modify Write	0	1	0	1	1	1	0	0	0	0	0	Increment the Column Address register when writing but no-change when reading.	
End	0	1	0	1	1	1	0	1	1	1	0	Release from the Read Modify Write Mode.	
Reset	0	1	0	1	1	1	0	0	0	1	0	Execute instruction reset	
Power Save (Dual Command)	0	1	0	1	0	1	0	1	1	1	0	Set the power save mode by selecting. Display Off and Static Driving On.	

5. Block Diagram



FIN NO.	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18
SYMBOL	VSS	VDD	V0	A0	E1	E2	RW	DB0	DB1	DB2	DB3	DB4	DB5	DB6	DB7	RES	LED+	LED-

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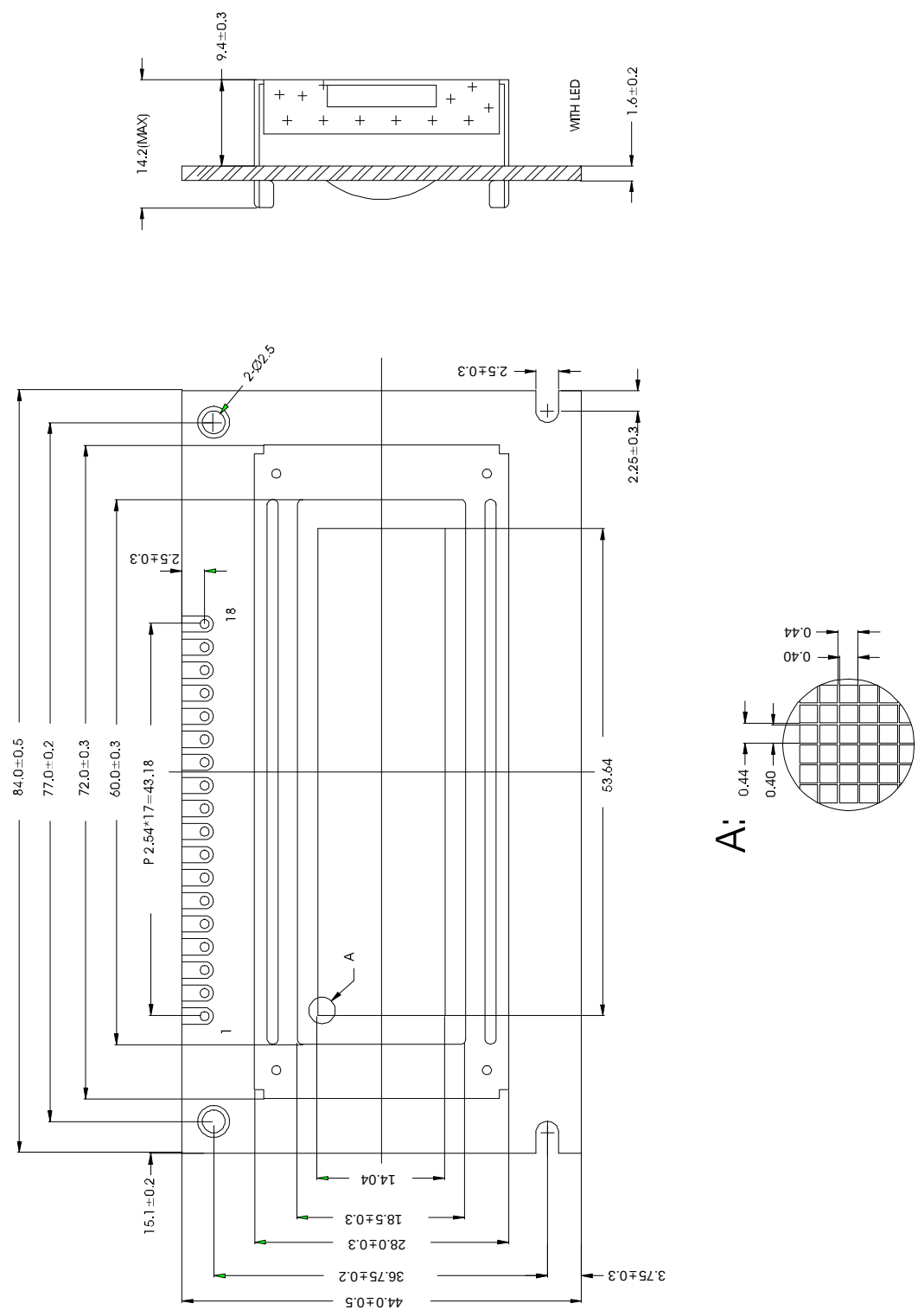
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6. Drawing



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