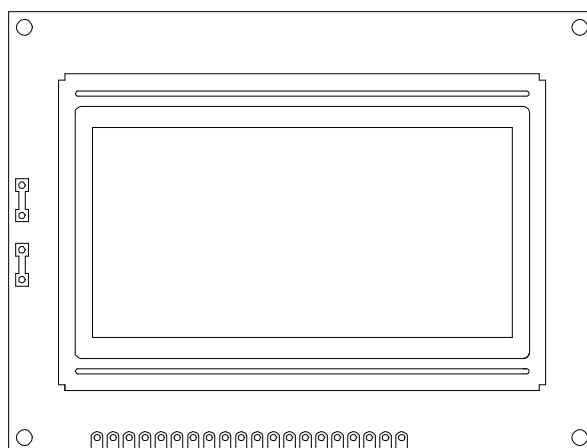




PRODUCT SPECIFICATION

HDM64GS12-R

128 x 64 GRAPHICS
LCD DISPLAY MODULE



HANTRONIX, INC. 10080 BUBB RD. CUPERTINO, CA 95014	Q.A.: JK	REV.: 1.0	HDM64GS12-R	SHEET 1 OF 13
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I . General Specifications

1. Features

- (1) Model: HDM64GS12_-R
- (2) Drive Method: 1/64 Duty, 1/9 Bias
- (3) Viewing Direction: 6:00 or 12:00 O'clock
- (4) Operating Temperature: Normal (0℃~50℃) or Extended (-20℃~70℃)
- (5) Storage Temperature: Normal (-20℃~70℃) or Extended (-30℃~85℃)
- (6) Display Type: FSTN, Gray or Yellow STN, Positive/ Negative
- (7) Backlight: None, LED (Yellow Green)
- (8) DC-to-DC Converter: Built-in
- (9) Controller: Samsung KS108B

2. Mechanical Data

- (1) Resolution: 128 dots x 64 dots
- (2) Module Size: 93.0 l * 70.0 w mm
- (3) Bessel Size: 77.2 l * 50.2 w mm
- (4) Viewing Area: 72.0 l * 40.0 w mm
- (5) Dot Size: 0.48 w * 0.48 h mm
- (6) Dot Pitch: 0.52 w * 0.52 h mm

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3. Pin Connections:

Pin No.	Symbol	Function
1	VSS	Ground(0v)
2	VDD	Logic Supply Voltage(+5.0v)
3	V0	LCD Driver Voltage Input(+9.7v)
4	RS	Data Or Instruction
5	R/W	Read/Write Select
6	E	Enable Signal
7~14	DB0~DB7	Data Bus Line
15	Cs1	Chip Selection(Segment Driver 1)
16	Cs2	Chip Selection(Segment Driver 2)
17	RSTB	Reset Signal
18	Vout	Dc-Dc-Converter Output
19-20	A,K	LED Backlight

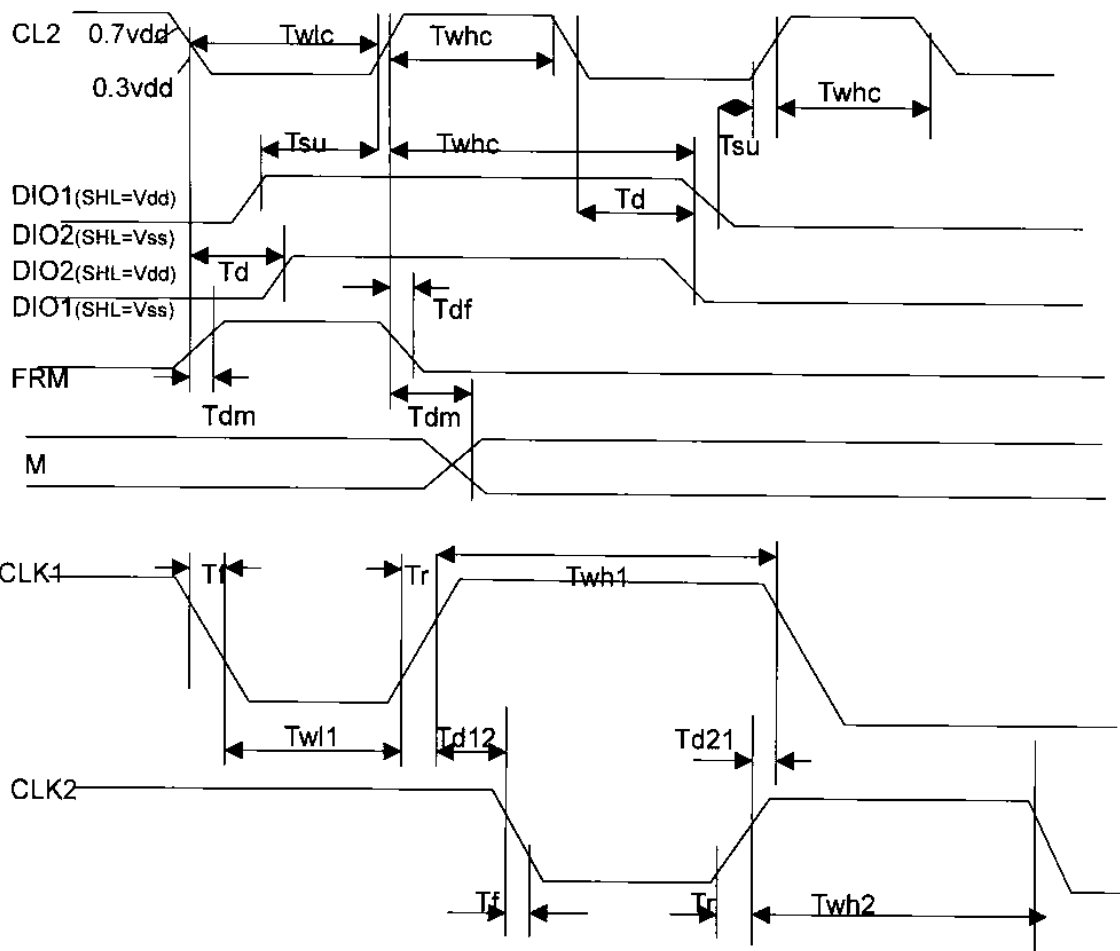
4. Timing Characteristics: (VDD=5V $\pm$ 10%)

(1) Common Driver :

Characteristic	Symbol	Min.	Typ.	Max.	Unit
Data Setup Time	Tsu	20	--	--	us
Data Hold Time	Tdh	40	--	--	
Data Delay Time	Td	5	--	--	
FRM Delay Time	Tdf	-2	--	2	
M Delay Time	Tdm	-2	--	2	
Cl2 Low Level Width	Twlc	35	--	--	
Cl2 High Level Width	Twhc	35	--	--	ns
Clk1 Low Level Width	Twl1	700	--	--	
Clk2 Low Level Width	Twl2	700	--	--	

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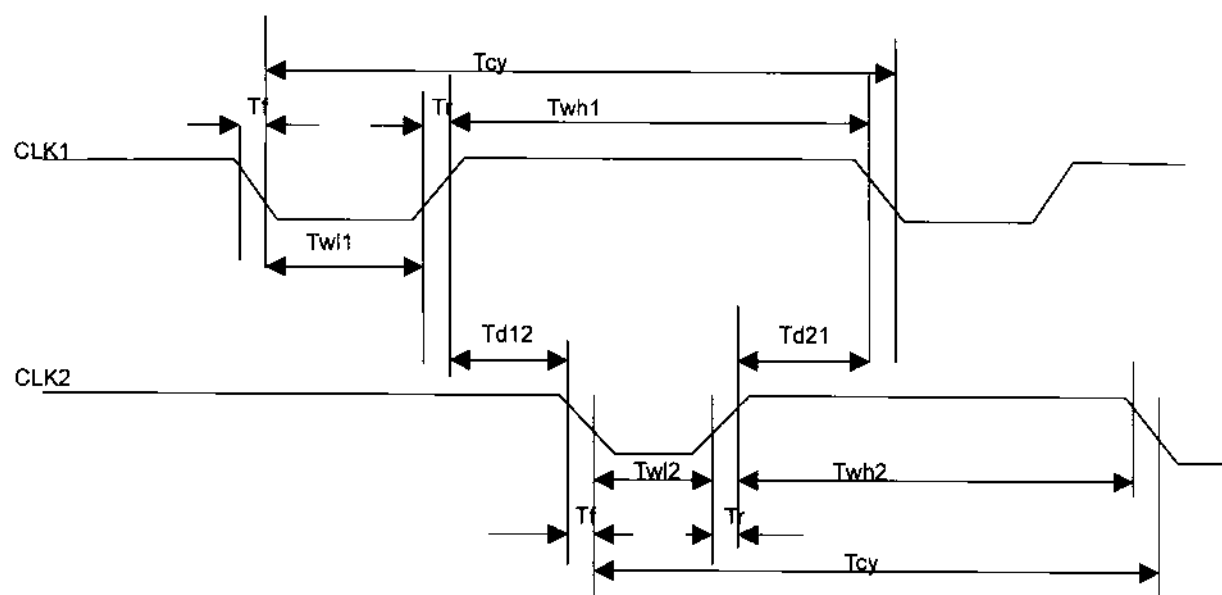
Clk1 High Level Width	Twh1	2100	--	--	ns
Clk2 High Level Width	Twh2	2100	--	--	
Clk1-Clk2 Phase Difference	Td12	700	--	--	
Clk2-Clk1 Phase Difference	Td21	700	--	--	
Clk1,Clk2 Rise/Fall Time	Tr/Tf	--	--	150	



(2) Segment Driver:

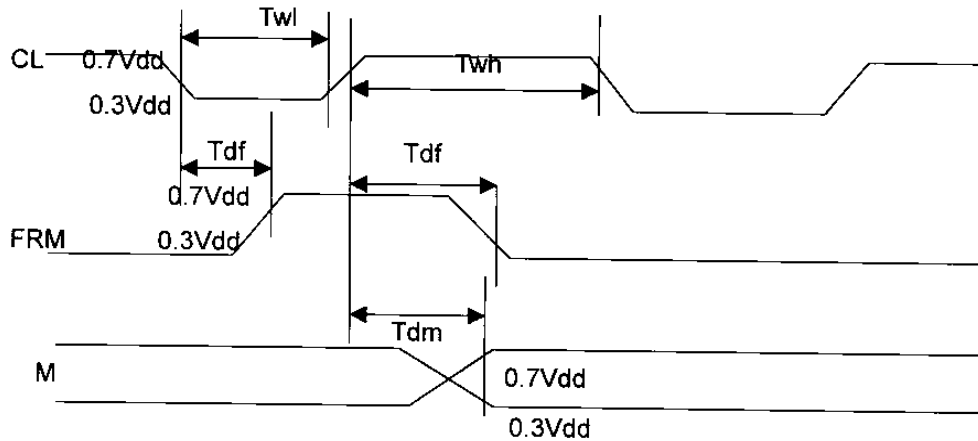
A. Clock Timing:

Characteristic	Symbol	Min.	Typ.	Max.	Unit
Clk1, Clk2 Cycle Time	Tcy	2.5	--	20	μ s
Clk1 "Low" Level Width	Twl1	625	--	--	ns
Clk2 "Low" Level Width	Twl2	625	--	--	
Clk1 "High" Level Width	Twh1	1875	--	--	
Clk2 "High" Level Width	Twh2	1875	--	--	
Clk1-Clk2 Phase Difference	Td12	625	--	--	
Clk2-Clk1 Phase Difference	Td21	625	--	--	
Clk1, Clk2 Rise Time	Tr	--	--	150	
Clk1, Clk2 Fall Time	Tf	--	--	150	



B. Display Control Timing

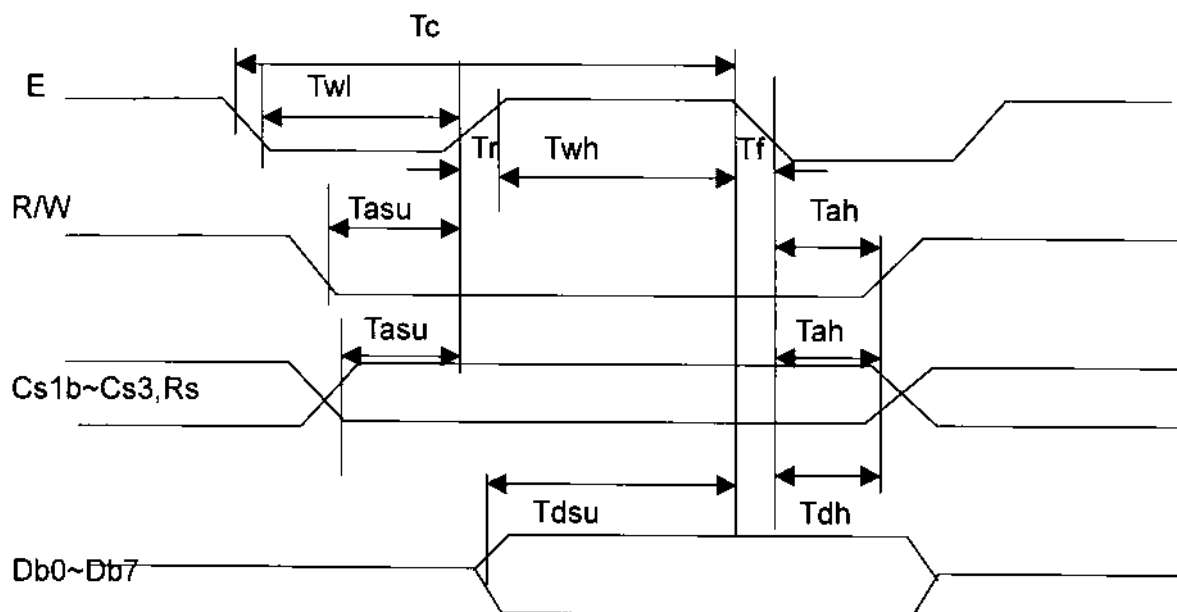
Characteristic	Symbol	Min.	Typ.	Max.	Unit
Frm Delay Time	Tdf	-2	--	+2	μ s
M Delay Time	Tdm	-2	--	+2	μ s
CL"Low" Level Width	Twl	35	--	--	μ s
CL"High" Level Width	Twh	35	--	--	μ s



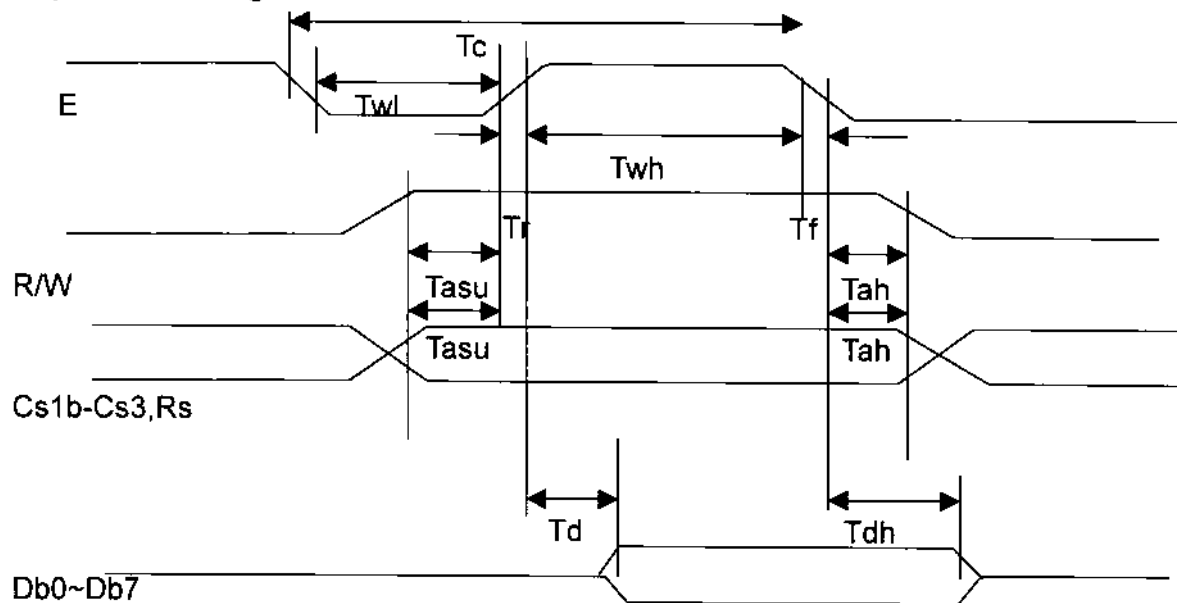
C. Mpu Interface:

Characteristic	Symbol	Min.	Typ.	Max.	Unit
E Cycle	Tc	1000	--	--	ns
E High Level Width	Twh	450	--	--	
E Low Level Width	Twl	450	--	--	
E Rise Time	Tr	--	--	25	
E Fall Time	Tf	--	--	25	
Address Set-Up Time	Tasu	140	--	--	
Address Hold Time	Tah	10	--	--	
Data Set-Up Time	Tdsu	200	--	--	
Data Delay Time	Td	--	--	320	
Data Hold Time(Write)	Tdhw	10	--	--	
Data Hold Time(Read)	Tdhr	20	--	--	

Mpu Write Timing:



Mpu Read Timing:



II .The Characteristics and The Reliability Test

1.Electro-Optic Characteristics:

Condition:TEMP=(23±3)℃ Hum=(70±5)%RH

V_{dd}: 5.0V

F_{osc}: 450KHZ

NO	Item		Symbol	Min	Typ.	Max	Unit	Condition
1	Supply Voltage(Logic)		V _{dd} -V _{ss}	4.5	5.0	5.5	V	
2	Supply Current (Logic)		I _{dd}		2.30		mA	V _{dd} =5V
3	LCD Operating Voltage		V _{dd} -V ₀		9.9		V	0℃
				9.2	9.7	10.2	V	25℃
					9.5		V	50℃
4	Response Time		T _{on}		72		ms	
			T _{off}		202		ms	
5	Contrast		CR	3				
6	Viewing Angel	12H	θ 1		55		De g.	(CR≥ 3.0)
		6H	θ 2		68			
		3H	θ 3		60			
		9H	θ 4		60			
7	LCD Threshold Voltage		V _{th}		1.22		V	25℃

2. Absolute Maximum Ratings(LED unit):

Item	Symbol	Typ.	Rating.	Unit	Condition
Forward Current	I _{FM}		720	mA	T _a =25℃
Peak forward current	I _{FP}		2160	mA	1 msec plus 10% duty cycle
Reverse Voltage	V _R		8	V	T _a =25℃
Power Dissipation	P _D		3600	mW	T _a =25℃

3. Electrical-optical Characteristics(LED unit):

Ltem	Symbol	Min	Typ	Max	Unit	Condition
Forward	V _F	3.9	4.1	4.3	V	I _F =360mA
Reverse current	I _R			3600	uA	V _R =8V
Luminous	L _V	184	230		cd/m ²	I _F =360mA
Color	Yellow/Green					

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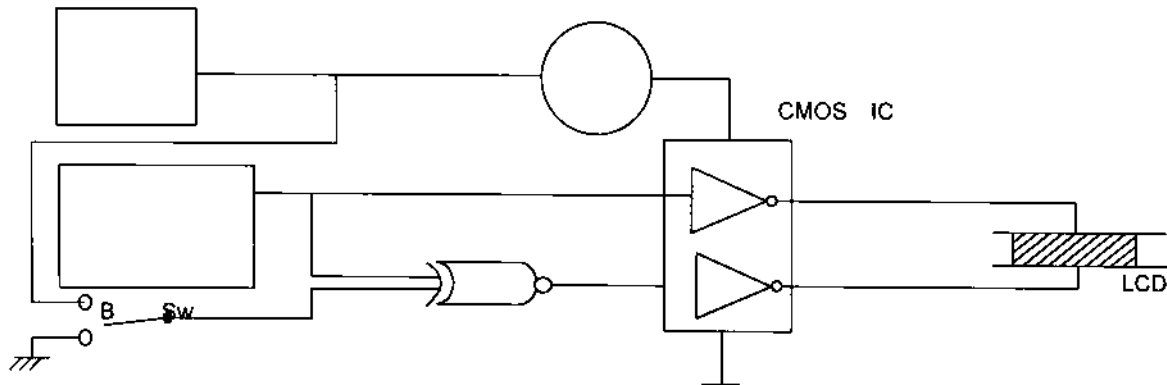
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III. The LCD Measuring Method and Equipment

1. Current Consumption Measuring

(1) Equipment



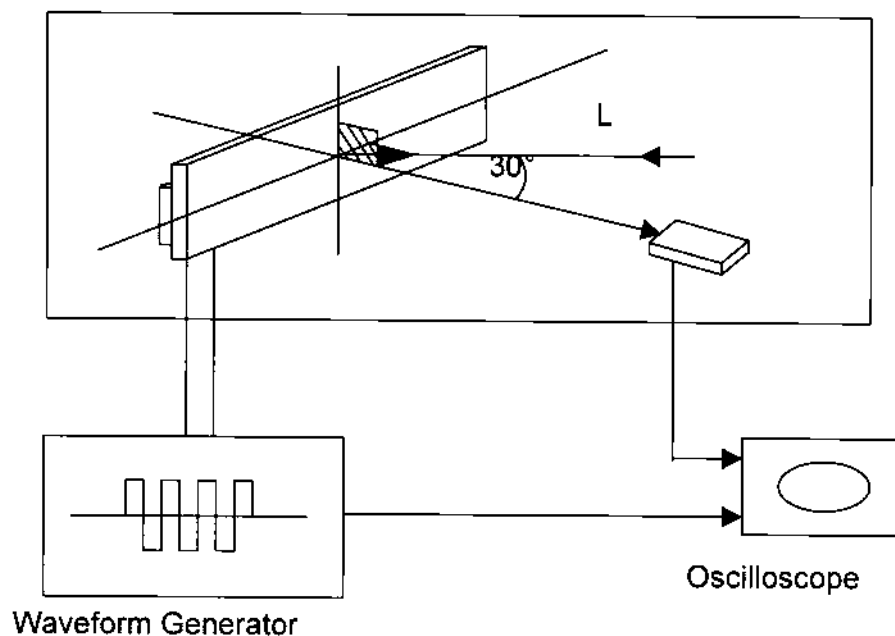
(2) Condition

Operating Frequency: 128HZ

Operating Voltage (RMS): Selected Voltage

2. Threshold Voltage and Response Time Measuring

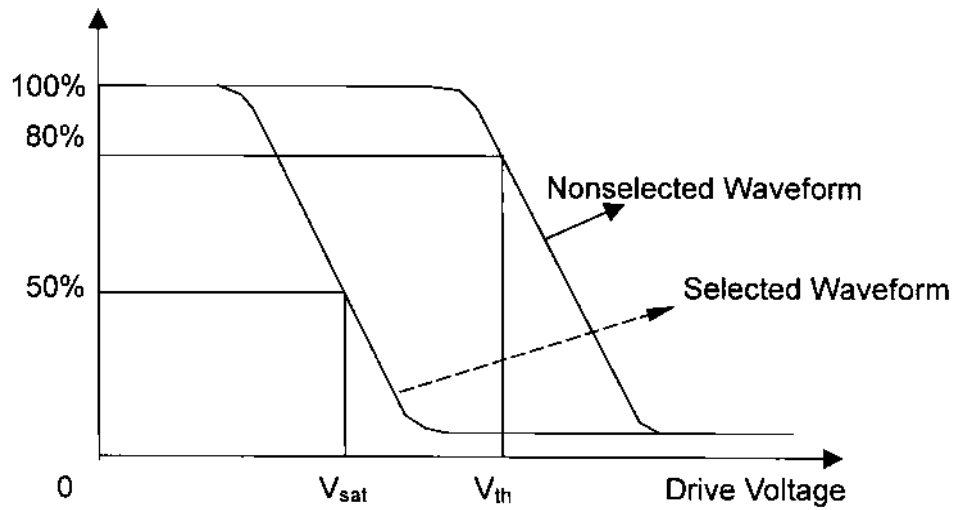
(1) Equipment



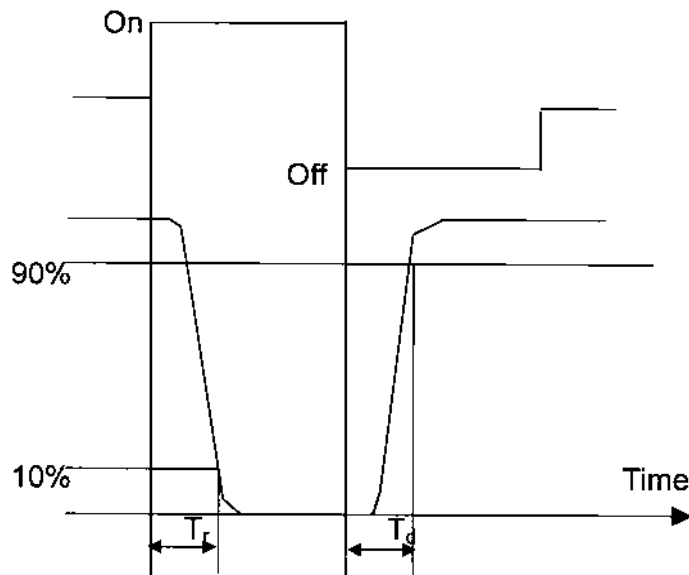
(2) Definition

A. Threshold Voltage (V_{th})

Brightness



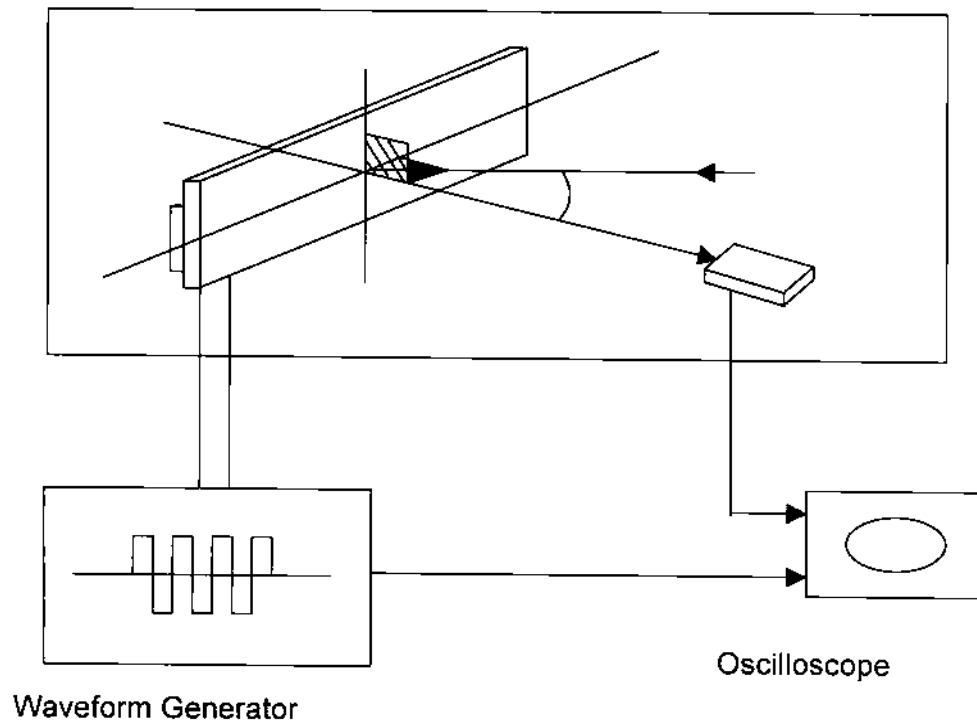
B. Response Time



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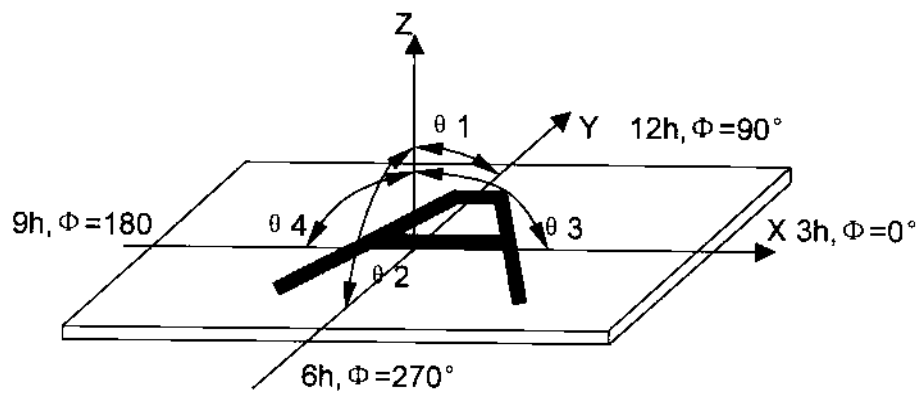
3. Contrast Measuring

(1) Equipment



(2) Definition:

A. Viewing Angle:

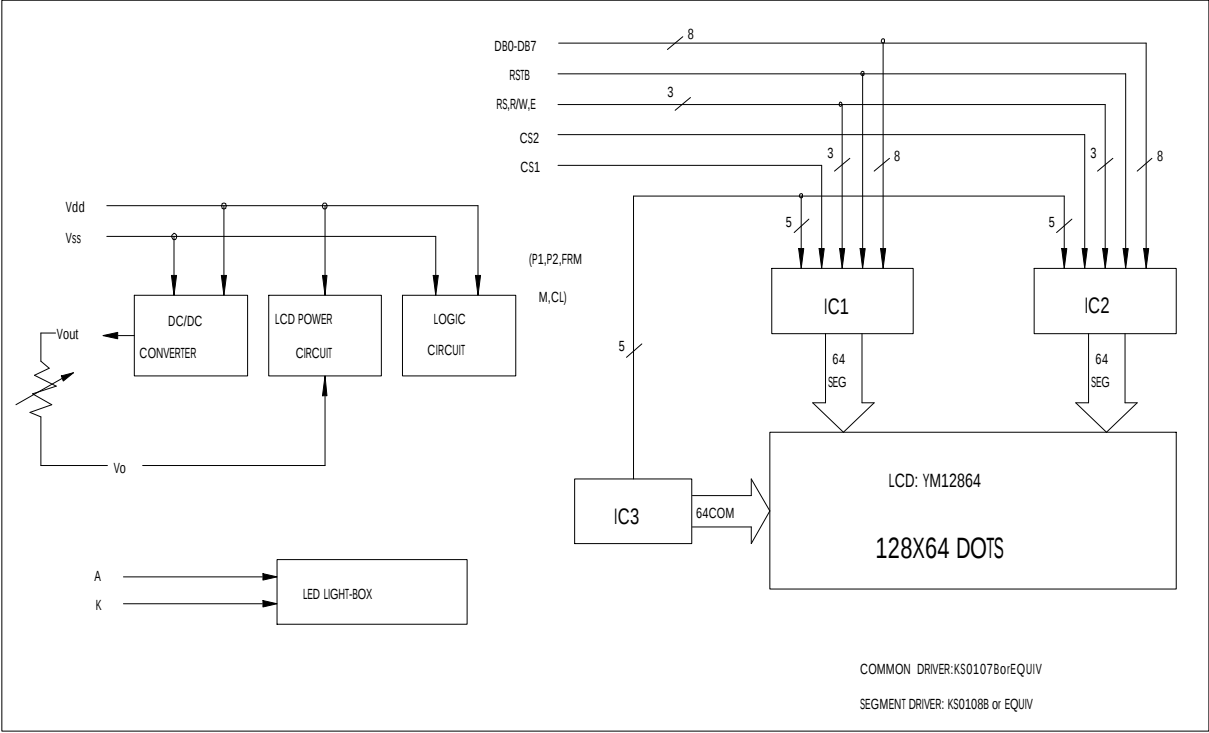


B. Contrast Ratio (Positive)

$$CR = \frac{\text{Brightness of non-selected wave-form}}{\text{Brightness of selected wave-form}}$$

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IV. Block Diagram



PIN NO.	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
SYMBOL	Vss	Vdd	Vo	RS	R/W	E	DB0	DB1	DB2	DB3	DB4	DB5	DB6	DB7	CS1	CS2	RSTB	Vout	A	K

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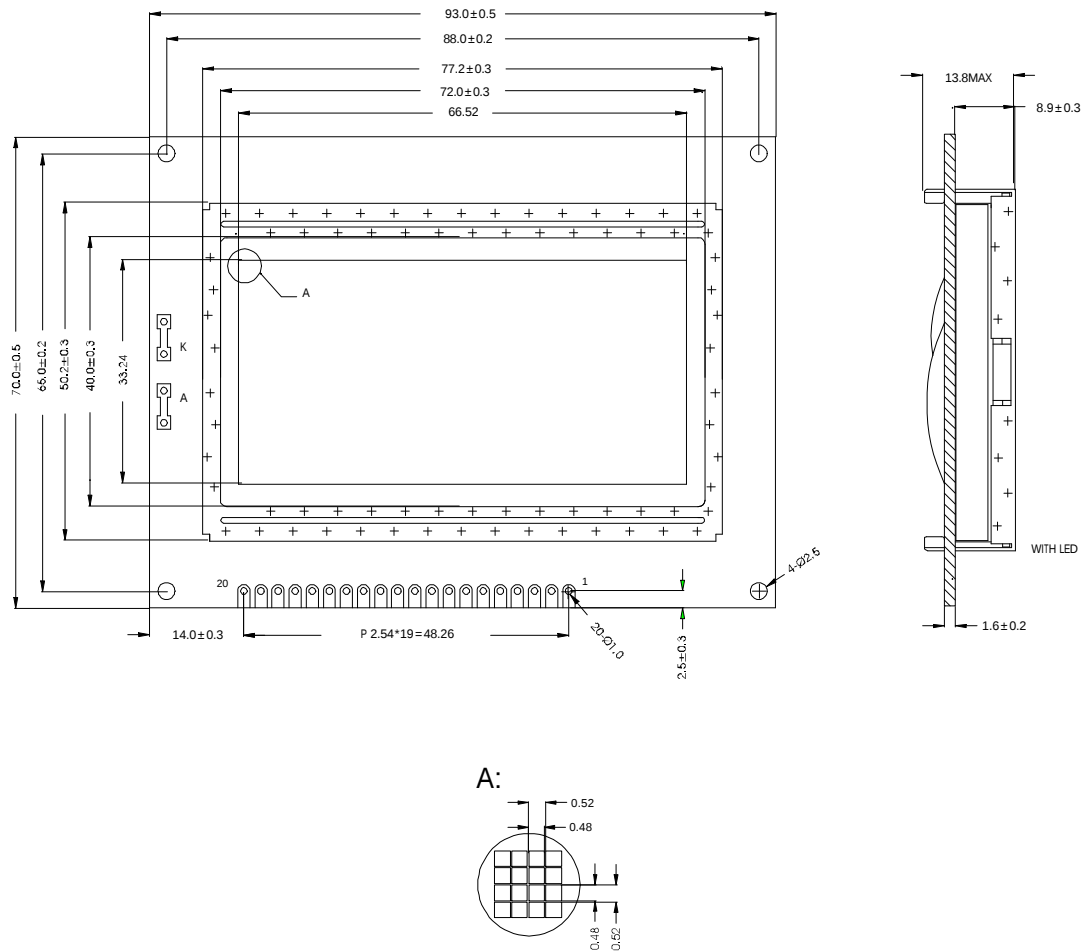
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V. Drawing



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