# HD74LV1GT04A

## Inverter / CMOS Logic Level Shifter

REJ03D0117-0900
Rev.9.00
Mar 21, 2008



## Description

The HD74LV1GT04A has an inverter in a 5 pin package. The input protection circuitry on this device allows over voltage tolerance on the input, allowing the device to be used as a logic–level translator from 3.0 V CMOS Logic to 5.0 V CMOS Logic or from 1.8 V CMOS logic to 3.0 V CMOS Logic while operating at the high-voltage power supply. Low voltage and high-speed operation is suitable for the battery powered products (e.g., notebook computers), and the low power consumption extends the battery life.

## Features

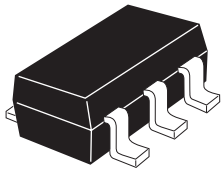
- The basic gate function is lined up as Renesas uni logic series.
- Supplied on emboss taping for high-speed automatic mounting.
- TTL compatible input level.
  Supply voltage range : 3.0 to 5.5 V
  Operating temperature range : –40 to +85°C
- Logic-level translate function
  3.0 V CMOS logic → 5.0 V CMOS logic (@$V_{CC}$ = 5.0 V)
  1.8 V or 2.5 V CMOS logic → 3.3 V CMOS logic (@$V_{CC}$ = 3.3 V)
- All inputs $V_{IH}$ (Max.) = 5.5 V (@$V_{CC}$ = 0 V to 5.5 V)
  All outputs $V_O$ (Max.) = 5.5 V (@$V_{CC}$ = 0 V)
- Output current ±6 mA (@$V_{CC}$ = 3.0 V to 3.6 V), ±12 mA (@$V_{CC}$ = 4.5 V to 5.5 V)
- All the logical input has hysteresis voltage for the slow transition.
- Ordering Information

| Part Name | Package Type | Package Code (Previous Code) | Package Abbreviation | Taping Abbreviation (Quantity) |
|---|---|---|---|---|
| HD74LV1GT04ACME | CMPAK–5 pin | PTSP0005ZC-A (CMPAK-5V) | CM | E (3000 pcs/reel) |
| HD74LV1GT04AVSE | VSON–5 pin | PUSN0005KA-A (TNP-5DV) | VS | E (3000 pcs/reel) |

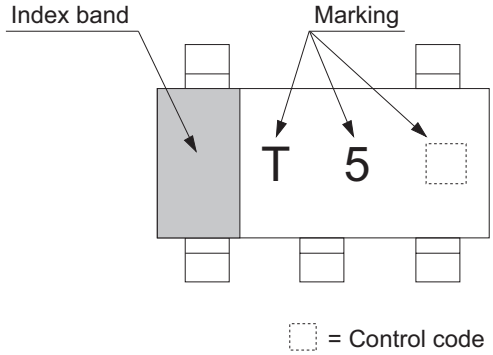
Note: Please consult the sales office for the above package availability.

## Outline and Article Indication

• HD74LV1GT04A



CMPAK–5



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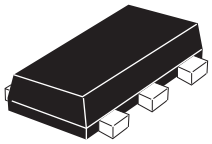
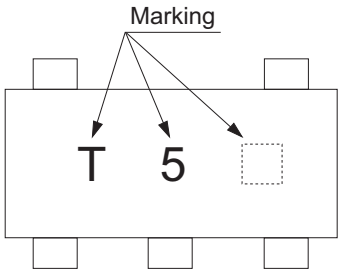


VSON–5



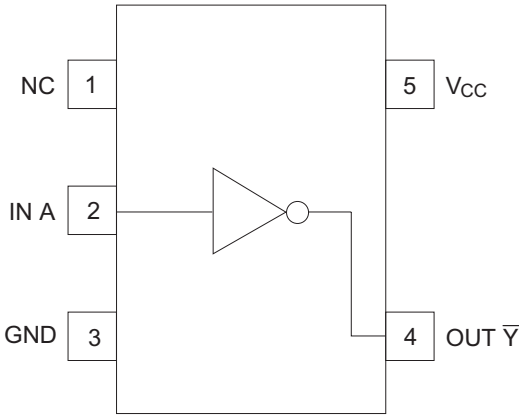

= Control code

## Function Table

| Input A | Output $\overline{Y}$ |
|---|---|
| H | L |
| L | H |

H : High level
L : Low level

## Pin Arrangement




(Top view)

## Absolute Maximum Ratings

| Item | Symbol | Ratings | Unit | Test Conditions |
|---|---|---|---|---|
| Supply voltage range | $V_{CC}$ | –0.5 to 7.0 | V | |
| Input voltage range [*1] | $V_I$ | –0.5 to 7.0 | V | |
| Output voltage range [*1, 2] | $V_O$ | –0.5 to $V_{CC}$ + 0.5 | V | Output : H or L |
| | | –0.5 to 7.0 | | $V_{CC}$ : OFF |
| Input clamp current | $I_{IK}$ | –20 | mA | $V_I$ < 0 |
| Output clamp current | $I_{OK}$ | ±50 | mA | $V_O$ < 0 or $V_O$ > $V_{CC}$ |
| Continuous output current | $I_O$ | ±25 | mA | $V_O$ = 0 to $V_{CC}$ |
| Continuous current through $V_{CC}$ or GND | $I_{CC}$ or $I_{GND}$ | ±50 | mA | |
| Maximum power dissipation at Ta = 25°C (in still air) [*3] | $P_T$ | 200 | mW | |
| Storage temperature | Tstg | –65 to 150 | °C | |

Notes: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore no two of which may be realized at the same time.

1. The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.
2. This value is limited to 5.5 V maximum.
3. The maximum package power dissipation was calculated using a junction temperature of 150°C.

## Recommended Operating Conditions

| Item | Symbol | Min | Max | Unit | Conditions |
|---|---|---|---|---|---|
| Supply voltage range | $V_{CC}$ | 3.0 | 5.5 | V | |
| Input voltage range | $V_I$ | 0 | 5.5 | V | |
| Output voltage range | $V_O$ | 0 | $V_{CC}$ | V | |
| Output current | $I_{OL}$ | — | 6 | mA | $V_{CC}$ = 3.0 to 3.6 V |
| | | — | 12 | | $V_{CC}$ = 4.5 to 5.5 V |
| | $I_{OH}$ | — | –6 | | $V_{CC}$ = 3.0 to 3.6 V |
| | | — | –12 | | $V_{CC}$ = 4.5 to 5.5 V |
| Input transition rise or fall rate | Δt / Δv | 0 | 100 | ns / V | $V_{CC}$ = 3.0 to 3.6 V |
| | | 0 | 20 | | $V_{CC}$ = 4.5 to 5.5 V |
| Operating free-air temperature | $T_a$ | –40 | 85 | °C | |

Note: Unused or floating inputs must be held high or low.

## Electrical Characteristic

- Ta = –40 to 85°C

| Item | Symbol | $V_{CC}$ (V) * | Min | Typ | Max | Unit | Test condition |
|---|---|---|---|---|---|---|---|
| Input voltage | $V_{IH}$ | 3.0 to 3.6 | 1.5 | — | — | V | |
| | | 4.5 to 5.5 | 2.0 | — | — | | |
| | $V_{IL}$ | 3.0 to 3.6 | — | — | 0.6 | | |
| | | 4.5 to 5.5 | — | — | 0.8 | | |
| Hysteresis voltage | $V_H$ | 3.3 | — | 0.10 | — | V | $V_T^+ - V_T^-$ |
| | | 5.0 | — | 0.15 | — | | |
| Output voltage | $V_{OH}$ | Min to Max | $V_{CC}$–0.1 | — | — | V | $I_{OH}$ = –50 µA |
| | | 3.0 | 2.48 | — | — | | $I_{OH}$ = –6 mA |
| | | 4.5 | 3.8 | — | — | | $I_{OH}$ = –12 mA |
| | $V_{OL}$ | Min to Max | — | — | 0.1 | | $I_{OL}$ = 50 µA |
| | | 3.0 | — | — | 0.44 | | $I_{OL}$ = 6 mA |
| | | 4.5 | — | — | 0.55 | | $I_{OL}$ = 12 mA |
| Input current | $I_{IN}$ | 0 to 5.5 | — | — | ±1 | µA | $V_{IN}$ = 5.5 V or GND |
| Quiescent supply current | $I_{CC}$ | 5.5 | — | — | 10 | µA | $V_{IN}$ = $V_{CC}$ or GND, $I_O$ = 0 |
| | $\Delta I_{CC}$ | 5.5 | — | — | 1.5 | mA | One input $V_{IN}$ = 3.4 V, other input $V_{CC}$ or GND |
| Output leakage current | $I_{OFF}$ | 0 | — | — | 5 | µA | $V_{IN}$ or $V_O$ = 0 to 5.5 V |
| Input capacitance | $C_{IN}$ | 5.0 | — | 3.0 | — | pF | $V_{IN}$ = $V_{CC}$ or GND |

Note: For conditions shown as Min or Max, use the appropriate values under recommended operating conditions.

## Switching Characteristics

- $V_{CC}$ = 3.3 ± 0.3 V

| Item | Symbol | Ta = 25°C | | | Ta = –40 to 85°C | | Unit | Test Conditions | FROM (Input) | TO (Output) |
|---|---|---|---|---|---|---|---|---|---|---|
| | | Min | Typ | Max | Min | Max | | | | |
| Propagation delay time | $t_{PLH}$ | — | 6.5 | 12.0 | 1.0 | 14.0 | ns | $C_L$ = 15 pF | A or B | $\overline{Y}$ |
| | $t_{PHL}$ | — | 11.0 | 15.0 | 1.0 | 17.0 | | $C_L$ = 50 pF | | |

- $V_{CC}$ = 5.0 ± 0.5 V

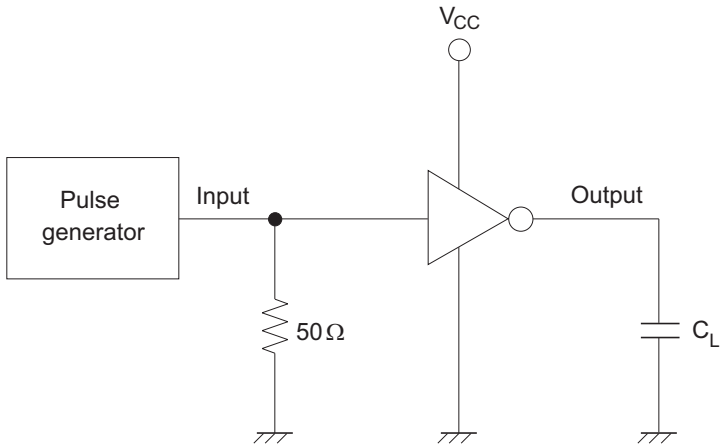
| Item | Symbol | Ta = 25°C | | | Ta = –40 to 85°C | | Unit | Test Conditions | FROM (Input) | TO (Output) |
|---|---|---|---|---|---|---|---|---|---|---|
| | | Min | Typ | Max | Min | Max | | | | |
| Propagation delay time | $t_{PLH}$ | — | 5.0 | 7.0 | 1.0 | 8.0 | ns | $C_L$ = 15 pF | A | $\overline{Y}$ |
| | $t_{PHL}$ | — | 8.0 | 10.5 | 1.0 | 12.0 | | $C_L$ = 50 pF | | |

## Operating Characteristics

- $C_L$ = 50 pF

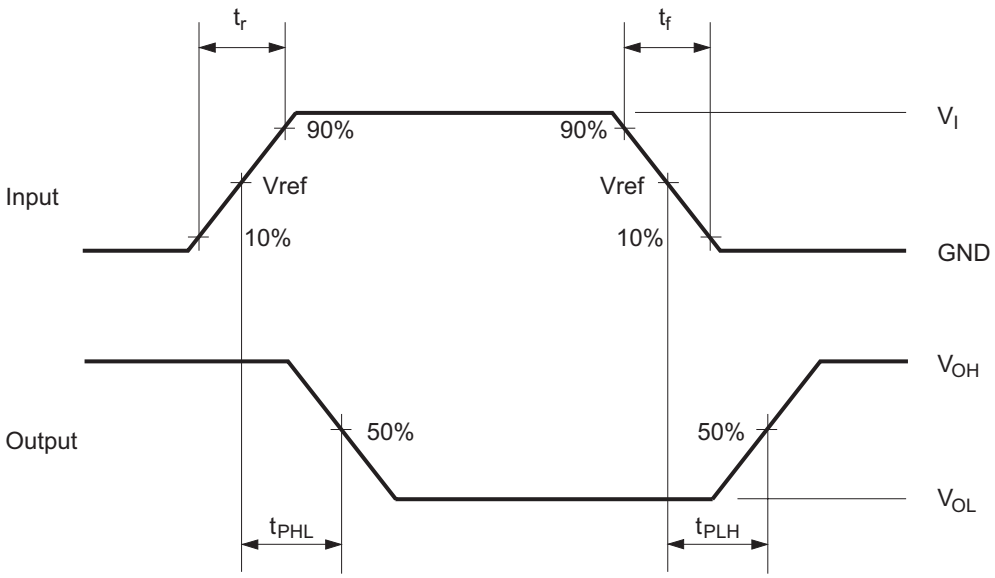
| Item | Symbol | $V_{CC}$ (V) | Ta = 25°C | | | Unit | Test Conditions |
|---|---|---|---|---|---|---|---|
| | | | Min | Typ | Max | | |
| Power dissipation capacitance | $C_{PD}$ | 5.0 | — | 10.0 | — | pF | f = 10 MHz |

## Test Circuit




Note: $C_L$ includes probe and jig capacitance.

## Waveforms

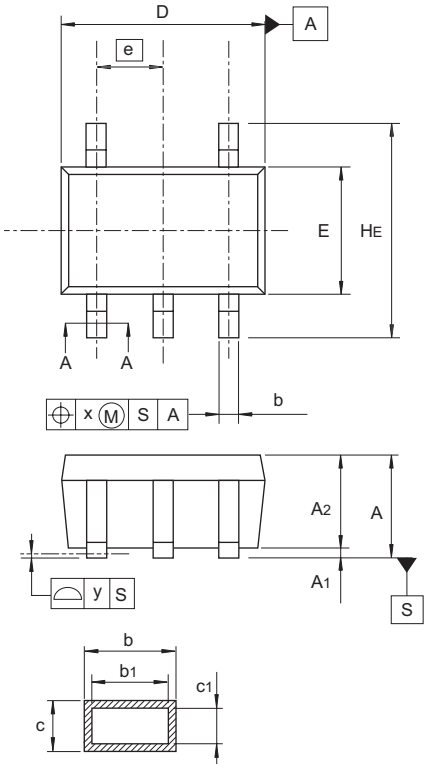



| $V_{CC}$ (V) | INPUTS | | Vref |
|---|---|---|---|
| | $V_I$ | $t_r$ / $t_f$ | |
| 3.3±0.3 | 2.5 V | ≤ 3.0 ns | 50% |
| 5.0±0.5 | 3 V | ≤ 3.0 ns | 1.5 V |

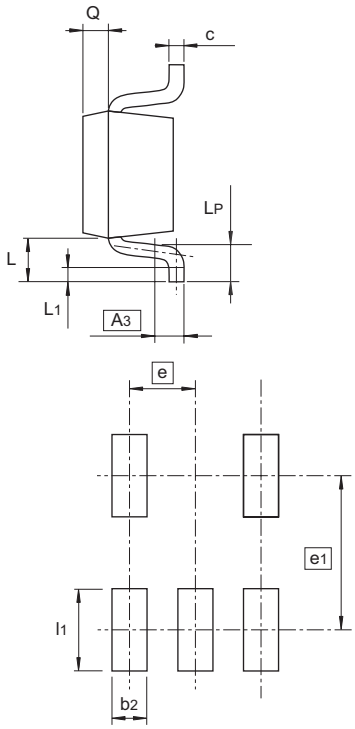
Notes: 1. Input waveform : PRR ≤ 1 MHz, Zo = 50 Ω.
2. The output are measured one at a time with one transition per measurement.

## Package Dimensions

| JEITA Package Code | RENESAS Code | Previous Code | MASS[Typ.] |
|---|---|---|---|
| SC-88A | PTSP0005ZC-A | CMPAK-5 / CMPAK-5V | 0.006g |



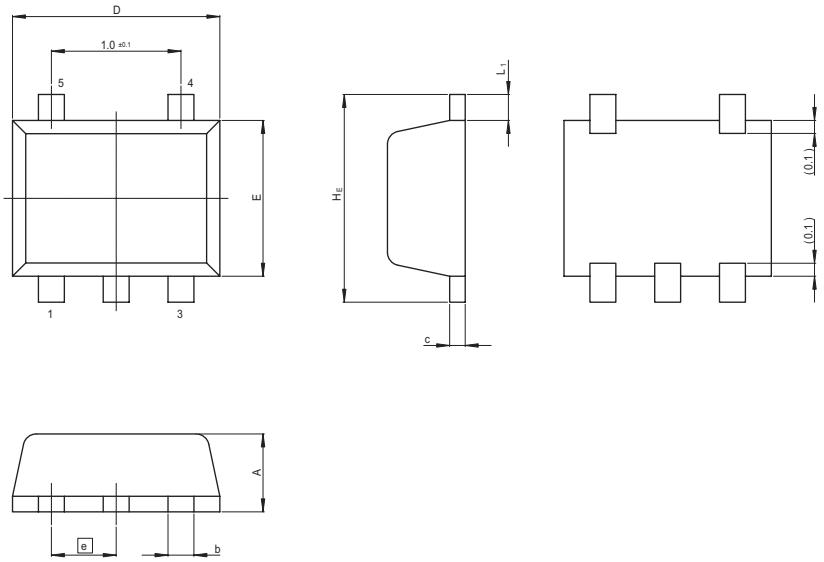

A-A Section




Pattern of terminal position areas

| Reference Symbol | Dimension in Millimeters | | |
|---|---|---|---|
| | Min | Nom | Max |
| A | 0.8 | — | 1.1 |
| $A_1$ | 0 | — | 0.1 |
| $A_2$ | 0.8 | 0.9 | 1.0 |
| $A_3$ | — | 0.25 | — |
| b | 0.15 | 0.22 | 0.3 |
| $b_1$ | — | 0.2 | — |
| c | 0.1 | 0.13 | 0.15 |
| $c_1$ | — | 0.11 | — |
| D | 1.8 | 2.0 | 2.2 |
| E | 1.15 | 1.25 | 1.35 |
| e | — | 0.65 | — |
| $H_E$ | 1.8 | 2.1 | 2.4 |
| L | 0.3 | — | 0.7 |
| $L_1$ | 0.1 | — | 0.5 |
| $L_P$ | 0.2 | — | 0.6 |
| x | — | — | 0.05 |
| y | — | — | 0.05 |
| $b_2$ | — | — | 0.35 |
| $e_1$ | — | 1.5 | — |
| $l_1$ | — | — | 0.9 |
| Q | — | 0.25 | — |

| JEITA Package Code | RENESAS Code | Previous Code | MASS[Typ.] |
|---|---|---|---|
| P-USON5-1.2x1.6-0.50 | PUSN0005KA-A | TNP-5D/TNP-5DV | 0.002g |




| Reference Symbol | Dimension in Millimeters | | |
|---|---|---|---|
| | Min | Nom | Max |
| D | 1.55 | 1.6 | 1.65 |
| E | 1.1 | 1.2 | 1.3 |
| A | — | — | 0.6 |
| $A_1$ | — | — | — |
| $A_2$ | — | — | — |
| b | 0.15 | 0.2 | 0.3 |
| $b_1$ | — | — | — |
| e | — | 0.5 | — |
| $L_p$ | — | — | — |
| x | — | — | — |
| y | — | — | — |
| $Z_D$ | — | — | — |
| c | 0.07 | 0.12 | 0.22 |
| $c_1$ | — | — | — |
| $H_E$ | 1.55 | 1.6 | 1.65 |
| $L_1$ | — | 0.2 | — |

Renesas Technology Corp. Sales Strategic Planning Div. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan



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**Renesas Technology America, Inc.**
450 Holger Way, San Jose, CA 95134-1368, U.S.A
Tel: <1> (408) 382-7500, Fax: <1> (408) 382-7501

**Renesas Technology Europe Limited**
Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K.
Tel: <44> (1628) 585-100, Fax: <44> (1628) 585-900

**Renesas Technology (Shanghai) Co., Ltd.**
Unit 204, 205, AZIACenter, No.1233 Lujiazui Ring Rd, Pudong District, Shanghai, China 200120
Tel: <86> (21) 5877-1818, Fax: <86> (21) 6887-7858/7898

**Renesas Technology Hong Kong Ltd.**
7th Floor, North Tower, World Finance Centre, Harbour City, Canton Road, Tsimshatsui, Kowloon, Hong Kong
Tel: <852> 2265-6688, Fax: <852> 2377-3473

**Renesas Technology Taiwan Co., Ltd.**
10th Floor, No.99, Fushing North Road, Taipei, Taiwan
Tel: <886> (2) 2715-2888, Fax: <886> (2) 3518-3399

**Renesas Technology Singapore Pte. Ltd.**
1 Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632
Tel: <65> 6213-0200, Fax: <65> 6278-8001

**Renesas Technology Korea Co., Ltd.**
Kukje Center Bldg. 18th Fl., 191, 2-ka, Hangang-ro, Yongsan-ku, Seoul 140-702, Korea
Tel: <82> (2) 796-3115, Fax: <82> (2) 796-2145

**Renesas Technology Malaysia Sdn. Bhd**
Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No.18, Jln Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia
Tel: <603> 7955-9390, Fax: <603> 7955-9510



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