

HAT2170H

Silicon N Channel MOS FET Power Switching

REJ03G0121-0500 Rev.5.00 Sep 26, 2005

Features

- High speed switching
- Capable of 7 V gate drive
- Low drive current
- High density mounting
- Low on-resistance $R_{DS(on)} = 3.3 \ m\Omega \ typ. \ (at \ V_{GS} = 10 \ V)$

Outline

RENESAS Package code: PTZZ0005DA-A)
(Package name: LFPAK)

5
D
1, 2, 3 Source
4 Gate

Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Drain

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	40	V
Gate to source voltage	V _{GSS}	±20	V
Drain current	I _D	45	А
Drain peak current	I _{D(pulse)} Note1	180	А
Body-drain diode reverse drain current	I _{DR}	45	А
Avalanche current	I _{AP} Note2	30	А
Avalanche energy	E _{AR} Note2	72	mJ
Channel dissipation	Pch ^{Note3}	30	W
Channel to Case Thermal Resistance	θch-C	4.17	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

2. Value at Tch = 25°C, Rg \geq 50 Ω

3. $Tc = 25^{\circ}C$

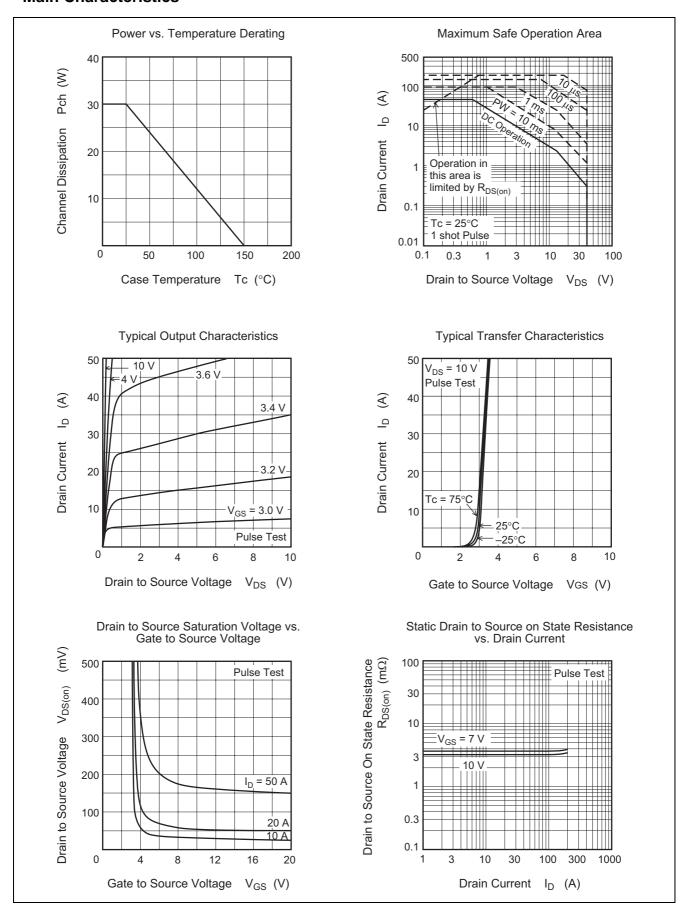
Electrical Characteristics

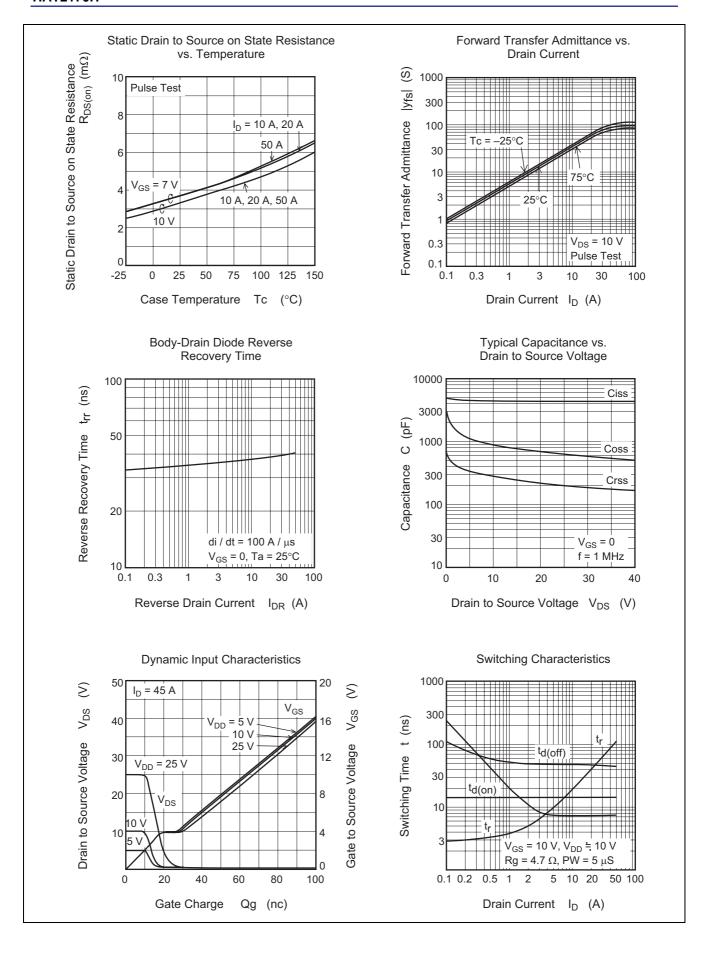
 $(Ta = 25^{\circ}C)$

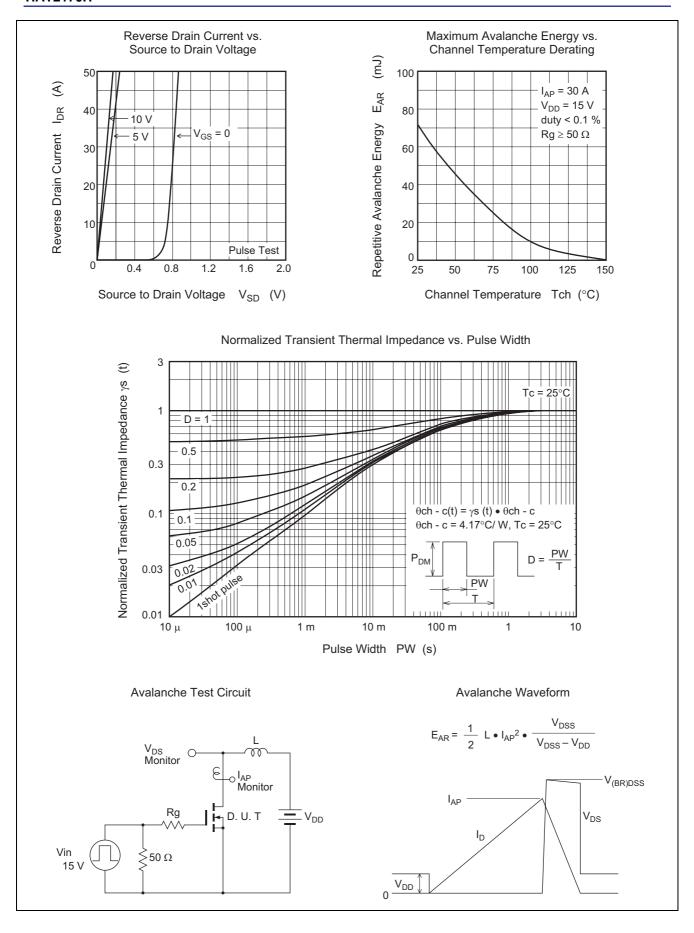
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	40	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±20	_	_	V	$I_G = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I _{GSS}	_	_	±10	μΑ	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I _{DSS}	_	_	1	μΑ	$V_{DS} = 40 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	1.5	_	3.0	V	$V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA}$
Static drain to source on state	R _{DS(on)}	_	3.3	4.2	mΩ	$I_D = 22.5 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note4}}$
resistance	R _{DS(on)}	_	3.7	5.0	mΩ	$I_D = 22.5 \text{ A}, V_{GS} = 7 \text{ V}^{Note4}$
Forward transfer admittance	y _{fs}	39	65	_	S	$I_D = 22.5 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note4}}$
Input capacitance	Ciss	-	4650	-	pF	$V_{DS} = 10 \text{ V}, V_{GS} = 0,$
Output capacitance	Coss	_	900	_	pF	f = 1 MHz
Reverse transfer capacitance	Crss	_	285	_	pF	
Gate Resistance	Rg	_	0.5	_	Ω	
Total gate charge	Qg	_	62	_	nC	V _{DD} = 10 V, V _{GS} = 10 V, I _D = 45 A
Gate to source charge	Qgs	_	18	_	nC	
Gate to drain charge	Qgd	_	7.0	_	nC	
Turn-on delay time	t _{d(on)}	_	15	_	ns	$V_{GS} = 10 \text{ V}, I_D = 22.5 \text{ A},$
Rise time	t _r	_	43	_	ns	$V_{DD} \cong 10 \text{ V}, R_L = 0.44 \Omega,$
Turn-off delay time	t _{d(off)}	_	44	_	ns	$Rg = 4.7 \Omega$
Fall time	t _f	_	7.1	_	ns	
Body-drain diode forward voltage	V_{DF}	_	0.84	1.1	V	$IF = 45 A$, $V_{GS} = 0^{Note4}$
Body-drain diode reverse recovery	t _{rr}	_	40	_	ns	$IF = 45 A, V_{GS} = 0,$
time						$di_F/dt = 100 A/ \mu s$

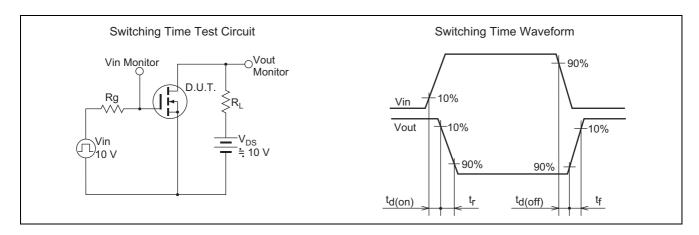
Notes: 4. Pulse test

Main Characteristics

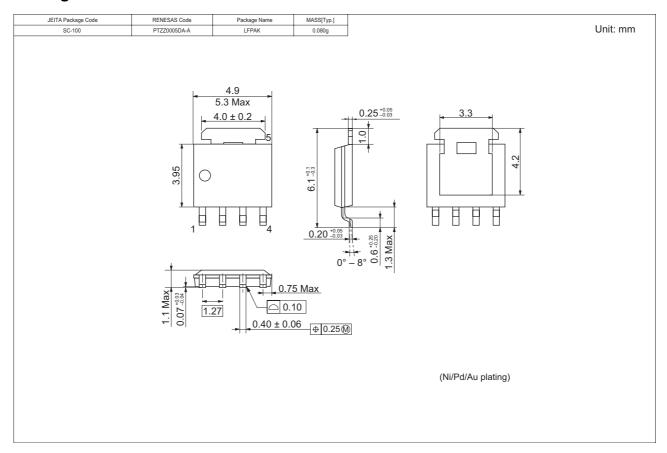








Package Dimensions



Ordering Information

Part Name	Quantity	Shipping Container
HAT2170H-EL-E	2500 pcs.	Emboss Taping

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.

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