

# H7N1004AB

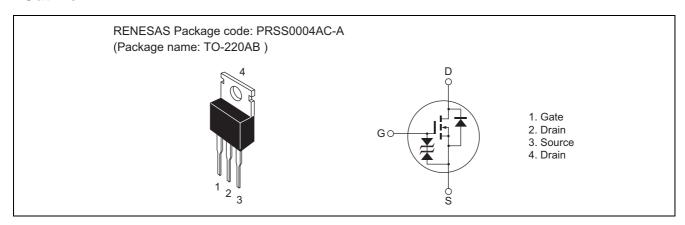
# Silicon N Channel MOS FET High Speed Power Switching

REJ03G1579-0100 Rev.1.00 Sep 03, 2007

## **Features**

- Low on-resistance  $R_{DS (on)} = 25 \text{ m}\Omega \text{ typ.}$
- Low drive current
- Available for 4.5 V gate drive

### **Outline**



# **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Value	Unit
Drain to source voltage	V <sub>DSS</sub>	100	V
Gate to source voltage	V <sub>GSS</sub>	±20	V
Drain current	I <sub>D</sub>	30	А
Drain peak current	I <sub>D(pulse)</sub> Note1	100	А
Body-drain diode reverse drain current	I <sub>DR</sub>	30	А
Avalanche current	I <sub>AP</sub> Note2	15	А
Avalanche energy	E <sub>AR</sub> Note2	22.5	mJ
Channel dissipation	Pch Note3	50	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW  $\leq$  10 ms, duty cycle  $\leq$  1%

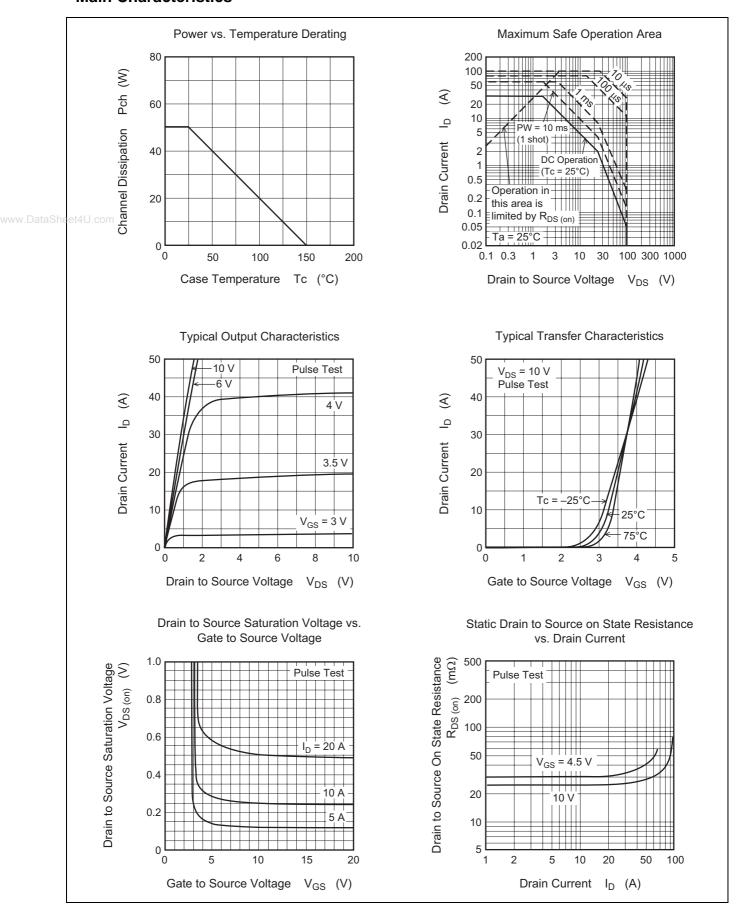
- 2. Value at Tch = 25°C, Rg  $\geq$  50  $\Omega$
- 3. Value at Tc = 25°C

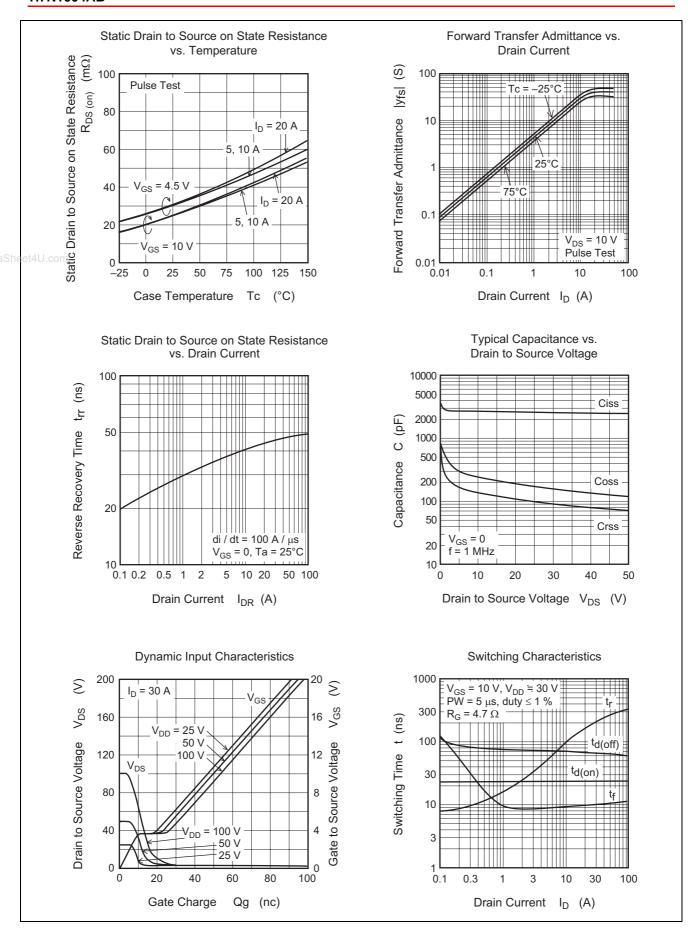
# **Electrical Characteristics**

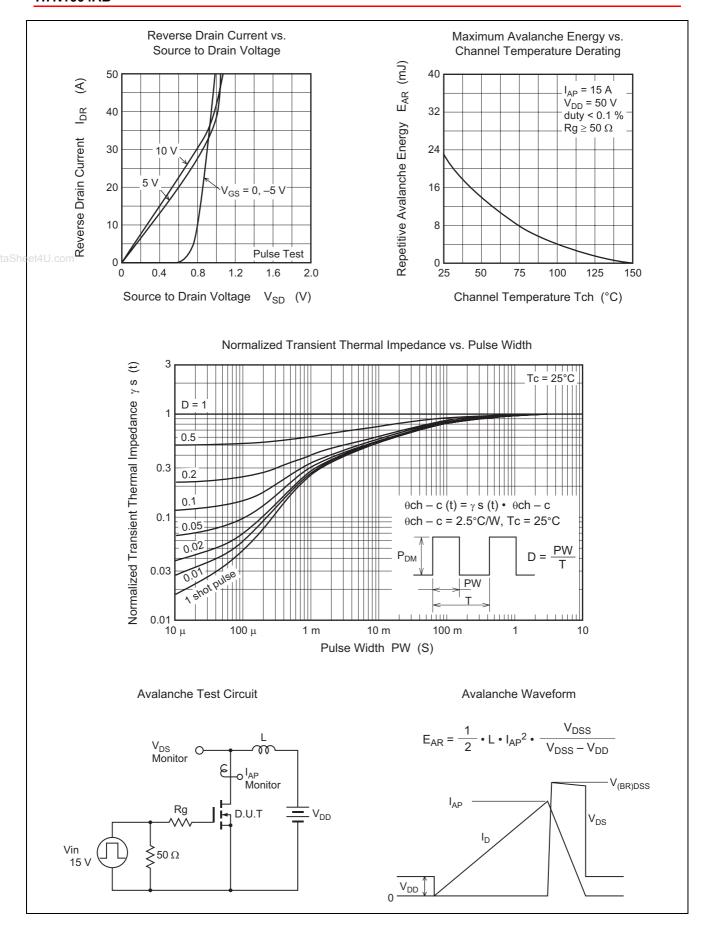
 $(Ta = 25^{\circ}C)$ 

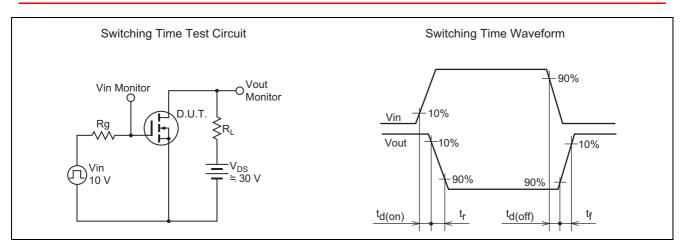
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	100	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±20	_	_	V	$I_G = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I <sub>GSS</sub>	_	_	±10	μΑ	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>	_	_	10	μΑ	$V_{DS} = 100 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	1.5	_	2.5	V	$I_D = 1 \text{ mA}, \text{ VDS} = 10 \text{ V}^{\text{Note4}}$
Static drain to source on state	R <sub>DS(on)</sub>	_	25	35	mΩ	$I_D = 15 \text{ A}, VGS = 10 \text{ V}^{\text{Note4}}$
resistance		_	30	45	mΩ	$I_D = 15 \text{ A}, \text{ VGS} = 4.5 \text{ V}^{\text{Note4}}$
Forward transfer admittance	y <sub>fs</sub>	22	37	_	S	$I_D = 15 \text{ A}, \text{ VDS} = 10 \text{ V}^{\text{Note4}}$
Input capacitance	Ciss	_	2800	_	pF	V <sub>DS</sub> = 10 V
Output capacitance	Coss	_	240	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	140	_	pF	f = 1 MHz
Total gate charge	Qg	_	50	_	nC	V <sub>DD</sub> = 50 V
Gate to source charge	Qgs	_	9	_	nC	V <sub>GS</sub> = 10 V
Gate to drain charge	Qgd	_	11	_	nC	$I_D = 30 A$
Turn-on delay time	t <sub>d(on)</sub>	_	23	_	ns	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 15 A
Rise time	t <sub>r</sub>	_	120	_	ns	$R_L = 2 \Omega$
Turn-off delay time	t <sub>d(off)</sub>	_	70	_	ns	$Rg = 4.7 \Omega$
Fall time	t <sub>f</sub>	_	9.5	_	ns	
Body-drain diode forward voltage	$V_{DF}$	_	0.9	_	V	$I_F = 30 \text{ A}, V_{GS} = 0$
Body-drain diode reverse recovery time	t <sub>rr</sub>	_	47	_	ns	$I_F = 30 \text{ A}, V_{GS} = 0$ $di_F/dt = 100 \text{ A}/\mu\text{s}$

## **Main Characteristics**



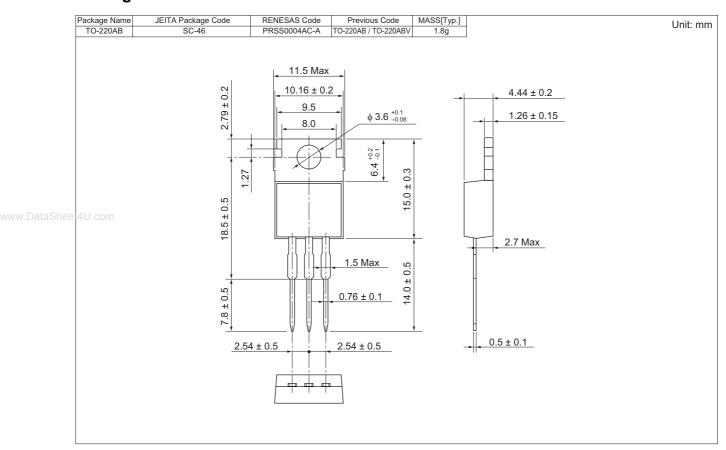






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# **Package Dimensions**



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