H5N2802PF

Silicon N Channel MOS FET
High Speed Power Switching

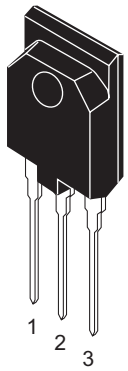
REJ03G1298-0100
Rev.1.00
Oct.05.2005

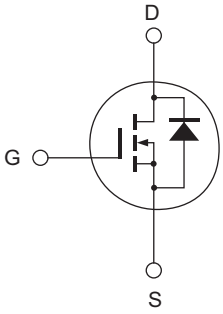
Features

- Low on-resistance
- Low leakage current
- High speed switching

Outline

RENESAS Package code: PRSS0003ZA-A
(Package name: TO-3PFM)





1. Gate
2. Drain
3. Source

Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V_{DSS}	280	V
Gate to source voltage	V_{GSS}	±30	V
Drain current	I_D	25	A
Drain peak current	$I_{D\ (pulse)}$ Note1	100	A
Body-drain diode reverse drain current	I_{DR}	25	A
Body-drain diode reverse drain peak current	$I_{DR\ (pulse)}$ Note1	100	A
Avalanche current	I_{AP} Note3	13	A
Avalanche energy	E_{AR} Note3	10.2	mJ
Channel dissipation	Pch Note2	60	W
Channel to case thermal impedance	θch-c	2.08	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	–55 to +150	°C

Notes: 1. PW ≤ 10 µs, duty cycle ≤ 1%
2. Value at Tc = 25°C
3. STch = 25°C, Tch ≤ 150°C

Electrical Characteristics

(Ta = 25°C)

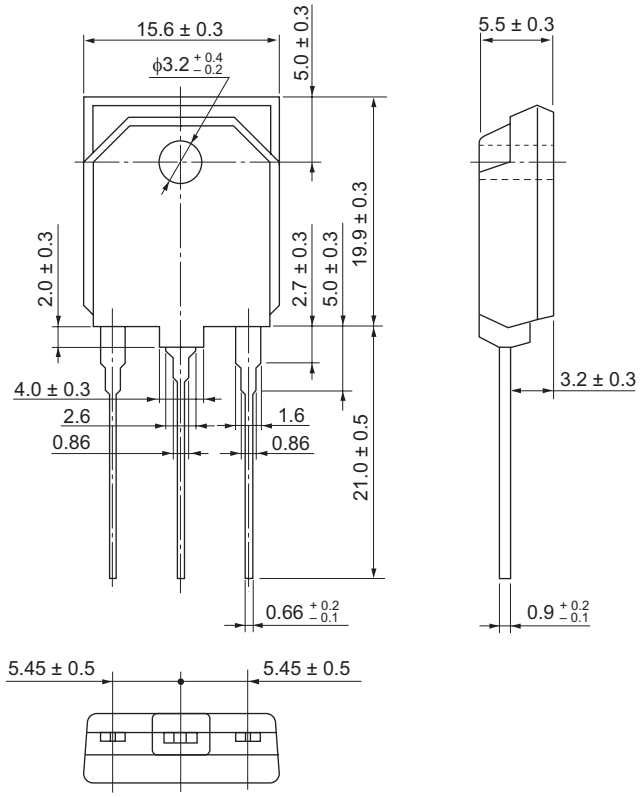
Item	Symbol	Min	Typ	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	280	—	—	V	I_D = 10 mA, V_{GS} = 0
Zero gate voltage drain current	I_{DSS}	—	—	1	µA	V_{DS} = 280 V, V_{GS} = 0
Gate to source leak current	I_{GSS}	—	—	±0.1	µA	V_{GS} = ±30 V, V_{DS} = 0
Gate to source cutoff voltage	$V_{GS(off)}$	3.0	—	4.0	V	V_{DS} = 10 V, I_D = 1 mA
Forward transfer admittance	$\|y_{fs}\|$	15	27	—	S	I_D = 12.5 A, V_{DS} = 10 V Note4
Static drain to source on state resistance	$R_{DS(on)}$	—	0.057	0.066	Ω	I_D = 12.5 A, V_{GS} = 10 V Note4
Input capacitance	Ciss	—	3600	—	pF	V_{DS} = 25 V, V_{GS} = 0, f = 1 MHz
Output capacitance	Coss	—	450	—	pF	
Reverse transfer capacitance	Crss	—	32	—	pF	
Turn-on delay time	$t_{d(on)}$	—	50	—	ns	I_D = 12.5 A, V_{GS} = 10 V, R_L = 11.2 Ω, Rg = 10 Ω
Rise time	t_r	—	90	—	ns	
Turn-off delay time	$t_{d(off)}$	—	120	—	ns	
Fall time	t_f	—	75	—	ns	
Total gate charge	Qg	—	72	—	nC	V_{DD} = 220 V, V_{GS} = 10 V, I_D = 25 A
Gate to source charge	Qgs	—	18	—	nC	
Gate to drain charge	Qgd	—	24	—	nC	
Body-drain diode forward voltage	V_{DF}	—	0.88	1.40	V	I_F = 25 A, V_{GS} = 0 Note4
Body-drain diode reverse recovery time	t_{rr}	—	200	—	ns	I_F = 25 A, V_{GS} = 0, di_F/dt = 100 A/µs
Body-drain diode reverse recovery charge	Q_{rr}	—	1.4	—	µC	

Notes: 4. Pulse test

Package Dimensions

JEITA Package Code	RENESAS Code	Package Name	MASS[Typ.]
SC-93	PRSS0003ZA-A	TO-3PFM / TO-3PFMV	5.2g

Unit: mm



Ordering Information

Part Name	Quantity	Shipping Container
H5N2802PF-E	30 pcs	Plastic magazine

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.

Renesas Technology Corp. Sales Strategic Planning Div. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan

Keep safety first in your circuit designs!

1. Renesas Technology Corp. puts the maximum effort into making semiconductor products better and more reliable, but there is always the possibility that trouble may occur with them. Trouble with semiconductors may lead to personal injury, fire or property damage.
   Remember to give due consideration to safety when making your circuit designs, with appropriate measures such as (i) placement of substitutive, auxiliary circuits, (ii) use of nonflammable material or (iii) prevention against any malfunction or mishap.

Notes regarding these materials

1. These materials are intended as a reference to assist our customers in the selection of the Renesas Technology Corp. product best suited to the customer's application; they do not convey any license under any intellectual property rights, or any other rights, belonging to Renesas Technology Corp. or a third party.
2. Renesas Technology Corp. assumes no responsibility for any damage, or infringement of any third-party's rights, originating in the use of any product data, diagrams, charts, programs, algorithms, or circuit application examples contained in these materials.
3. All information contained in these materials, including product data, diagrams, charts, programs and algorithms represents information on products at the time of publication of these materials, and are subject to change by Renesas Technology Corp. without notice due to product improvements or other reasons. It is therefore recommended that customers contact Renesas Technology Corp. or an authorized Renesas Technology Corp. product distributor for the latest product information before purchasing a product listed herein.
   The information described here may contain technical inaccuracies or typographical errors.
   Renesas Technology Corp. assumes no responsibility for any damage, liability, or other loss rising from these inaccuracies or errors.
   Please also pay attention to information published by Renesas Technology Corp. by various means, including the Renesas Technology Corp. Semiconductor home page (http://www.renesas.com).
4. When using any or all of the information contained in these materials, including product data, diagrams, charts, programs, and algorithms, please be sure to evaluate all information as a total system before making a final decision on the applicability of the information and products. Renesas Technology Corp. assumes no responsibility for any damage, liability or other loss resulting from the information contained herein.
5. Renesas Technology Corp. semiconductors are not designed or manufactured for use in a device or system that is used under circumstances in which human life is potentially at stake. Please contact Renesas Technology Corp. or an authorized Renesas Technology Corp. product distributor when considering the use of a product contained herein for any specific purposes, such as apparatus or systems for transportation, vehicular, medical, aerospace, nuclear, or undersea repeater use.
6. The prior written approval of Renesas Technology Corp. is necessary to reprint or reproduce in whole or in part these materials.
7. If these products or technologies are subject to the Japanese export control restrictions, they must be exported under a license from the Japanese government and cannot be imported into a country other than the approved destination.
   Any diversion or reexport contrary to the export control laws and regulations of Japan and/or the country of destination is prohibited.
8. Please contact Renesas Technology Corp. for further details on these materials or the products contained therein.

RENESAS SALES OFFICES

http://www.renesas.com

Refer to "**http://www.renesas.com/en/network**" for the latest and detailed information.

Renesas Technology America, Inc.
450 Holger Way, San Jose, CA 95134-1368, U.S.A
Tel: <1> (408) 382-7500, Fax: <1> (408) 382-7501

Renesas Technology Europe Limited
Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K.
Tel: <44> (1628) 585-100, Fax: <44> (1628) 585-900

Renesas Technology Hong Kong Ltd.
7th Floor, North Tower, World Finance Centre, Harbour City, 1 Canton Road, Tsimshatsui, Kowloon, Hong Kong
Tel: <852> 2265-6688, Fax: <852> 2730-6071

Renesas Technology Taiwan Co., Ltd.
10th Floor, No.99, Fushing North Road, Taipei, Taiwan
Tel: <886> (2) 2715-2888, Fax: <886> (2) 2713-2999

Renesas Technology (Shanghai) Co., Ltd.
Unit2607 Ruijing Building, No.205 Maoming Road (S), Shanghai 200020, China
Tel: <86> (21) 6472-1001, Fax: <86> (21) 6415-2952

Renesas Technology Singapore Pte. Ltd.
1 Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632
Tel: <65> 6213-0200, Fax: <65> 6278-8001

Renesas Technology Korea Co., Ltd.
Kukje Center Bldg. 18th Fl., 191, 2-ka, Hangang-ro, Yongsan-ku, Seoul 140-702, Korea
Tel: <82> 2-796-3115, Fax: <82> 2-796-2145

Renesas Technology Malaysia Sdn. Bhd.
Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No.18, Jalan Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia
Tel: <603> 7955-9390, Fax: <603> 7955-9510