

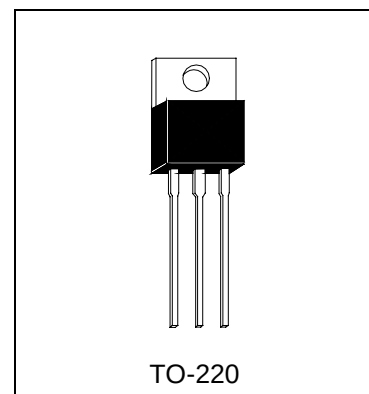


H2584

PNP EPITAXIAL PLANAR TRANSISTOR

Description

The H2584 is designed for use in low voltage and low dropout regulator applications.



TO-220

Absolute Maximum Ratings

- Maximum Temperatures
Storage Temperature -55 ~ +150 °C
Junction Temperature 150 °C Maximum
- Maximum Power Dissipation
Total Power Dissipation ($T_c=25^{\circ}\text{C}$) 65 W
- Maximum Voltages and Currents ($T_A=25^{\circ}\text{C}$)
 V_{CBO} Collector to Base Voltage -20 V
 V_{CEO} Collector to Emitter Voltage -15 V
 V_{EBO} Emitter to Base Voltage -5 V
 I_C Collector Current -10 A

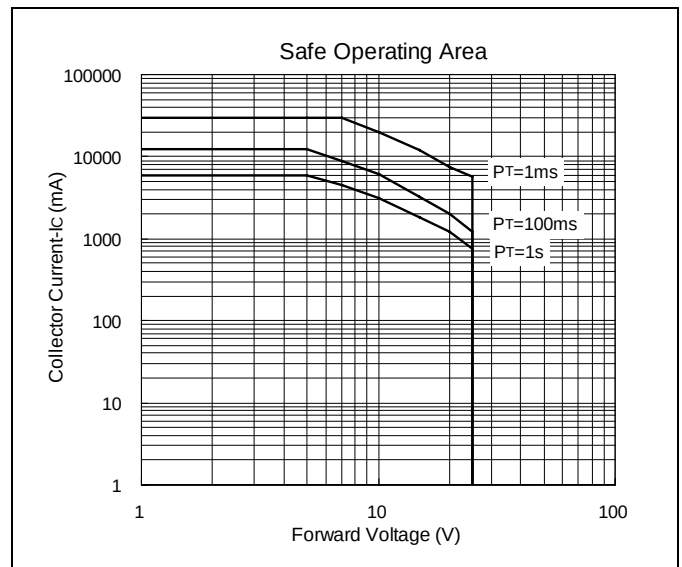
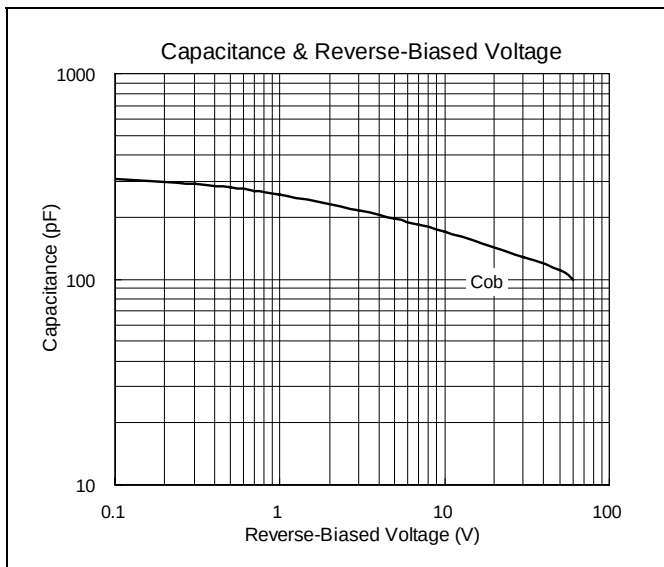
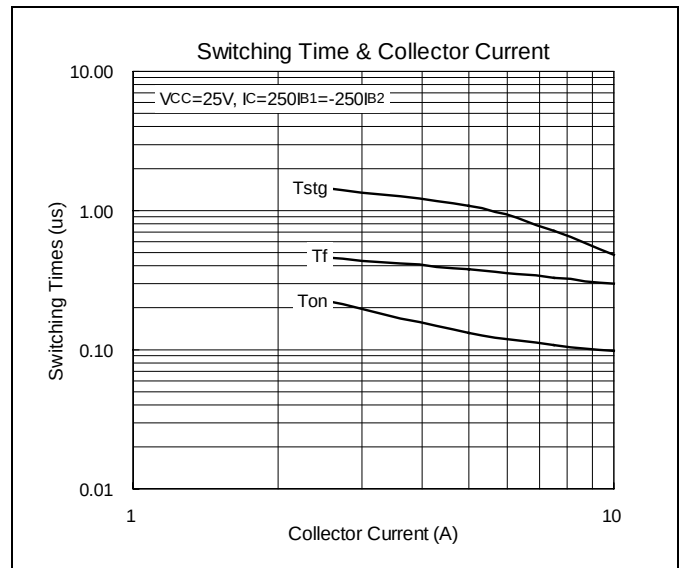
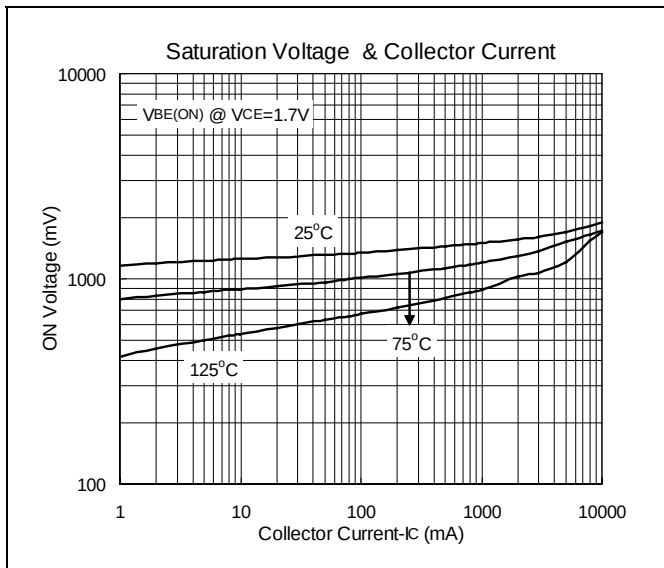
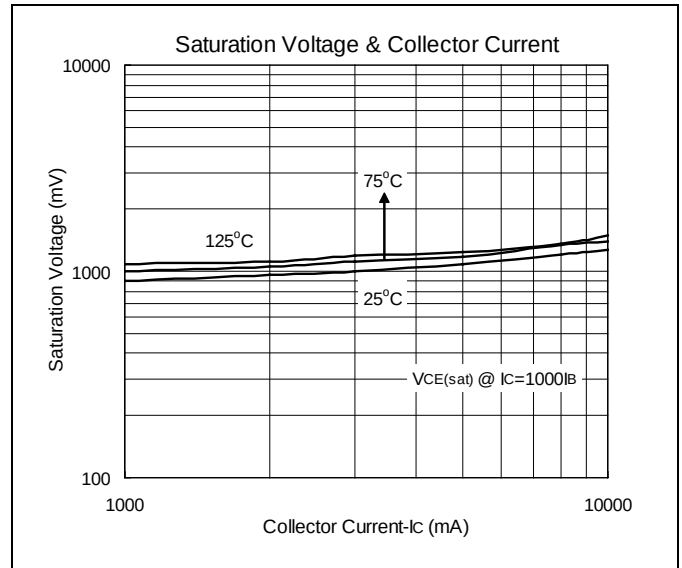
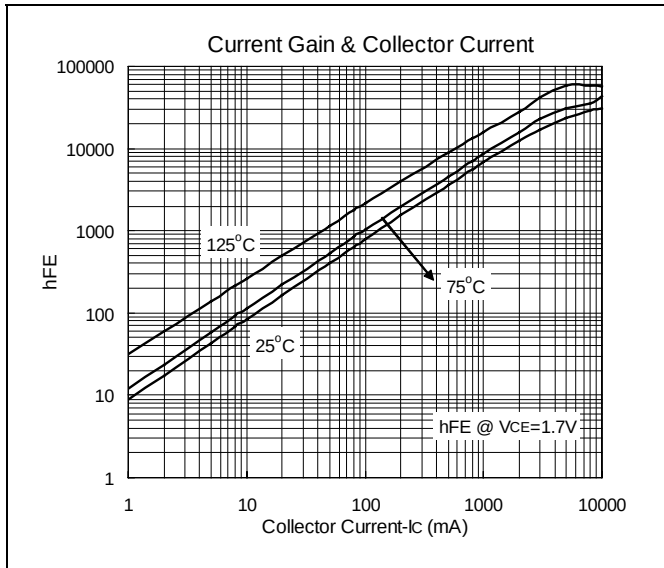
Electrical Characteristics ($T_A=25^{\circ}\text{C}$)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
I_{CBO}	-	-	-10	uA	$V_{CB}=-20\text{V}$
I_{CEO}	-	-	-20	uA	$V_{CE}=-15\text{V}$
I_{EBO}	-	-	-2	mA	$V_{EB}=-5\text{V}$
$*V_{CE(sat)}$	-	-	-1.5	V	$I_C=-10\text{A}$, $I_B=-10\text{mA}$
$V_{BE(on)}$	-	-	-2	V	$I_C=-5\text{A}$, $V_{CE}=-1.7\text{V}$
$*h_{FE1}$	2	-	60	K	$I_C=-500\text{mA}$, $V_{CE}=-1.7\text{V}$
$*h_{FE2}$	1	15	60	K	$I_C=-10\text{A}$, $V_{CE}=-1.7\text{V}$

*Pulse Test: Pulse Width $\leq 380\mu\text{s}$, Duty Cycle $\leq 2\%$

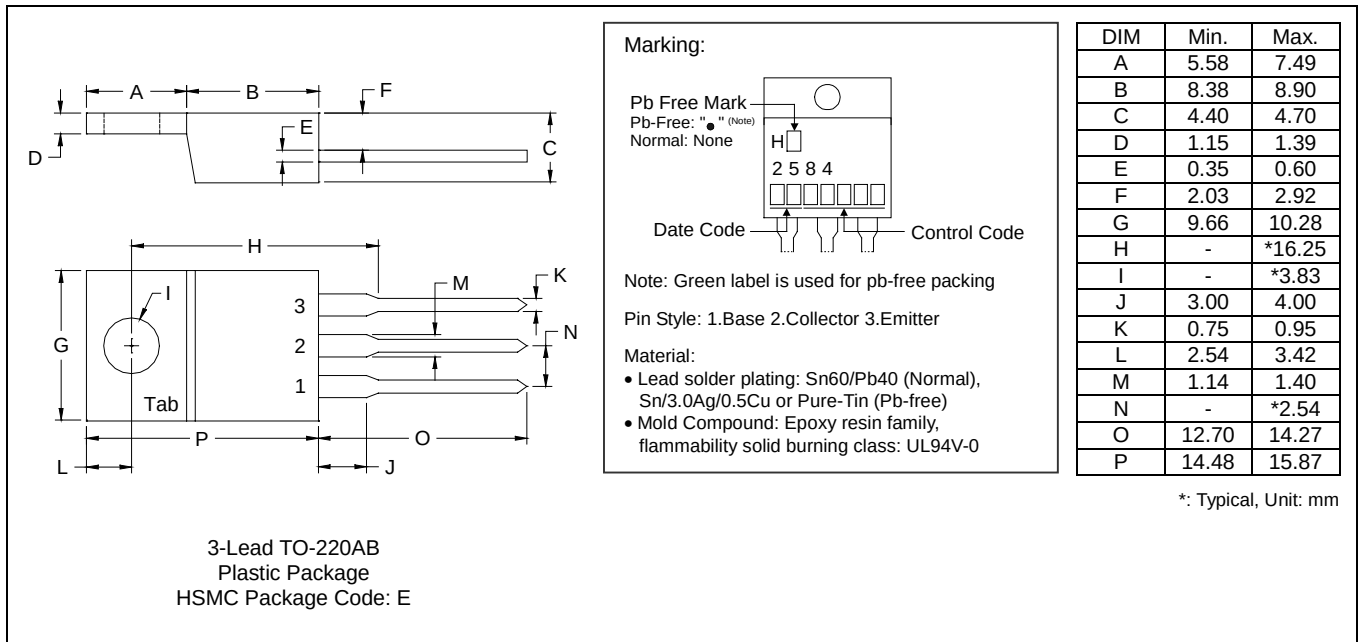


Characteristics Curve





TO-220AB Dimension



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Head Office And Factory:

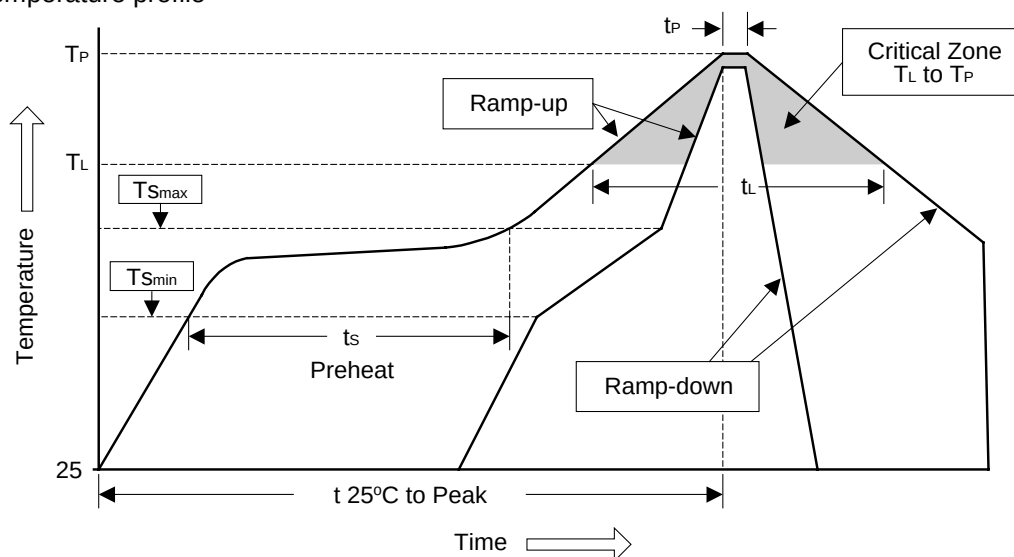
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Soldering Methods for HSMC's Products

1. Storage environment: Temperature=10°C~35°C Humidity=65%±15%
2. Reflow soldering of surface-mount devices

Figure 1: Temperature profile



Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Average ramp-up rate (T_L to T_P)	<3°C/sec	<3°C/sec
Preheat		
- Temperature Min (T_{Smin})	100°C	150°C
- Temperature Max (T_{Smax})	150°C	200°C
- Time (min to max) (t_s)	60~120 sec	60~180 sec
T_{Smax} to T_L		
- Ramp-up Rate	<3°C/sec	<3°C/sec
Time maintained above:		
- Temperature (T_L)	183°C	217°C
- Time (t_L)	60~150 sec	60~150 sec
Peak Temperature (T_P)	240°C +0/-5°C	260°C +0/-5°C
Time within 5°C of actual Peak Temperature (t_P)	10~30 sec	20~40 sec
Ramp-down Rate	<6°C/sec	<6°C/sec
Time 25°C to Peak Temperature	<6 minutes	<8 minutes

3. Flow (wave) soldering (solder dipping)

Products	Peak temperature	Dipping time
Pb devices.	245°C ±5°C	5sec ±1sec
Pb-Free devices.	260°C +0/-5°C	5sec ±1sec