

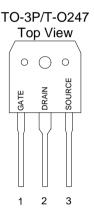
## **GENERAL DESCRIPTION**

This high voltage MOSFET uses an advanced termination scheme to provide enhanced voltage-blocking capability without degrading performance over time. In addition, this advanced MOSFET is designed to withstand high energy in avalanche and commutation modes. The new energy efficient design also offers a drain-to-source diode with a fast recovery time. Designed for high voltage, high speed switching applications in power supplies, converters and PWM motor controls, these devices are particularly well suited for bridge circuits where diode speed and commutating safe operating areas are critical and offer additional and safety margin against unexpected voltage transients.

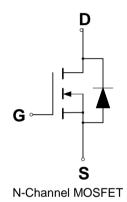
#### FEATURES

- Robust High Voltage Termination
- Avalanche Energy Specified
- Source-to-Drain Diode Recovery Time Comparable to a Discrete Fast Recovery Diode
- Diode is Characterized for Use in Bridge Circuits
- I<sub>DSS</sub> and V<sub>DS</sub>(on) Specified at Elevated Temperature
- Isolated Mounting Hole Reduces Mounting Hardware

### **PIN CONFIGURATION**



#### SYMBOL



## **ABSOLUTE MAXIMUM RATINGS**

Rating		Value	Unit
Drain to Current – Continuous		19	А
<ul> <li>Pulsed</li> </ul>	I <sub>DM</sub>	57	
Gate-to-Source Voltage – Continue	V <sub>GS</sub>	V	
Total Power Dissipation – TO3P	PD	264	W
– TO247		243	<b>W/°</b> C
Derate above 25℃ – TO3P		2.5	
– TO247		2.4	
Operating and Storage Temperature Range	TJ, TSTG	-55 to 150	°C
Single Pulse Drain-to-Source Avalanche Energy $-T_J = 25^{\circ}$			
(V <sub>DD</sub> = 100V, V <sub>GS</sub> = 10V, I <sub>L</sub> = 17A, L = 10mH, R <sub>G</sub> = 25 )	E <sub>AS</sub>	1445	mJ
Thermal Resistance – Junction to Case -TO3P		0.43	°C/W
<ul> <li>Junction to Case -TO247</li> </ul>		0.55	
<ul> <li>Junction to Ambient -TO3P, TO247</li> </ul>	JA	40	
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds	TL	260	°C
ESD SENSITIVITY – HBM, C=100pF, R=1.5k	Vesd	2000	V
(1) Drain current limited by maximum junction temperature			



#### ORDERING INFORMATION

Package
TO-3P
TO-247

\*Note: G : Suffix for PB Free Product

#### **ELECTRICAL CHARACTERISTICS**

Unless otherwise specified,  $T_J$  = 25  $^\circ\mathrm{C}$  .

				GPT19N65	19N65	
Characteristic		Symbol	Min	Тур	Max	Units
Drain-Source Breakdown Voltage		N/	050			ν.
$(V_{GS} = 0 V, I_D = 250 \mu A)$		V <sub>(BR)DSS</sub>	650			V
Drain-Source Leakage Current		1			1	uA
$(V_{DS} = 650 \text{ V}, V_{GS} = 0 \text{ V})$		I <sub>DSS</sub>			1	uA
Gate-Source Leakage Current-Forward					100	nA
$(V_{gsf} = 30 \text{ V}, V_{DS} = 0 \text{ V})$		I <sub>GSSF</sub>				
Gate-Source Leakage Current-Reverse		IGSSR			100	nA
$(V_{gsr} = 30 \text{ V}, V_{DS} = 0 \text{ V})$		IGSSR			100	IIA
Gate Threshold Voltage		V	3		5	V
$(V_{DS} = V_{GS}, I_D = 250 \ \mu A)$		V <sub>GS(th)</sub>	5			
Static Drain-Source On-Resistance ( $V_{GS}$ = 10 V, $I_{D}$ = 9.5 A) *		R <sub>DS(on)</sub>			0.35	
Forward Transconductance ( $V_{DS} = 50 \text{ V}$ , $I_D = 9.5 \text{ A}$ ) *		<b>g</b> fs		18		S
Input Capacitance	$(V_{DS} = 25 V, V_{GS} = 0 V, f = 1.0 MHz)$	C <sub>iss</sub>		3895		pF
Output Capacitance		C <sub>oss</sub>		351		pF
Reverse Transfer Capacitance		C <sub>rss</sub>		11.6		pF
Turn-On Delay Time	(V <sub>DD</sub> = 300 V, I <sub>D</sub> = 19 A,	t <sub>d(on)</sub>		44.8		ns
Rise Time		tr		101		ns
Turn-Off Delay Time	$R_{G} = 25$ ) *	t <sub>d(off)</sub>		86.9		ns
Fall Time	1	t <sub>f</sub>		68		ns
Total Gate Charge		Qg		73.7		nC
Gate-Source Charge	$(V_{DS} = 480 \text{ V}, I_D = 19 \text{ A},$ $V_{GS} = 10 \text{ V})^*$	Q <sub>gs</sub>		21.9		nC
Gate-Drain Charge		$Q_{gd}$		29.1		nC
	SOURCE-DRAIN DIODE CH					
Forward On-Voltage(1)	(I <sub>S</sub> = 19 A, d <sub>IS</sub> /d <sub>t</sub> = 100A/µs)	V <sub>SD</sub>			1.5	V
Forward Turn-On Time		t <sub>on</sub>		**		ns
Reverse Recovery Time		t <sub>rr</sub>		592		ns

\* Pulse Test: Pulse Width  $\leq$ 300µs, Duty Cycle  $\leq$ 2%

\*\* Negligible, Dominated by circuit inductance



# TYPICAL ELECTRICAL CHARACTERISTICS

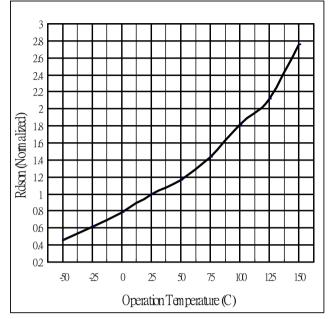


Fig 1. On-Resistance Variation with vs. Temperature

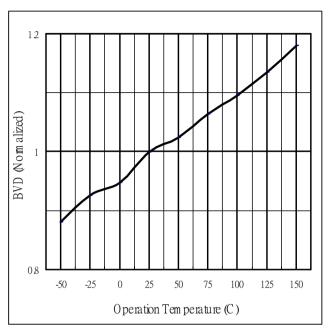


Fig.2 Breakdown Voltage Variation vs. Temperature

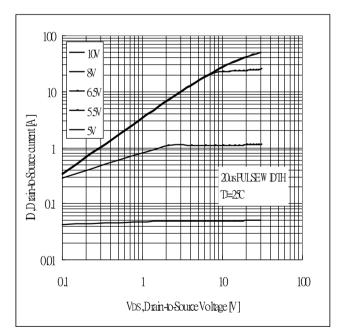
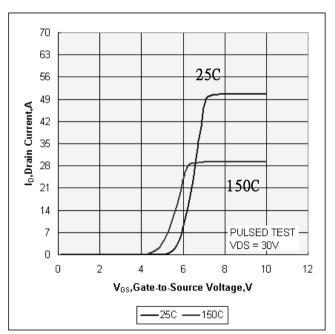


Fig 3. Typical Output Characteristics



**Fig 4. Typical Transfer Characteristics** 



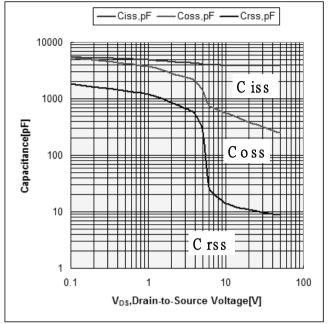
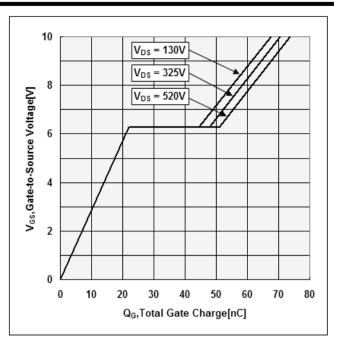


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

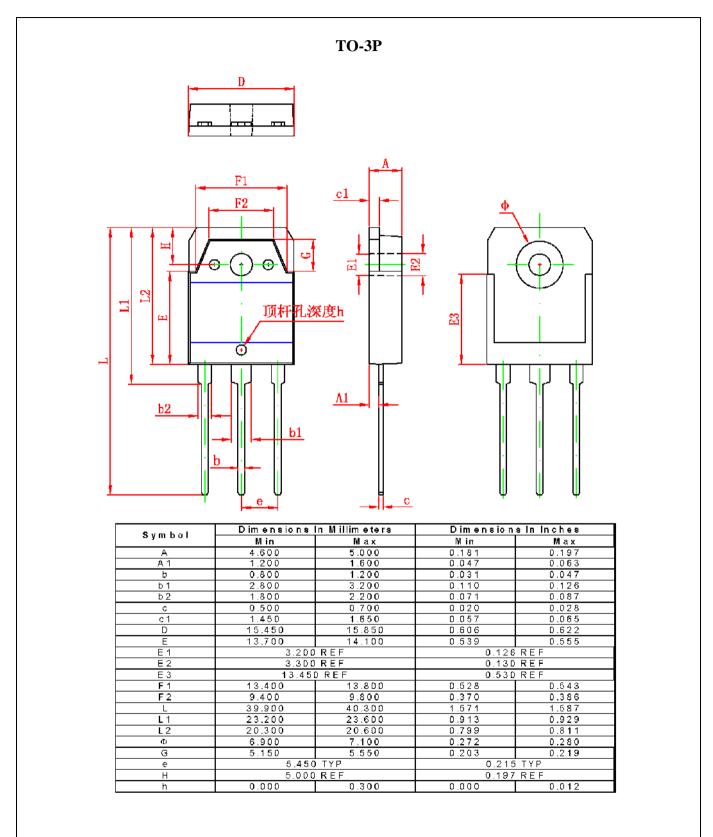
**GPT19N65** Power Field Effect Transistor





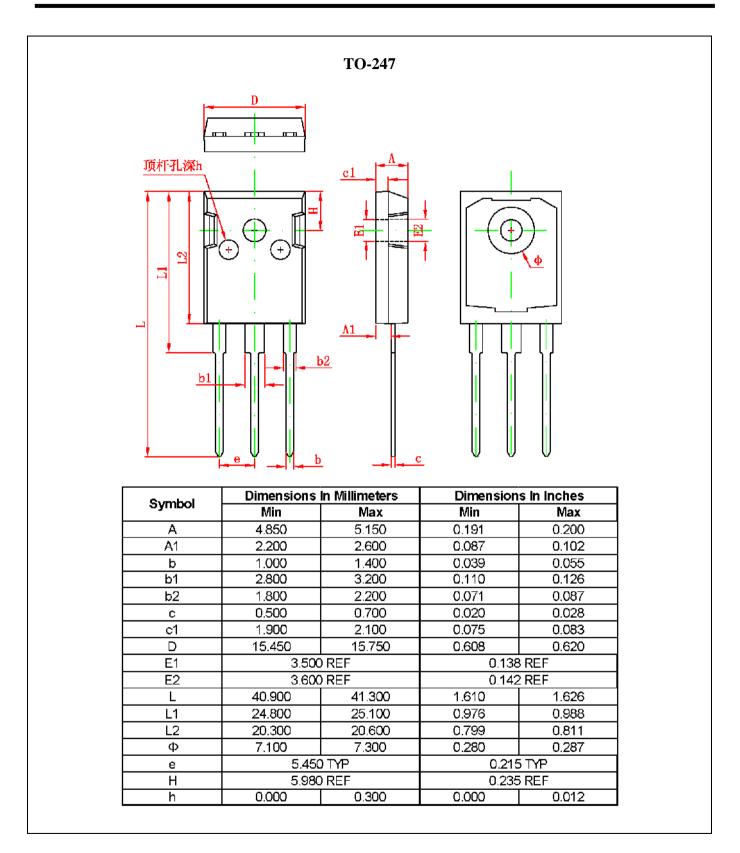


### PACKAGE DIMENSION





# **GPT19N65** Power Field Effect Transistor





### **IMPORTANT NOTICE**

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