



Power Field Effect Transistor

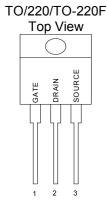
GENERAL DESCRIPTION

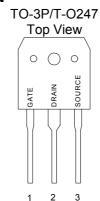
This high voltage MOSFET uses an advanced termination scheme to provide enhanced voltage-blocking capability without degrading performance over time. In addition, this advanced MOSFET is designed to withstand high energy in avalanche and commutation modes. The new energy efficient design also offers a drain-to-source diode with a fast recovery time. Designed for high voltage, high speed switching applications in power supplies, converters and PWM motor controls, these devices are particularly well suited for bridge circuits where diode speed and commutating safe operating areas are critical and offer additional and safety margin against unexpected voltage transients.

FEATURES

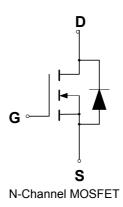
- ◆ Robust High Voltage Termination
- Avalanche Energy Specified
- Source-to-Drain Diode Recovery Time Comparable to a Discrete Fast Recovery Diode
- ◆ Diode is Characterized for Use in Bridge Circuits
- ♦ I_{DSS} and V_{DS}(on) Specified at Elevated Temperature
- ◆ Isolated Mounting Hole Reduces Mounting Hardware

PIN CONFIGURATION





SYMBOL



ABSOLUTE MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Drain to Current — Continuous	I _D	13.5	Α
- Pulsed	I _{DM}	40.5	
Gate-to-Source Voltage — Continue	V_{GS}	±30	V
Total Power Dissipation – TO220	P_D	220	W
– TO220FP		50	W/°C
– TO3P		230	
Derate above 25℃ - TO220		1.6	
– TO220FP		0.4	
– TO3P		1.8	
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to 150	$^{\circ}\!\mathbb{C}$
Single Pulse Drain-to-Source Avalanche Energy $-T_J = 25^{\circ}$		700	
$(V_{DD} = 100V, V_{GS} = 10V, I_L = 12A, L = 10mH, R_G = 25\Omega)$		720	mJ
Thermal Resistance — Junction to Case -TO220	θ_{JC}	0.51	°C/W
 Junction to Case -TO220FP 		3.2	
 Junction to Case -TO3P 		0.5	
 Junction to Ambient -TO220,TO220FP 	θ_{JA}	62.5	
 Junction to Ambient -TO3P 		40	
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds		260	$^{\circ}\!\mathbb{C}$
ESD SENSITIVITY — HBM, C=100pF, R=1.5kΩ	Vesd	2000	V





Power Field Effect Transistor

ORDERING INFORMATION

Part Number	Package
GPT14N60GN220*	TO-220
GPT14N60DGN220FP*	TO-220F
GPT14N60GN3P*	TO-3P
GPT14N60GN247*	TO-247

^{*}Note: G: Suffix for PB Free Product

ELECTRICAL CHARACTERISTICS

Unless otherwise specified, $T_J = 25^{\circ}C$.

			GP14N60			
Chara	Symbol	Min	Тур	Max	Units	
Drain-Source Breakdown Voltage	V	600			V	
$(V_{GS} = 0 \text{ V}, I_D = 250 \ \mu \text{ A})$		V _{(BR)DSS}	600			V
Drain-Source Leakage Current		I _{DSS}			1	uA
$(V_{DS} = 600 \text{ V}, V_{GS} = 0 \text{ V})$		DSS			ļ	uA
Gate-Source Leakage Current-Forward	İ	I _{GSSF}			100	nA
$(V_{gsf} = 30 \text{ V}, V_{DS} = 0 \text{ V})$		IGSSF				IIA
Gate-Source Leakage Current-Reverse	lasan			100	nA	
$(V_{gsr} = 30 \text{ V}, V_{DS} = 0 \text{ V})$		I _{GSSR}			100	IIA
Gate Threshold Voltage		$V_{GS(th)}$	3		5	V
$(V_{DS} = V_{GS}, I_{D} = 250 \ \mu A)$		▼ GS(th)	3			
Static Drain-Source On-Resistance (Vo	R _{DS(on)}			0.54	Ω	
Forward Transconductance (V _{DS} = 50 V	g FS		14		S	
Input Capacitance	()/ - 25 \/ \/ - 0 \/	C_{iss}		2259		pF
Output Capacitance	$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V},$ $V_{DS} = 1.0 \text{ MHz}$	C _{oss}		212		pF
Reverse Transfer Capacitance	1 – 1.0 MHz)	C_{rss}		9.41		pF
Turn-On Delay Time		t _{d(on)}		36.53		ns
Rise Time	$(V_{DD} = 300 \text{ V}, I_D = 14 \text{ A},$	t _r		45.6		ns
Turn-Off Delay Time	$R_G = 25\Omega)$ *	$t_{d(off)}$		36.8		ns
Fall Time		t _f		41.6		ns
Total Gate Charge	0/ 400 \/ 1 44 4	Q_g		43.5		nC
Gate-Source Charge	$(V_{DS} = 480 \text{ V}, I_{D} = 14 \text{ A}, V_{GS} = 10 \text{ V})^*$	Q _{gs}		11.3		nC
Gate-Drain Charge	V _{GS} = 10 V)"	Q_{gd}		16.4		nC
	SOURCE-DRAIN DIODE CHA	ARACTERISTICS				
Forward On-Voltage(1)	(1 44 A	V _{SD}			1.5	V
Forward Turn-On Time	$(I_S = 14 A, d_{IS}/d_t = 100A/\mu S)$	t _{on}		**		ns
Reverse Recovery Time		t _{rr}		502		ns

^{*} Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%

^{**} Negligible, Dominated by circuit inductance



POWER FIELD EFFECT TRANSISTOR

TYPICAL ELECTRICAL CHARACTERISTICS

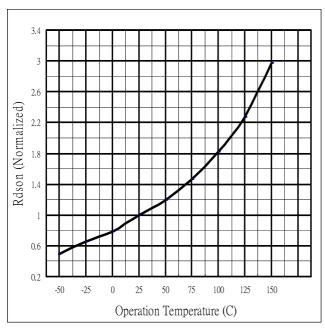


Fig 1. On-Resistance Variation with vs. Temperature

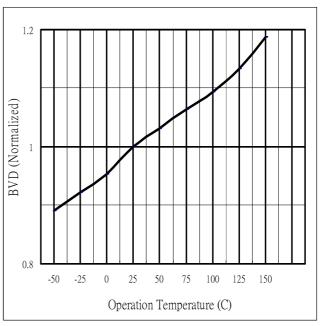


Fig.2 Breakdown Voltage Variation vs. Temperature

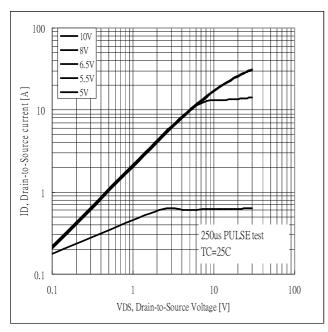


Fig 3. Typical Output Characteristics

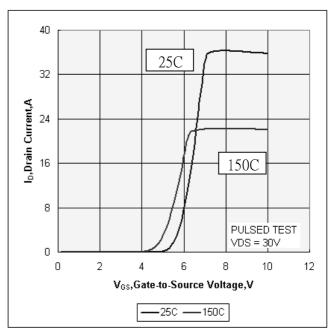


Fig 4. Typical Transfer Characteristics





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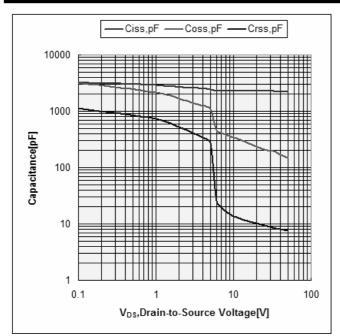


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

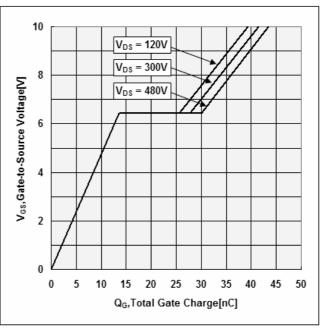


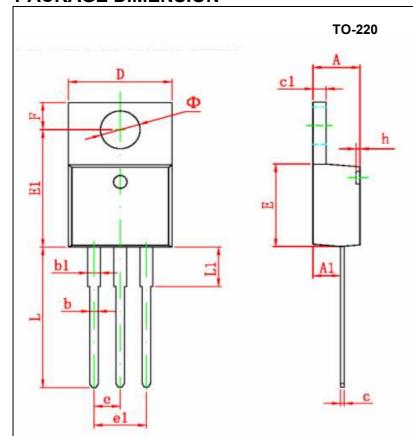
Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage





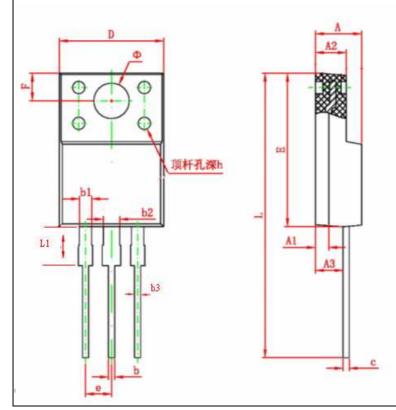
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PACKAGE DIMENSION



Completed	Dimensions I	n Millimeters	
Symbol	Min.	Max	
Α	4.40	4.80	
A1	2.10	2.84	
Ъ	0.71	0.91	
b1	1.17	1.37	
С	0.30	0.60	
c1	1.17	1.47	
D	9.40	10.60	
Е	8.40	9.60	
е	2.54	TYP.	
e1	4.90	5.60	
F	3,00 REF.		
Φ	3.50	REF.	
h	0.00	0.30	
L	12.50	14.00	
L1	3.50	4.00	

TO-220F

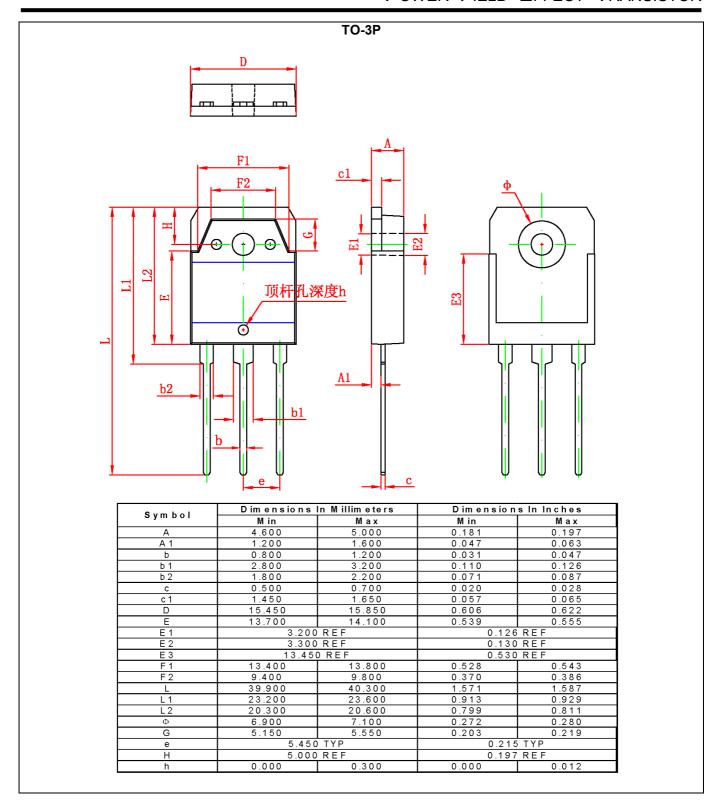


Comple al	Dimensions	In Millimeters	
Symbol	Min.	Max	
Α	3.80	4.70	
A1	1.3	REF.	
A2	2.20	3.20	
A3	2.10	3.20	
Ъ	0.30	0.95	
b1	1.00	1.75	
ь2	1.00	1.75	
b3	0.50	0.80	
С	0.30	0.90	
D	9.90	10.40	
Е	14.60	16.20	
е	2.54 TYP.		
F	3.00 REF.		
Φ	3.50 REF.		
h	0.00	0.30	
L	28.00	30.00	
L1	3.20	3.55	





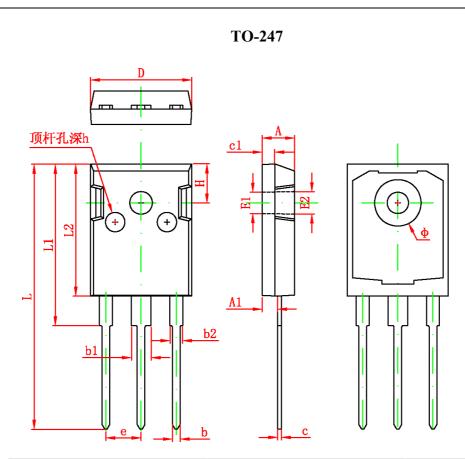
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Cumbal	Dimensions	In Millimeters	Dimension	s In Inches
Symbol	Min	Max	Min	Max
Α	4.850	5.150	0.191	0.200
A1	2.200	2.600	0.087	0.102
b	1.000	1.400	0.039	0.055
b1	2.800	3.200	0.110	0.126
b2	1.800	2.200	0.071	0.087
С	0.500	0.700	0.020	0.028
c1	1.900	2.100	0.075	0.083
D	15.450	15.750	0.608	0.620
E1	3.500 REF		0.138	REF
E2	3.600 REF		0.142 REF	
L	40.900	41.300	1.610	1.626
L1	24.800	25.100	0.976	0.988
L2	20.300	20.600	0.799	0.811
Ф	7.100	7.300	0.280	0.287
е	5.450 TYP		0.215	5 TYP
Н	5.980 REF		0.235	REF
h	0.000	0.300	0.000	0.012





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IMPORTANT NOTICE

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