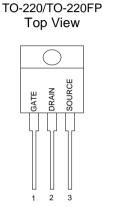


# **GENERAL DESCRIPTION**

This advanced high voltage MOSFET is designed to withstand high energy in the avalanche mode and switch efficiently. This new high energy device also offers a drain-to-source diode with fast recovery time. Designed for high voltage, high speed switching applications such as power supplies, converters, power motor controls and bridge circuits.

## **PIN CONFIGURATION**

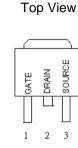




GATE DRAIN

2 3

SOURCE

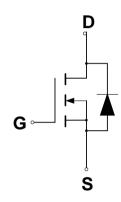


TO-252

# FEATURES

- Higher Current Rating
- Lower Rds(on)
- Lower Capacitances
  - Lower Total Gate Charge
- Tighter VSD Specifications
- Avalanche Energy Specified

## SYMBOL



N-Channel MOSFET

## **ABSOLUTE MAXIMUM RATINGS**

1

Rating		Value	Unit
Drain to Current — Continuous		3.5	А
- Pulsed	I <sub>DM</sub>	10.5	
Gate-to-Source Voltage – Continue	$V_{GS}$	±30	V
Total Power Dissipation TO-251/TO-252		50	W
TO-220		84	
TO-220FP		28	W/°C
Derate above 25°C TO-251/TO-252		0.4	
TO-220		0.8	
TO-220FP		0.26	
Operating and Storage Temperature Range	$T_J,T_STG$	-55 to 150	°C
Single Pulse Drain-to-Source Avalanche Energy $-T_J = 25^{\circ}C$		45	
$(V_{DD} = 100V, V_{GS} = 10V, I_L = 3 \text{ A}, L = 10\text{mH}, R_G = 25$ )			mJ
Thermal Resistance – Junction to Case TO-251/TO-252	JC	2.6	°C <b>/W</b>
TO-220		1.3	
TO220FP	JA	4.3	
<ul> <li>Junction to Ambient TO-251/TO-252</li> </ul>		120	
TO-220, TO-220FP		62.5	
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds	T∟	260	°C



#### ORDERING INFORMATION

Part Number	Package
GPT04N70GN220*	TO-220
GPT04N70GN220FP*	TO-220 Full Package
GPT04N70GN251*	TO-251
GPT04N70GN252*	TO-252

\*Note: G : Suffix for Pb Free Product

X : Suffix for Halogen and Pb Free Product

# ELECTRICAL CHARACTERISTICS

Unless otherwise specified,  $T_J = 25^{\circ}C$ .

				GPT04N70	)	
Cha	racteristic	Symbol	Min	Тур	Мах	Units
Drain-Source Breakdown Voltage		V <sub>(BR)DSS</sub>	700			V
$(V_{GS} = 0 V, I_D = 250 \mu A)$						
Drain-Source Leakage Current		I <sub>DSS</sub>			1	uA
$(V_{DS} = 700 \text{ V}, V_{GS} = 0 \text{ V})$						
Gate-Source Leakage Current-Forward		IGSSF			100	nA
$(V_{gsf} = 30 \text{ V}, V_{DS} = 0 \text{ V})$						
Gate-Source Leakage Current-Re	everse	I <sub>GSSR</sub>			100	nA
$(V_{gsr} = -30 \text{ V}, V_{DS} = 0 \text{ V})$						
Gate Threshold Voltage		$V_{GS(th)}$	2.5	3.5	4.5	V
$(V_{DS} = V_{GS}, I_D = 250 \ \mu A)$						
Static Drain-Source On-Resistance ( $V_{GS}$ = 10 V, $I_D$ = 2.0A) *		R <sub>DS(on)</sub>			3.8	
Forward Transconductance (V <sub>DS</sub>	= 15 V, I <sub>D</sub> = 2.0 A) *	<b>g</b> fs		3		S
Input Capacitance	$(V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V},$	C <sub>iss</sub>		572		pF
Output Capacitance	( , ,	C <sub>oss</sub>		42.8		pF
Reverse Transfer Capacitance	f = 1.0 MHz)	C <sub>rss</sub>		2.3		pF
Turn-On Delay Time	(V <sub>DD</sub> = 350 V. I <sub>D</sub> = 4.0 A.	t <sub>d(on)</sub>		18.8		ns
Rise Time	$V_{DD} = 350 \text{ V}, \text{ ID} = 4.0 \text{ A},$ $V_{GS} = 10 \text{ V},$	tr		15.7		ns
Turn-Off Delay Time	V <sub>GS</sub> = 10 V, R <sub>G</sub> = 9.1 )*	t <sub>d(off)</sub>		30.7		ns
Fall Time	$R_G = 9.1$	t <sub>f</sub>		17.87		ns
Total Gate Charge		Qg		13.8		nC
Gate-Source Charge	$(V_{DS} = 560 \text{ V}, I_D = 4.0 \text{ A},$ $V_{GS} = 10 \text{ V})^*$	Q <sub>gs</sub>		3.31		nC
Gate-Drain Charge		Q <sub>gd</sub>		6.16		nC
SOURCE-DRAIN DIODE CHAR	ACTERISTICS					
Forward On-Voltage(1)	(1 4 0 4	V <sub>SD</sub>			1.5	V
Forward Turn-On Time	$(I_{S} = 4.0 \text{ A}, d_{IS}/d_{t} = 100 \text{ A}/\mu \text{s})$	t <sub>on</sub>		**		ns
Reverse Recovery Time		t <sub>rr</sub>		260		ns

\* Pulse Test: Pulse Width  $\leq$ 300µs, Duty Cycle  $\leq$ 2%

\*\* Negligible, Dominated by circuit inductance



# **TYPICAL ELECTRICAL CHARACTERISTICS**

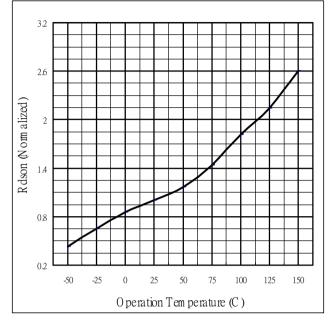


Fig 1. On-Resistance Variation with vs. Temperature

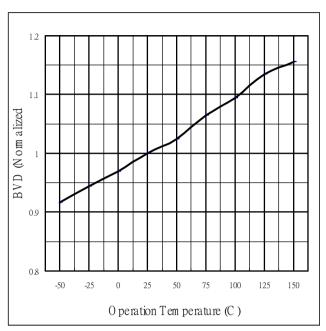


Fig.2 Breakdown Voltage Variation vs. Temperature

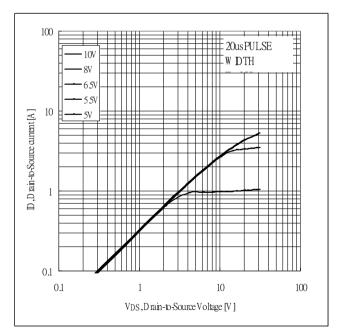


Fig 3. Typical Output Characteristics

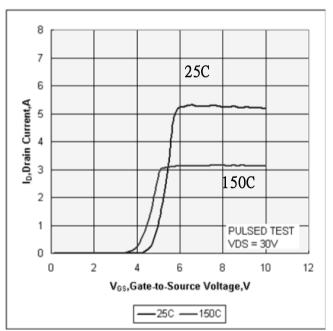
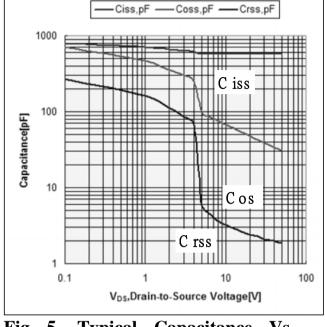
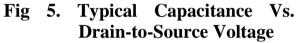


Fig 4. Typical Transfer Characteristics









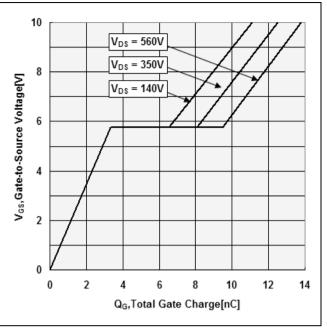
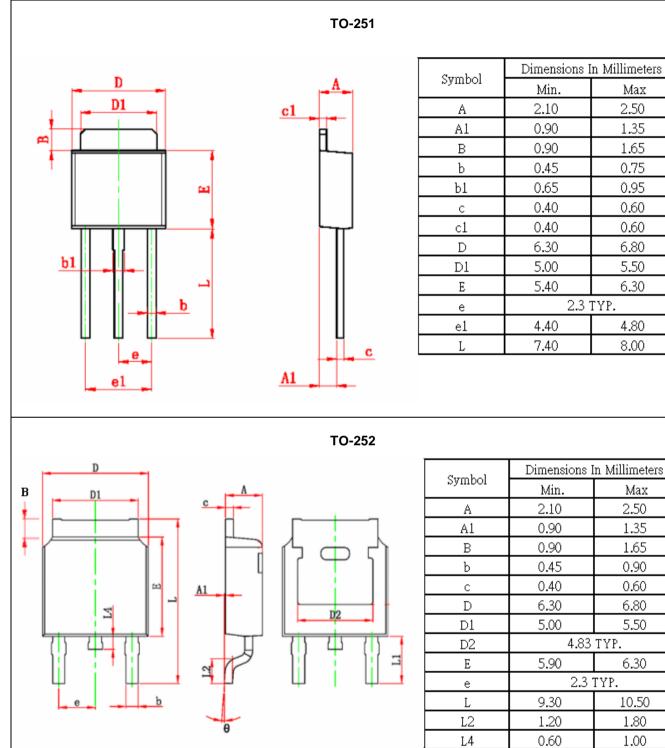


Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage





## PACKAGE DIMENSION



θ

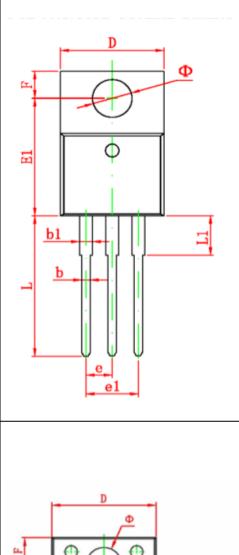
0.00

10.00



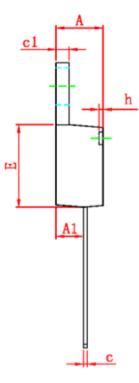
**GPT04N70** Power Field Effect Transistor

TO-220



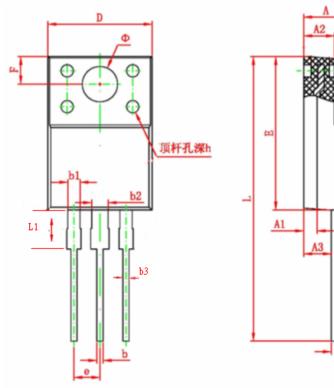
</>
CHAMPION

虹冠電子



Course la cal	Dimensions In Millimeter		
Symbol	Min.	Max	
А	4.40	4.80	
A1	2.10	2.84	
b	0.71	0.91	
b1	1.17	1.37	
с	0.30	0.60	
c1	1.17	1.47	
D	9.40	10.60	
E	8.40	9.60	
е	2.54 TYP.		
el	4.90	5.60	
F	3.00 REF.		
Φ	3.50 REF.		
h	0.00	0.30	
L	12.50	14.00	
L1	3.50	4.00	

TO-220FP



Crurch al	Dimensions In Millimeters		
Symbol	Min.	Max	
А	3.80	4.70	
A1	1.3 REF.		
A2	2.20	3.20	
A3	2.10	3.20	
b	0.30	0.95	
b1	1.00	1.75	
b2	1.00	1.75	
b3	0.50	0.80	
с	0.30	0.90	
D	9.90	10.40	
E	14.60	16.20	
е	2.54 TYP.		
F	3.00 REF.		
Φ	3.50 REF.		
h	0.00	0.30	
L	28.00	30.00	
L1	3.20	3.55	

С



# **IMPORTANT NOTICE**

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