

GENERAL DESCRIPTION

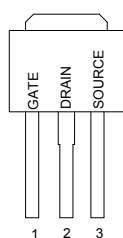
This high voltage MOSFET uses an advanced termination scheme to provide enhanced voltage-blocking capability without degrading performance over time. In addition, this advanced MOSFET is designed to withstand high energy in avalanche and commutation modes. The new energy efficient design also offers a drain-to-source diode with a fast recovery time. Designed for high voltage, high speed switching applications in power supplies, converters and PWM motor controls, these devices are particularly well suited for bridge circuits where diode speed and commutating safe operating areas are critical and offer additional and safety margin against unexpected voltage transients.

FEATURES

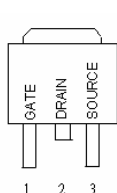
- ◆ Robust High Voltage Termination
- ◆ Avalanche Energy Specified
- ◆ Source-to-Drain Diode Recovery Time Comparable to a Discrete Fast Recovery Diode
- ◆ Diode is Characterized for Use in Bridge Circuits
- ◆ I_{DSS} and $V_{DS(on)}$ Specified at Elevated Temperature

PIN CONFIGURATION

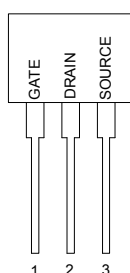
TO-251
Front View



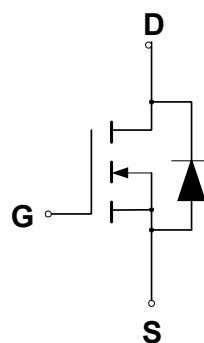
TO-252
Front View



TO-92
Front View



SYMBOL



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Drain to Current — TO-251(Continuous)	I_D	0.9	A
TO-92 (Continuous)		0.29	
— Pulsed (TO-251)	I_{DM}	2.7	
Gate-to-Source Voltage — Continue	V_{GS}	±30	V
Total Power Dissipation TO-251	P_D	31	W
TO-92		2.9	W/°C
Derate above 25°C TO-251		0.23	
TO-92		0.02	
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to 150	°C
Single Pulse Drain-to-Source Avalanche Energy — $T_J = 25^\circ\text{C}$ ($V_{DD} = 100\text{V}$, $V_{GS} = 10\text{V}$, $I_{AS} = 0.7\text{A}$, $L = 50\text{mH}$, $R_G = 25\Omega$)	E_{AS}	2.45	mJ
Thermal Resistance — Junction to Case (TO-251)	θ_{JC}	4.5	°C/W
— Junction to Lead (TO-92)	θ_{JL}	48	
— Junction to Ambient (TO-251)	θ_{JA}	120	
— Junction to Ambient (TO-92)		140	
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds	T_L	260	°C

ORDERING INFORMATION

Part Number	Package
GPT01N65AGN251	TO-251
GPT01N65AGN252	TO-252
GPT01N65AGN92	TO-92

*Note: G : Suffix for Pb Free Product

ELECTRICAL CHARACTERISTICS

Unless otherwise specified, $T_J = 25^\circ\text{C}$.

		GPT01N65A				
Characteristic		Symbol	Min	Typ	Max	Units
Drain-Source Breakdown Voltage (V _{GS} = 0 V, I _D = 250 μA)		V _{(BR)DSS}	650			V
Drain-Source Leakage Current (V _{DS} = 650 V, V _{GS} = 0 V, TO-92) (V _{DS} = 650 V, V _{GS} = 0 V, TO-251)		I _{DSS}			50 1	uA
Gate-Source Leakage Current-Forward (V _{gsf} = 30 V, V _{DS} = 0 V)		I _{GSSF}			100	nA
Gate-Source Leakage Current-Reverse (V _{gsr} =- 30 V, V _{DS} = 0 V)		I _{GSSR}			100	nA
Gate Threshold Voltage (V _{DS} = V _{GS} , I _D = 250 μA)		V _{GS(th)}	2.5	3.5	4.5	V
Static Drain-Source On-Resistance (V _{GS} = 10 V, I _D = 0.5A) * TO-251 TO-92		R _{DS(on)}			13	Ω
Forward Transconductance (V _{DS} ≥ 50 V, I _D = 0.5A) *		g _{FS}		1.3		S
Input Capacitance	(V _{DS} = 25 V, V _{GS} = 0 V, f = 1.0 MHz)	C _{iss}		161.1		pF
Output Capacitance		C _{oss}		14.1		pF
Reverse Transfer Capacitance		C _{rss}		1.03		pF
Turn-On Delay Time	(V _{DD} = 325 V, I _D = 0.5 A, R _G = 25Ω) *	t _{d(on)}		11.3		ns
Rise Time		t _r		30.1		ns
Turn-Off Delay Time		t _{d(off)}		34.1		ns
Fall Time		t _f		65.1		ns
Total Gate Charge	(V _{DS} = 520 V, I _D = 0.5 A, V _{GS} = 10 V)*	Q _g		6.15		nC
Gate-Source Charge		Q _{gs}		1.12		nC
Gate-Drain Charge		Q _{gd}		3.67		nC
SOURCE-DRAIN DIODE CHARACTERISTICS						
Forward On-Voltage(1)	(I _S = 0.5 A, V _{GS} = 0 V, dI _S /dI _t = 100A/μs)	V _{SD}			1.4	V
Forward Turn-On Time		t _{on}		**		ns
Reverse Recovery Time		t _{rr}		180		ns

* Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$

** Negligible, Dominated by circuit inductance

TYPICAL ELECTRICAL CHARACTERISTICS

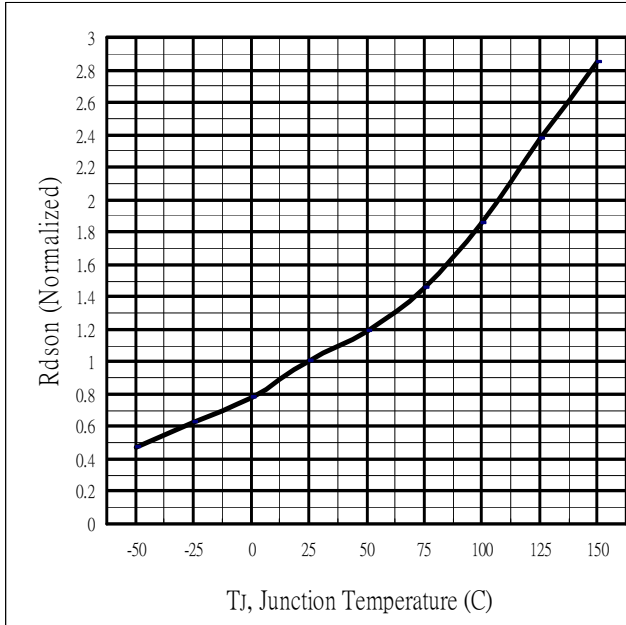


Fig 1. On-Resistance Variation with vs. Temperature

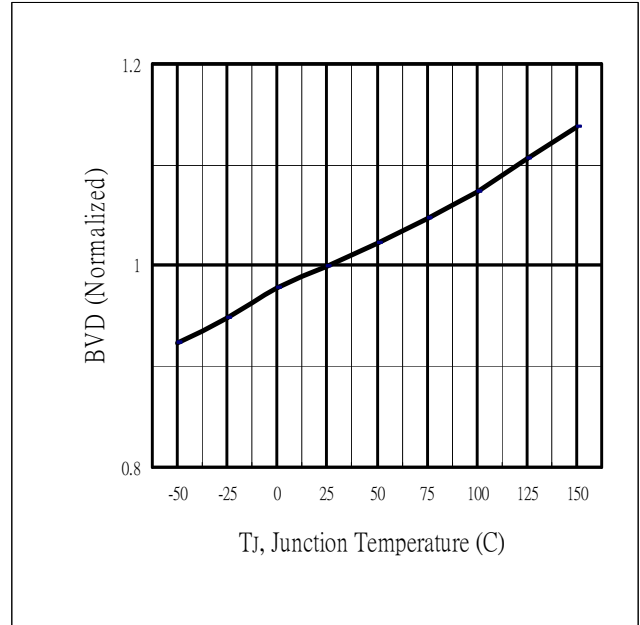


Fig.2 Breakdown Voltage Variation vs. Temperature

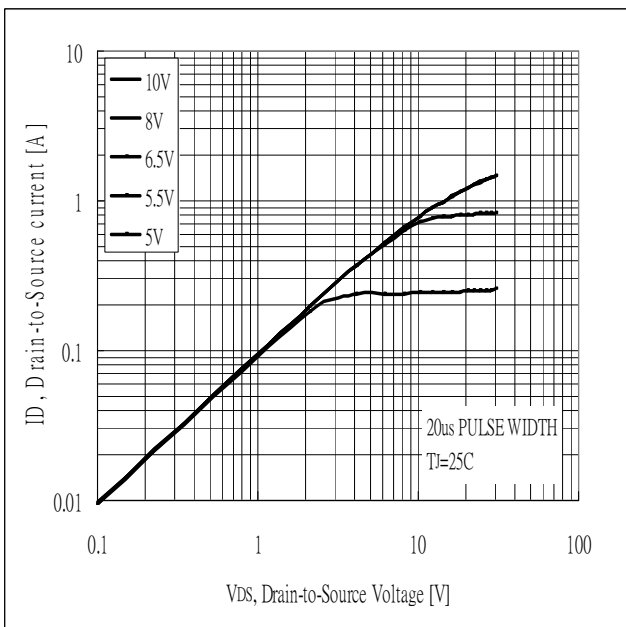


Fig 3. Typical Output Characteristics

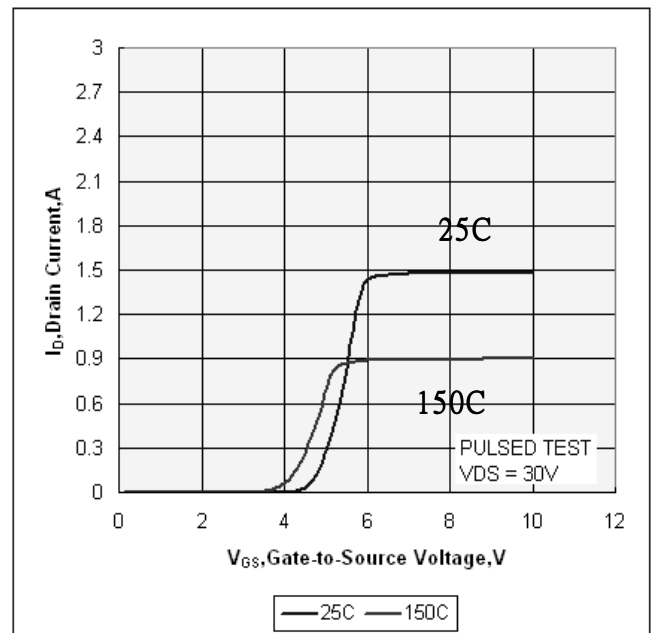


Fig 4. Typical Transfer Characteristics

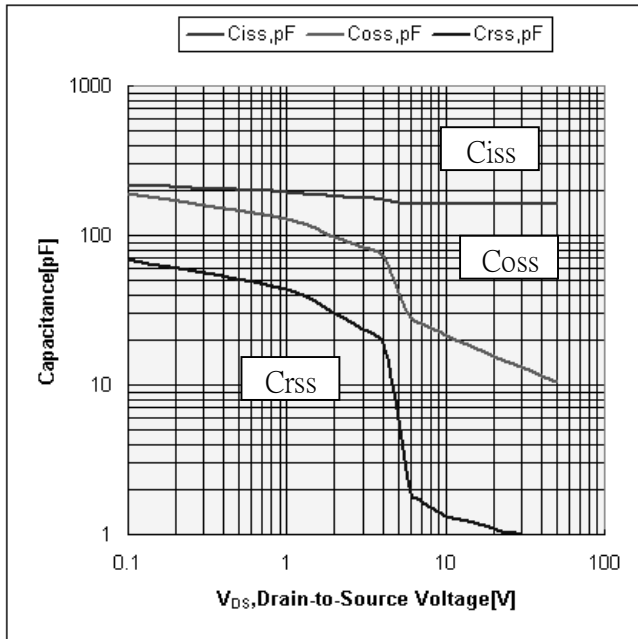


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

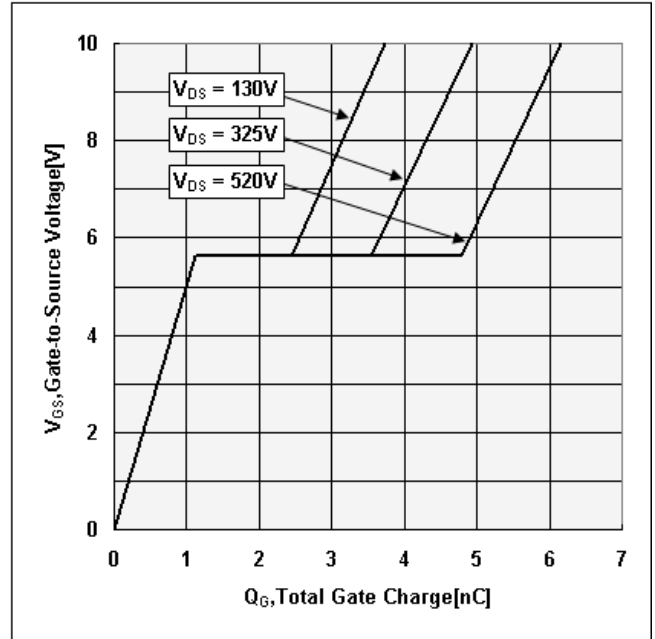
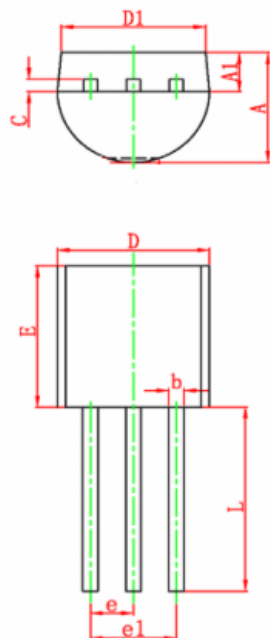


Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage

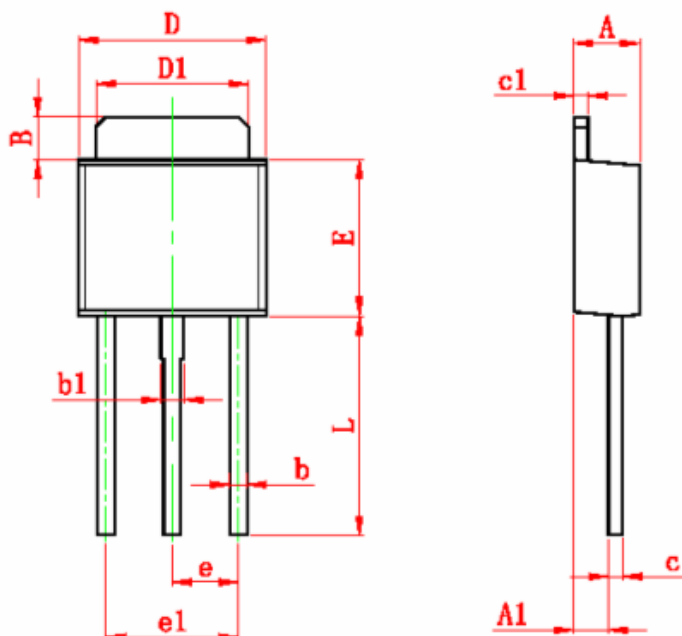
PACKAGE DIMENSION

TO-92



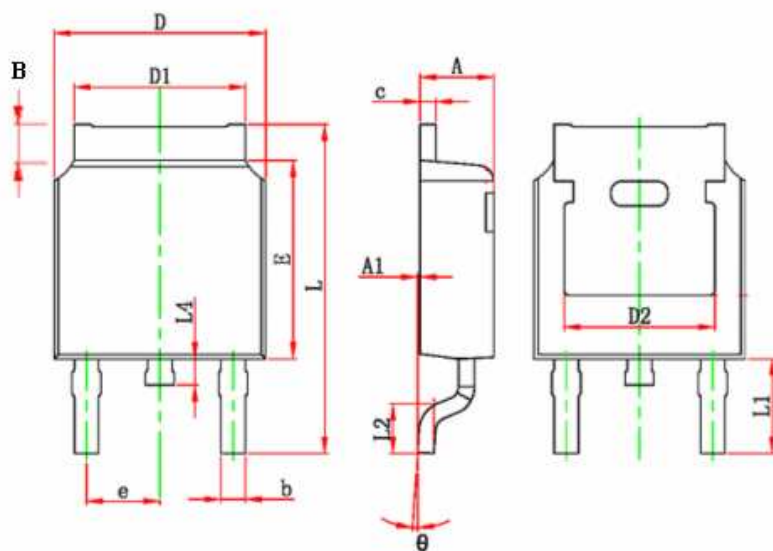
Symbol	Dimensions In Millimeters	
	Min.	Max
A	3.25	3.85
A1	1.00	1.50
b	0.38	0.58
c	0.30	0.52
D	4.30	4.90
D1	4.00 TYP.	
E	4.20	4.80
e	1.27 TYP.	
e1	2.40	2.70
L	13.00	15.00

TO251



Symbol	Dimensions In Millimeters	
	Min.	Max
A	2.10	2.50
A1	0.90	1.35
B	0.90	1.65
b	0.45	0.75
b1	0.65	0.95
c	0.40	0.60
c1	0.40	0.60
D	6.30	6.80
D1	5.00	5.50
E	5.40	6.30
e	2.3 TYP.	
e1	4.40	4.80
L	7.40	8.00

TO-252



Symbol	Dimensions In Millimeters	
	Min.	Max
A	2.10	2.50
A1	0.90	1.35
B	0.90	1.65
b	0.45	0.90
c	0.40	0.60
D	6.30	6.80
D1	5.00	5.50
D2	4.83 TYP.	
E	5.90	6.30
e	2.3 TYP.	
L	9.30	10.50
L2	1.20	1.80
L4	0.60	1.00
Θ	0.00	10.00

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