

GJ32C

PNP EPITAXIAL PLANAR TRANSISTOR

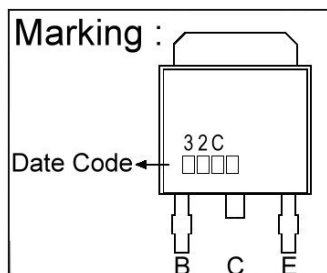
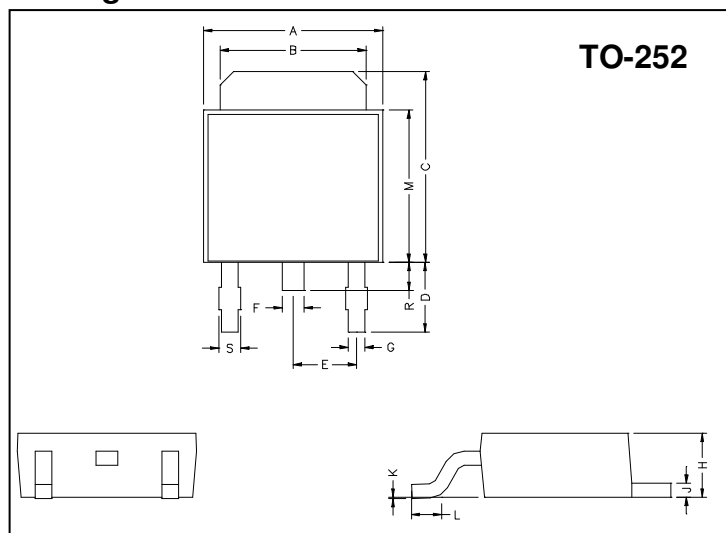
Description

The GJ32C is designed for use in general purpose amplifier and switching applications.

Features

*Complementary to GJ31C

Package Dimensions



REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	6.40	6.80	G	0.50	0.70
B	5.20	5.50	H	2.20	2.40
C	6.80	7.20	J	0.45	0.55
D	2.40	3.00	K	0	0.15
E	2.30 REF.		L	0.90	1.50
F	0.70	0.90	M	5.40	5.80
S	0.60	0.90	R	0.80	1.20

Absolute Maximum Ratings (Ta = 25°C, unless otherwise specified)

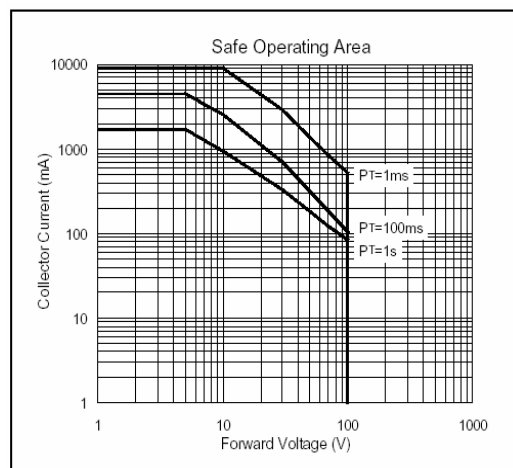
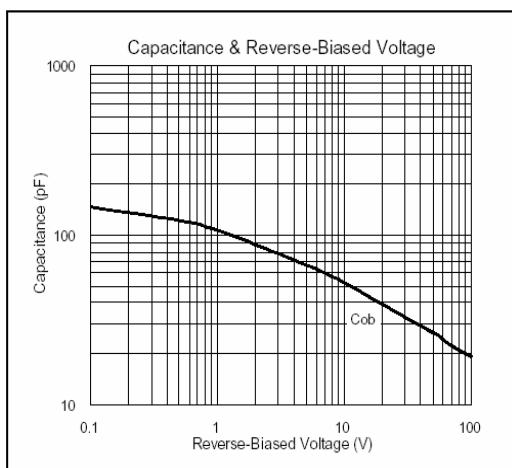
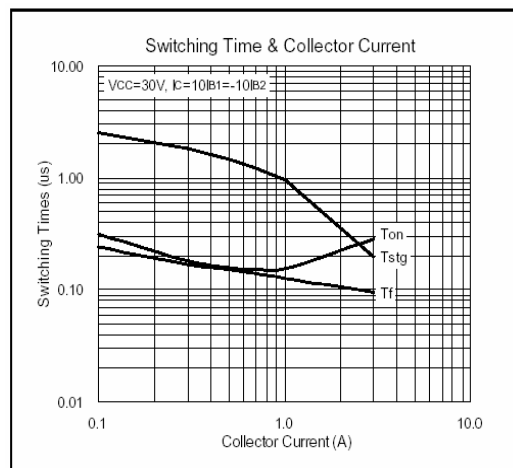
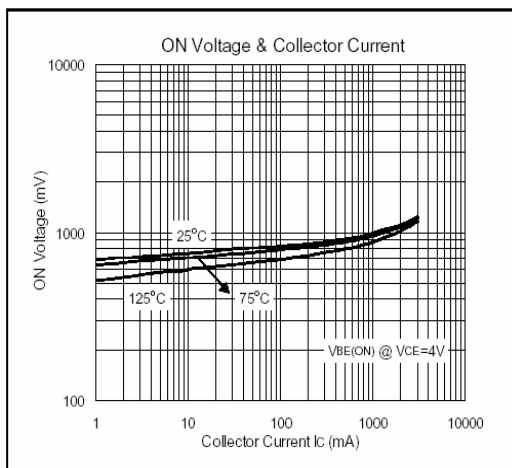
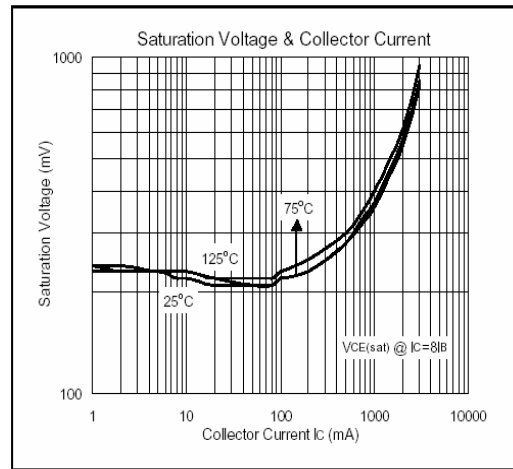
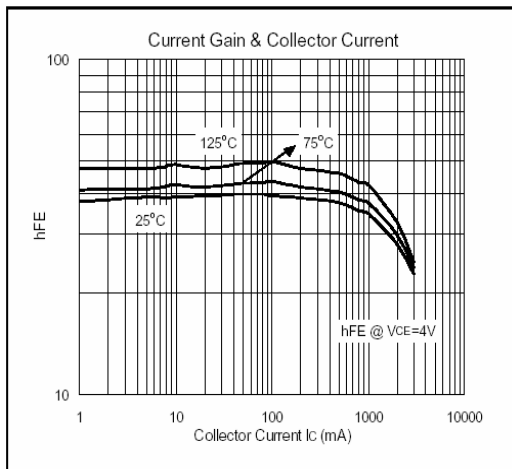
Parameter	Symbol	Ratings	Unit
Collector to Base Voltage	V _{CB0}	-100	V
Collector to Emitter Voltage	V _{CEO}	-100	V
Emitter to Base Voltage	V _{CEO}	-5	V
Collector Current (DC)	I _C	-3	A
Collector Current (Pulse)	I _C	-5	A
Junction Temperature	T _j	+150	°C
Storage Temperature	T _{stg}	-55 ~ +150	°C
Total Power Dissipation	P _D	2	W
	P _D (T _C =25°C)	15	W

Electrical Characteristics (Rating at Ta=25°C)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
BV _{CB0}	-100	-	-	V	I _C =-1mA, I _E =0
BV _{CEO}	-100	-	-	V	I _C =-30mA, I _B =0
BV _{EB0}	-5	-	-	V	I _E =-100uA, I _C =0
I _{CES}	-	-	-20	uA	V _{CE} =-100V, V _{EB} =0V
I _{CEO}	-	-	-50	uA	V _{CE} =-60V, I _B =0
I _{EB0}	-	-	-1	mA	V _{EB} =-5V, I _C =0
*V _{CE(sat)}	-	-	-1.2	V	I _C =-3A, I _B =-375mA
*V _{BE(on)}	-	-	-1.8	V	V _{CE} =-4V, I _C =-3A
*h _{FE1}	25	-	-		V _{CE} =-4V, I _C =-1A
*h _{FE2}	10	-	50		V _{CE} =-4V, I _C =-3A
f _T	3	-	-	MHz	V _{CE} =-10V, I _C =-500mA, f=1MHz

* Pulse Test: Pulse Width ≤ 380μs, Duty Cycle ≤ 2%

Characteristics Curve



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