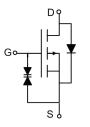


### **Main Product Characteristics:**

$V_{DSS}$	-30 V	
R <sub>DS</sub> (on)	10.6 mΩ(typ.)	
I <sub>D</sub>	-12A	

# D D D D 8 7 6 5 SSF3611E 1 2 3 4 S S S G



Marking and pin
Assignment

Schematic diagram

#### **Features and Benefits:**

- Advanced trench MOSFET process technology
- Special designed for PWM, load switching and general purpose applications
- Ultra low on-resistance with low gate charge
- Fast switching and reverse body recovery
- 150°C operating temperature



## **Description:**

It utilizes the latest trench processing techniques to achieve the high cell density and reduces the on-resistance with high repetitive avalanche rating. These features combine to make this design an extremely efficient and reliable device for use in power switching application and a wide variety of other applications

## **Absolute max Rating:**

Symbol	Max.	Units			
I <sub>D</sub> @ TC = 25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V①	-12			
I <sub>D</sub> @ TC = 100°C	Continuous Drain Current, V <sub>GS</sub> @ 10V①	-7.4	Α		
I <sub>DM</sub>	Pulsed Drain Current②	-48	-48		
P <sub>D</sub> @TC = 25°C	Power Dissipation③	2	W		
V <sub>DS</sub>	Drain-Source Voltage	-30	V		
V <sub>GS</sub>	Gate-to-Source Voltage	± 20	V		
T <sub>J</sub> T <sub>STG</sub>	Operating Junction and Storage Temperature Range	-55 to +150	°C		

#### **Thermal Resistance**

Symbol	Characterizes	Тур.	Max.	Units
$R_{\theta JA}$	Junction-to-ambient (t $\leq 10$ s) $\textcircled{4}$	1	62.5	°C/W



# **Electrical Characterizes** $@T_A$ =25°C unless otherwise specified

Symbol	Parameter		Тур.	Max.	Units	Conditions
V <sub>(BR)DSS</sub>	Drain-to-Source breakdown voltage	-30	_	_	V	V <sub>GS</sub> = 0V, ID = 250μA
В	Static Ducin to Course on registering	_	10.6	13	mΩ	V <sub>GS</sub> =-10.0V, I <sub>D</sub> =-10.0A
R <sub>DS(on)</sub>	Static Drain-to-Source on-resistance	_	14.1	16		V <sub>GS</sub> =-4.50V, I <sub>D</sub> =-7.50A
V <sub>GS(th)</sub>	Gate threshold voltage	1	_	2	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$
I <sub>DSS</sub>	Drain-to-Source leakage current	_	_	-1	μA	V <sub>DS</sub> = -30V,V <sub>GS</sub> = 0V
1	Cata to Source forward lookage	_	_	10		V <sub>GS</sub> = 20V
I <sub>GSS</sub>	Gate-to-Source forward leakage	_	_	-10	μA	V <sub>GS</sub> = -20V
Qg	Total gate charge	_	55	_		I <sub>D</sub> = -10A,
Q <sub>gs</sub>	Gate-to-Source charge	_	3.5	_	nC	V <sub>DS</sub> =-25V,
$Q_{gd}$	Gate-to-Drain("Miller") charge	_	18	_		V <sub>GS</sub> = -10V
t <sub>d(on)</sub>	Turn-on delay time	_	8.0	_		\\ - 40\\ \\DC- 45\\
t <sub>r</sub>	Rise time	_	5.8	_	no	$V_{GS}$ =-10V, VDS=-15V, $R_L$ =15 $\Omega$ ,
t <sub>d(off)</sub>	Turn-Off delay time	_	56	_	ns	
t <sub>f</sub>	Fall time	_	38	_		$R_{GEN}$ =3 $\Omega$
C <sub>iss</sub>	Input capacitance	_	3224	_		V <sub>GS</sub> = 0V
Coss	Output capacitance	_	459	_	pF	V <sub>DS</sub> = -15V
C <sub>rss</sub>	Reverse transfer capacitance		425			f = 1MHz

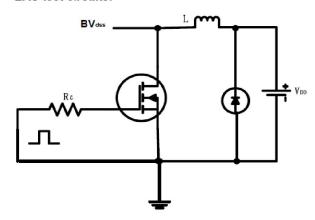
# **Source-Drain Ratings and Characteristics**

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions
I <sub>S</sub>	Continuous Source Current			-12	А	MOSFET symbol
	(Body Diode)	_				showing the
I <sub>SM</sub>	Pulsed Source Current	_	_	-48	А	integral reverse
	(Body Diode)					p-n junction diode.
V <sub>SD</sub>	Diode Forward Voltage	_	-0.73	-1.2	V	I <sub>S</sub> =-2.1A, V <sub>GS</sub> =0V
t <sub>rr</sub>	Reverse Recovery Time	_	16	_	ns	$T_J = 25^{\circ}C$ , $I_F = -10A$ , $di/dt =$
Qrr	Reverse Recovery Charge	_	5.9	_	uC	100A/µs

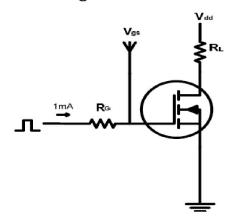


#### **Test circuits and Waveforms**

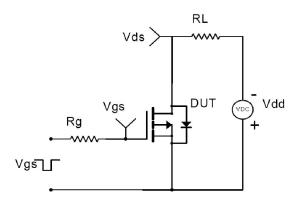
#### EAS test circuits:



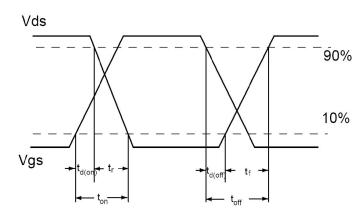
#### Gate charge test circuit:



#### Switch time test circuit:



#### Switch Waveforms:

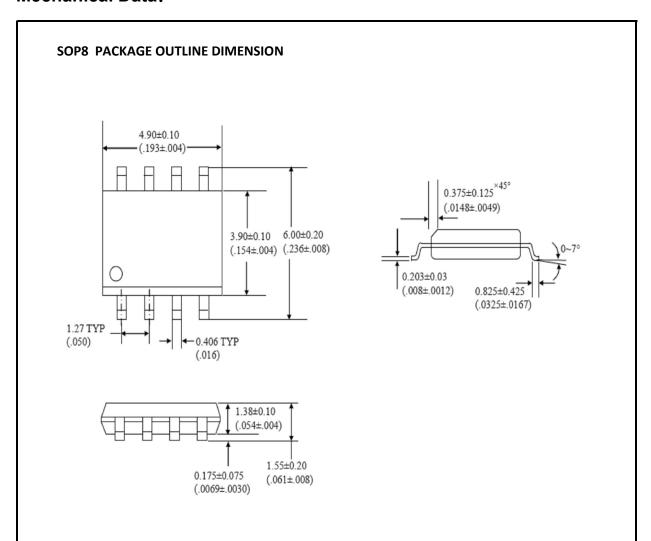


#### Notes:

- ①The maximum current rating is limited by bond-wires.
- ②Repetitive rating; pulse width limited by max. junction temperature.
- ③The power dissipation PD is based on max. junction temperature, using junction-to-ambient thermal resistance.
- 4The value of  $R_{\theta JA}$  is measured with the device mounted on 1in 2 FR-4 board with 2oz. Copper, in a still air environment with TA =25°C
- ⑤These curves are based on the junction-to-case thermal impedence which is measured with the device mounted to a large heatsink, assuming a maximum junction temperature of  $T_{J(MAX)}=150$ °C.
- ⑥ The maximum current rating is limited by bond-wires.



## **Mechanical Data:**



Symbol	Dimension I	n Millimeters	Dimension In Inches		
Symbol	Min	Max	Min	Max	
Α	1.350	1.750	0.053	0.069	
A1	0.100	0.250	0.004	0.010	
A2	1.280	1.480	0.050	0.058	
b	0.4	-06	0.016		
С	0.173	0.233	0.007	0.009	
D	4.800	5.000	0.189	0.197	
E	3.800	4.000	0.150	0.157	
E1	5.800	6.200	0.228	0.244	
е	1.27TYP		0.050TYP		
L	0.400	1.250	0.016	0.050	



# **Ordering and Marking Information**

Device Marking: SSF3611E

Package (Available) SOP-8 Operating Temperature Range

C: -55 to 150 °C

# **Devices per Unit**

Package	Units/	Tapes/Inner	Units/Inner	Inner	Units/Carton
Type	Tape	Box	Box	<b>Boxes/Carton</b>	Box
	_			Box	
SOP-8	2500	2	5000	8	40000