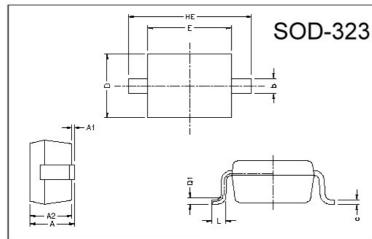
GTM CORPORATION ISSUED DATE :2005/05/20 REVISED DATE :

SURFACE MOUNT, SCHOTTKY BARRIER DIODE VOLTAGE 40V, CURRENT 0.1A

Description

The GD421SD is designed for low power rectification.

Package Dimensions



Markin	ig :		Circuit:			
	Ve		0-	4	-0	
DEE	Milli	Millimeter		Millimeter		
REF.	Min.	Max.	REF.	Min.	Max.	
		1110./1				
A A	0.85	1.05				
			L	0.20	0.40	
A	0.85	1.05	L b	0.20 0.25	0.40	
A A1	0.85 0	1.05 0.10	L b c			
A A1 A2	0.85 0 0.80	1.05 0.10 1.00		0.25	0.40	

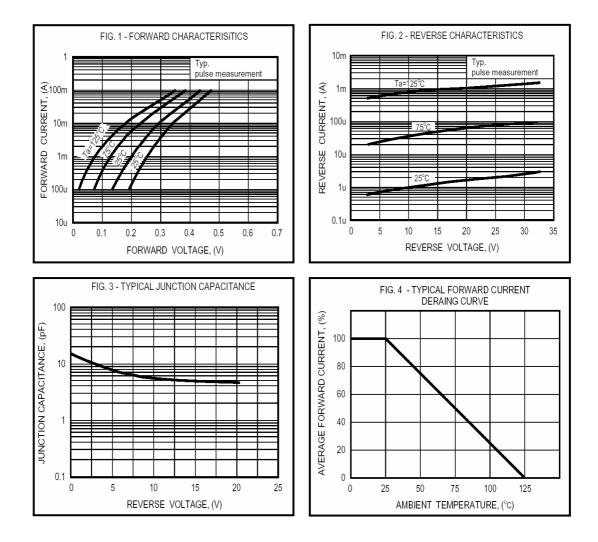
Absolute Maximum Ratings at Ta = 25° C

Parameter	Symbol	Ratings	Unit
Junction Temperature	Tj	+125	°C
Storage Temperature	Tstg	-40 ~ +125	°C
Maximum Recurrent Peak Reverse Voltage	V _{RRM}	40	V
Maximum RMS Voltage	V _{RPS}	28	V
Maximum DC Blocking Voltage	V _{DC}	40	V
Peak Forward Surge Current at 8.3mSec single half sine-wave	I _{FSM}	1.0	А
Typical Junction Capacitance between Terminal	CJ	6.0	pF
Maximum Average Forward Rectified Current	lo	0.1	А
Total Power Dissipation	PD	225	mW

Characteristics at $Ta = 25^{\circ}C$

Parameter	Symbol	Min.	Тур.	Max.	Unit	Test Conditions
Reverse Breakdown Voltage	V(BR)R	40	-	-	V	IR=50µA
Forward Voltage	VF(1)	-	-	340	mV	IF=10mA
	VF(2)	-	-	550	mV	IF=100mA
Reverse Leakage Current	IR	-	-	30	μA	VR=10V

Characteristics Curve



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