

GBJ25A THRU GBJ25M

GLASS PASSIVATED SINGLE PHASE BRIDGE RECTIFIERS

Reverse Voltage - 50 to 1000 V

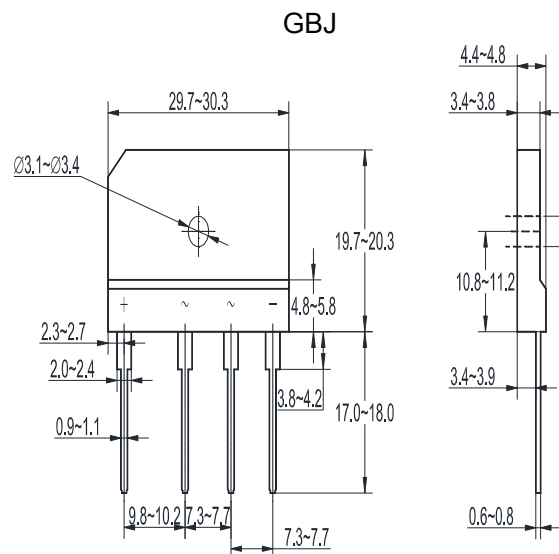
Forward Current - 25 A

Features

- Thin Single In-Line package
- Ideal for printed circuit boards
- Glass passivated chip junction
- Plastic package has Underwriters Laboratory Flammability Classification 94V-0

Mechanical Data

- Case: GBJ
- Terminals: Plated leads solderable per MIL-STD-750 Method 2026
- Polarity: As marked on body

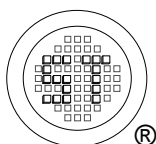


Dimensions in inches and (millimeters)

Maximum Ratings and Electrical Characteristics

Ratings at 25 °C ambient temperature unless otherwise specified.

Parameter		Symbols	GBJ25A	GBJ25B	GBJ25D	GBJ25G	GBJ25J	GBJ25K	GBJ25M	Units
Maximum Repetitive Peak Reverse Voltage		V _{RRM}	50	100	200	400	600	800	1000	V
Maximum RMS Voltage		V _{RMS}	35	70	140	280	420	560	700	V
Maximum DC Blocking Voltage		V _{DC}	50	100	200	400	600	800	1000	V
Maximum Average Forward Rectified Output Current at T _C = 98 °C		I _{F(AV)}	25							A
Peak Forward Surge Current, 8.3 ms Single Half Sine Wave Superimposed on Rated Load (JEDEC Method)		I _{FSM}	350							A
Maximum Forward Voltage per Leg at 12.5 A		V _F	1							V
Maximum DC Reverse Current at Rated DC Blocking Voltage	T _A = 25 °C	I _R	5							μA
	T _A = 125 °C		250							
Operating Junction and Storage Temperature Range		T _j , T _{stg}	- 55 to + 150							°C



SEMTECH ELECTRONICS LTD.



ISO/TS 16949 : 2009
Certificate No. 18071300



ISO14001 : 2004
Certificate No. 7116



ISO 9001 : 2008
Certificate No. 90719410



BS-OHSAS 18001 : 2007
Certificate No. 7116



IECQ QC 080000
Certificate No. PRC-ISO9001-1483-1

GBJ25A THRU GBJ25M

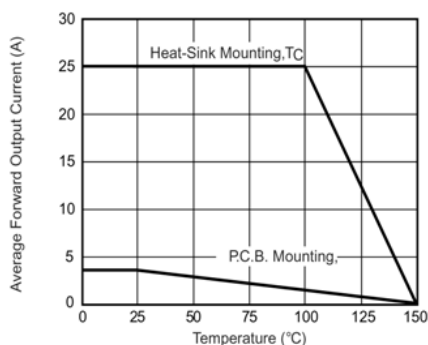


Figure 1. Derating Curve Output Rectified Current

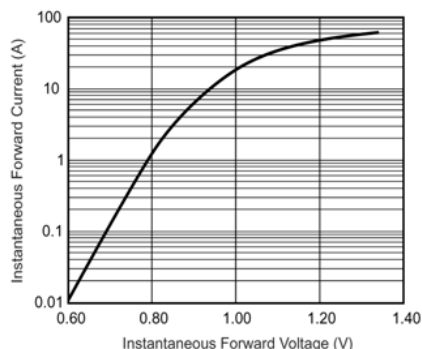


Figure 3. Typical Forward Characteristics Per Leg

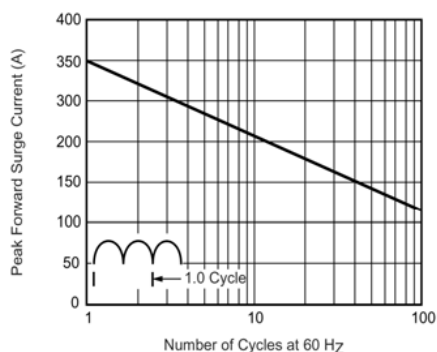


Figure 2. Maximum Non-Repetitive Peak Forward Surge Current Per Leg

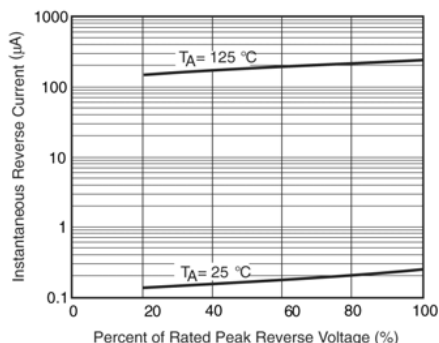


Figure 4. Typical Reverse Characteristics Per Leg

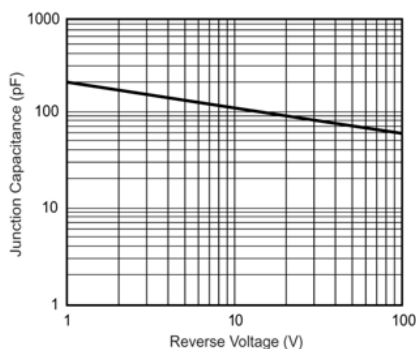


Figure 5. Typical Junction Capacitance Per Leg

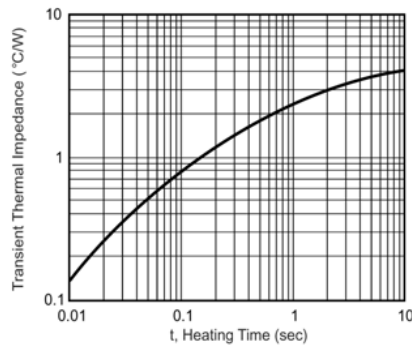
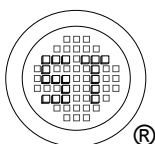


Figure 6. Typical Transient Thermal Impedance



SEMTECH ELECTRONICS LTD.



Dated : 16/08/2016 GD Rev:01