

G8550S

PNP EPITAXIAL PLANAR TRANSISTOR

Description

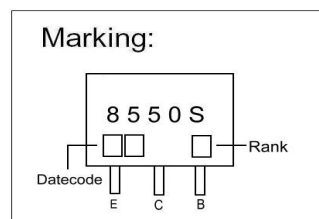
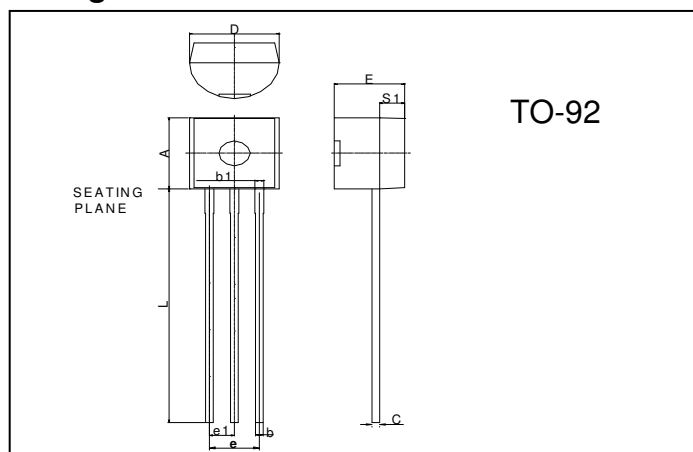
The G8550S is designed for general purpose amplifier applications.

Features

* High DC Current gain: 100-500 at $I_C = 150\text{mA}$

* Complementary to G8050S

Package Dimensions



REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	4.45	4.7	D	4.44	4.7
S1	1.02	-	E	3.30	3.81
b	0.36	0.51	L	12.70	-
b1	0.36	0.76	e1	1.150	1.390
C	0.36	0.51	e	2.42	2.66

Absolute Maximum Ratings at $T_a = 25^\circ\text{C}$

Parameter	Symbol	Ratings	Unit
Junction Temperature	T_j	+150	$^\circ\text{C}$
Storage Temperature	T_{stg}	-55 ~ +150	$^\circ\text{C}$
Collector to Base Voltage	V_{CBO}	-25	V
Collector to Emitter Voltage	V_{CEO}	-20	V
Emitter to Base Voltage	V_{EBO}	-5	V
Collector Current	I_C	-700	mA
Total Power Dissipation	PD	625	mW

Characteristics at $T_a = 25^\circ\text{C}$

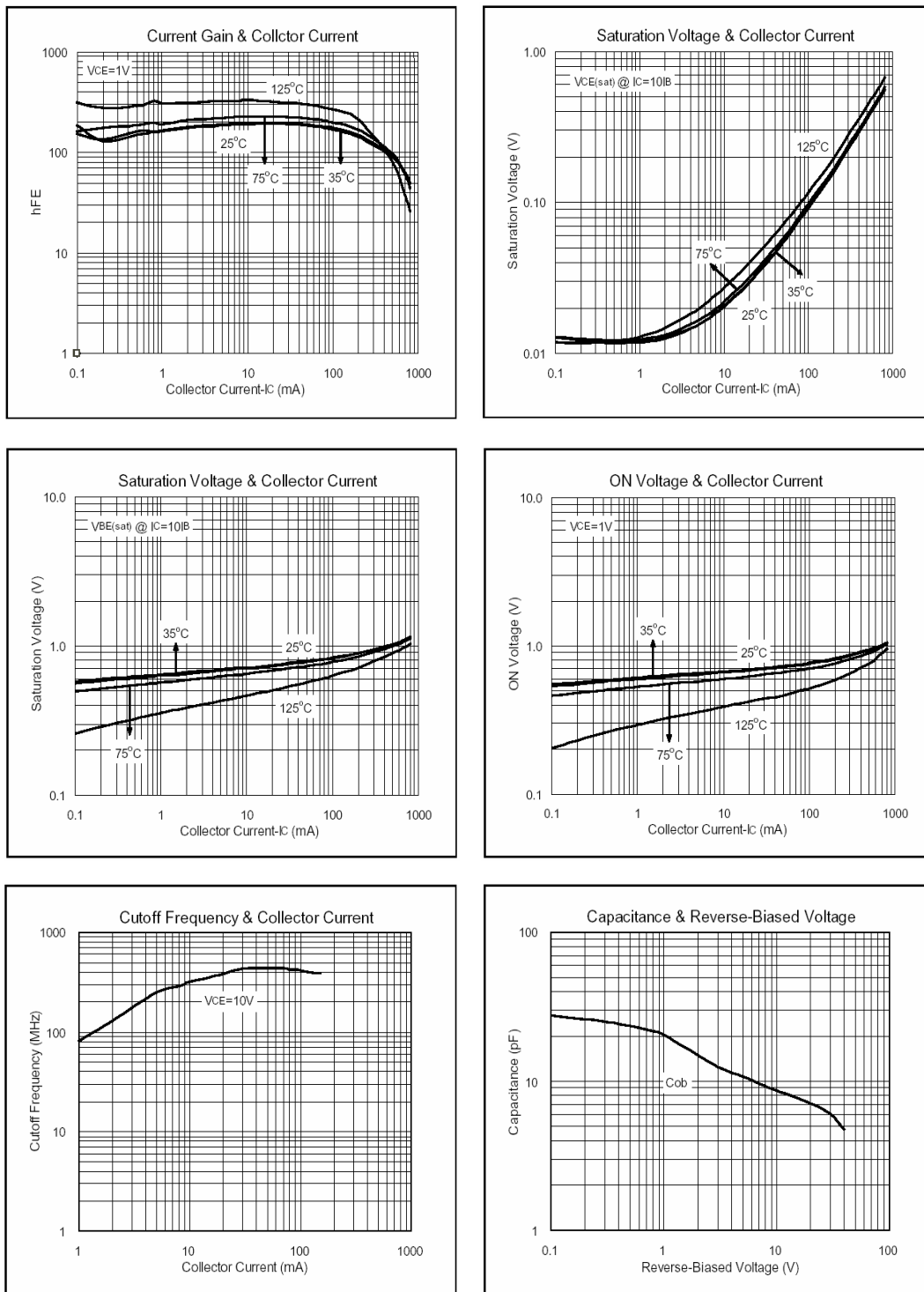
Symbol	Min.	Typ.	Max.	Unit	Test Conditions
V_{CBO}	-25	-	-	V	$I_C = -10\mu\text{A}$, $I_E = 0$
V_{CEO}	-20	-	-	V	$I_C = -1\text{mA}$, $I_B = 0$
V_{EBO}	-5	-	-	V	$I_E = -10\mu\text{A}$, $I_C = 0$
I_{CEX}	-	-	-1	μA	$V_{CE} = -20\text{V}$, $I_E = 0$
* $V_{CE(sat)}$	-	-	-0.5	V	$I_C = -0.5\text{A}$, $I_B = -50\text{mA}$
* $V_{BE(on)}$	-	-	-1	V	$V_{CE} = -1\text{V}$, $I_C = -150\text{mA}$
* h_{FE1}	100	-	500		$V_{CE} = -1\text{V}$, $I_C = -150\text{mA}$
* h_{FE2}	-	100	-		$V_{CE} = -1\text{V}$, $I_C = -500\text{mA}$
fT	150	-	-	MHz	$V_{CE} = -10\text{V}$, $I_C = -20\text{mA}$, $f = 100\text{MHz}$
Cob	-	-	10	pF	$V_{CB} = -10\text{V}$, $f = 1\text{MHz}$

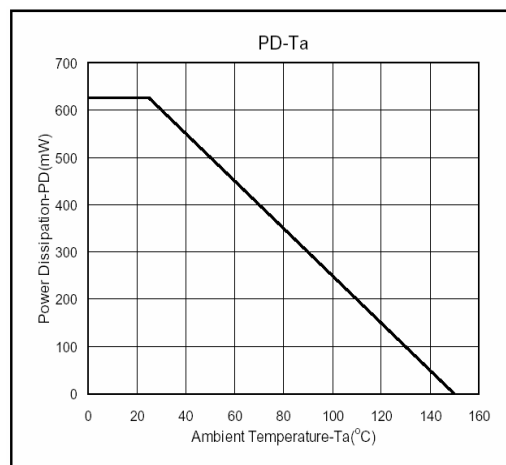
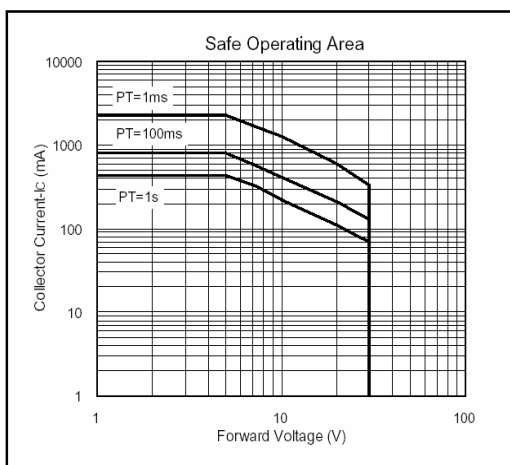
* Pulse Test: Pulse Width $\leq 380\mu\text{s}$, Duty Cycle $\leq 2\%$

Classification Of h_{FE1}

Rank	C	D	E
Range	100-180	160-300	250-500

Characteristics Curve





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