

650V N-Channel MOSFET

Features

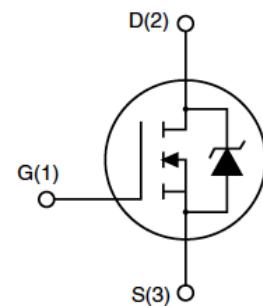
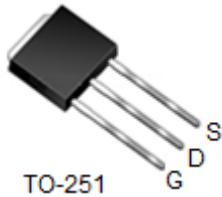
V _{DSS}	R _{DSON} @ 10V (Typ)	I _D
650V	4Ω	2A

- Fast switching
- 100% avalanche tested
- Improved dv/dt capability
- RoHS Compliant

Application

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)

Package



Ordering Information

Part Number	Marking	Case	Packaging
G2N65J	G2N65	TO-251	72pcs/Tube
G2N65F	G2N65	TO-220F	50pcs/Tube
G2N65K	G2N65	TO-252	2500pcs/Reel

Absolute Maximum Ratings T_C = 25°C, unless otherwise noted

Parameter	Symbol	Value			Unit
		TO-220F	TO-251	TO-252	
Drain-Source Voltage (V _{GS} = 0V)	V _{DSS}	650			V
Continuous Drain Current	I _D	2			A
Pulsed Drain Current (note1)	I _{DM}	6			A
Gate-Source Voltage	V _{GSS}	±30			V
Single Pulse Avalanche Energy (note2)	E _{AS}	28.8			mJ
Avalanche Current (note1)	I _{AS}	2.4			A
Repetitive Avalanche Energy (note1)	E _{AR}	17.28			mJ
Power Dissipation (T _C = 25°C)	P _D	20	25		W
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55~+150			°C

Thermal Resistance

Parameter	Symbol	Value			Unit
		TO-220F	TO-251	TO-252	
Thermal Resistance, Junction-to-Case	R _{thJC}	6.25	5		K/W
Thermal Resistance, Junction-to-Ambient	R _{thJA}	62.5	60		

Specifications $T_J = 25^\circ\text{C}$, unless otherwise noted

Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
Static						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	650	--	--	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}} = 650\text{V}, V_{\text{GS}} = 0\text{V}, T_J = 25^\circ\text{C}$	--	--	1	μA
Gate-Source Leakage	I_{GSS}	$V_{\text{GS}} = \pm 30\text{V}$	--	--	± 100	nA
Gate-Source Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	3.0	--	4.0	V
Drain-Source On-Resistance (Note3)	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = 10\text{V}, I_D = 1.0\text{A}$	--	4	4.8	Ω
Dynamic						
Input Capacitance	C_{iss}	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 25\text{V}, f = 1.0\text{MHz}$	--	250	--	pF
Output Capacitance	C_{oss}		--	30	--	
Reverse Transfer Capacitance	C_{rss}		--	4.2	--	
Total Gate Charge	Q_g	$V_{\text{DD}} = 520\text{V}, I_D = 2.0\text{A}, V_{\text{GS}} = 10\text{V}$	--	10.6	--	nC
Gate-Source Charge	Q_{gs}		--	1.5	--	
Gate-Drain Charge	Q_{gd}		--	5.8	--	
Turn-on Delay Time	$t_{d(\text{on})}$	$V_{\text{DD}} = 300\text{V}, I_D = 2.0\text{A}, R_G = 25 \Omega$	--	33.6	--	ns
Turn-on Rise Time	t_r		--	7.2	--	
Turn-off Delay Time	$t_{d(\text{off})}$		--	64	--	
Turn-off Fall Time	t_f		--	31	--	
Drain-Source Body Diode Characteristics						
Continuous Body Diode Current	I_S	$T_C = 25^\circ\text{C}$	--	--	2	A
Pulsed Diode Forward Current	I_{SM}		--	--	8	
Body Diode Voltage	V_{SD}	$T_J = 25^\circ\text{C}, I_{\text{SD}} = 1.0\text{A}, V_{\text{GS}} = 0\text{V}$	--	--	1.4	V
Reverse Recovery Time	t_{rr}	$V_{\text{GS}} = 0\text{V}, I_S = 2.0\text{A}, dI_F/dt = 100\text{A}/\mu\text{s}$	--	500	--	ns
Reverse Recovery Charge	Q_{rr}		--	6	--	μC

Notes

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. $L = 10.0\text{mH}, V_{\text{DD}} = 50\text{V}, R_G = 25 \Omega$, Starting $T_J = 25^\circ\text{C}$
3. Pulse Test: Pulse width $\leq 300\mu\text{s}$, Duty Cycle $\leq 1\%$

Typical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted

Figure 1. Output Characteristics ($T_J = 25^\circ\text{C}$)

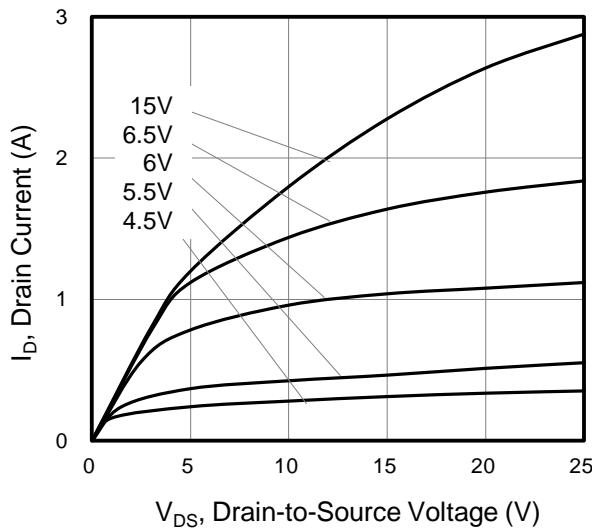


Figure 2. Body Diode Forward Voltage

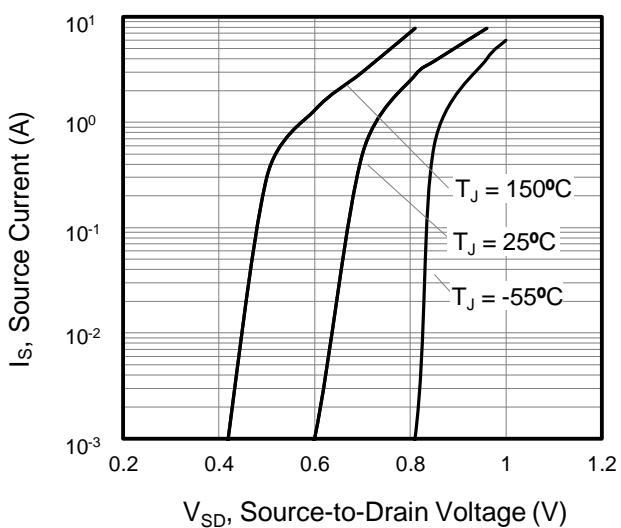
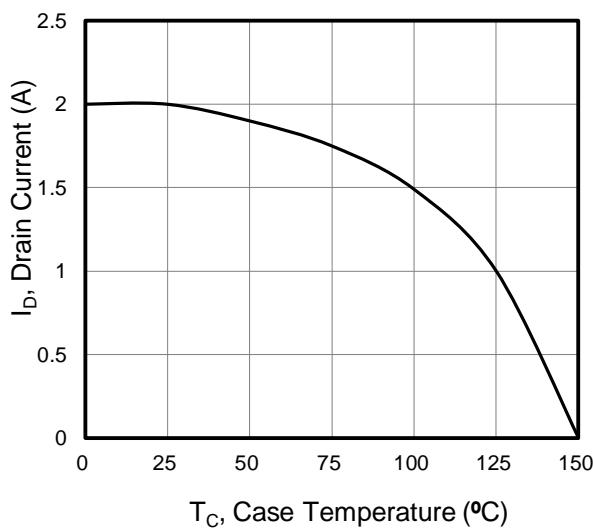


Figure 3. Drain Current vs. Temperature



**Figure 4. Power Dissipation vs. Temperature
TO-251, TO-252**

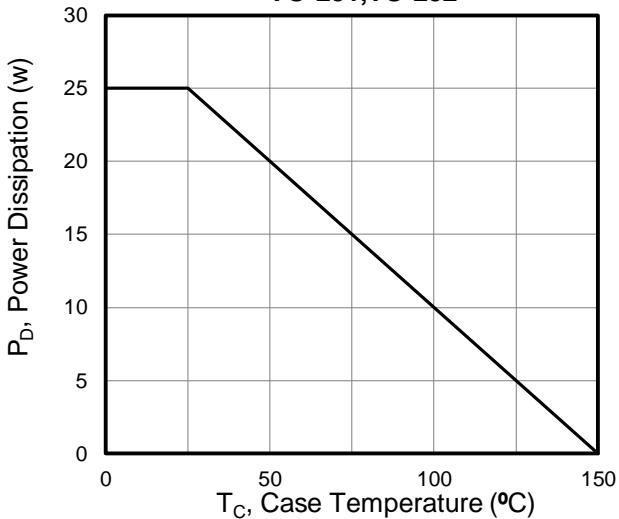


Figure 5. Transfer Characteristics

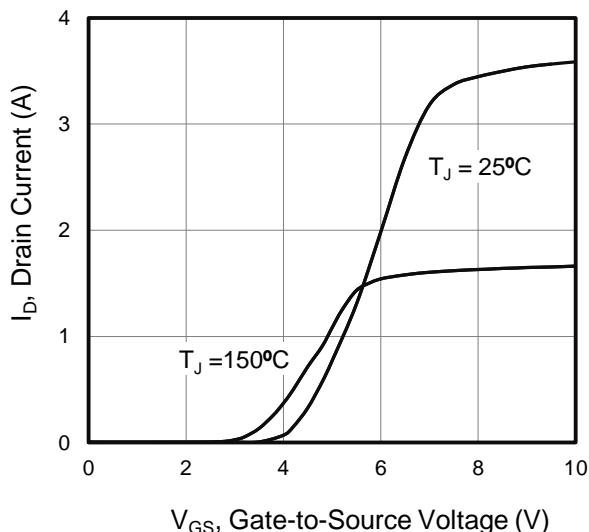
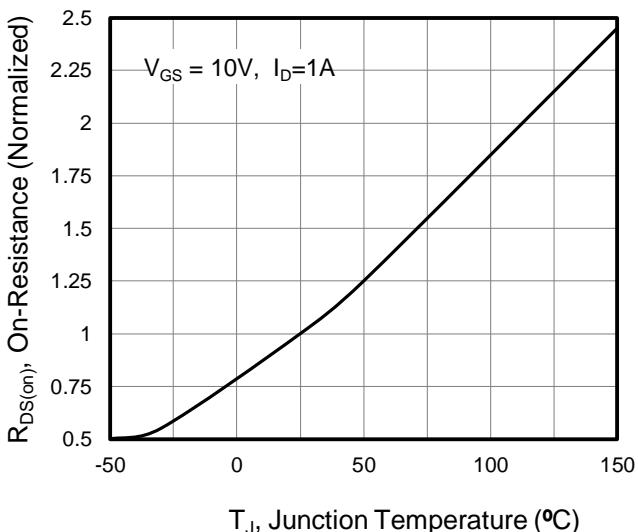


Figure 6. On-Resistance vs. Temperature



Typical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted

Figure 7. Capacitance

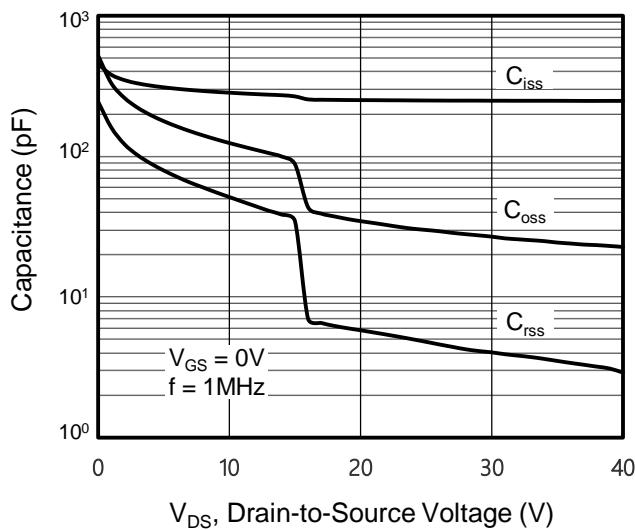
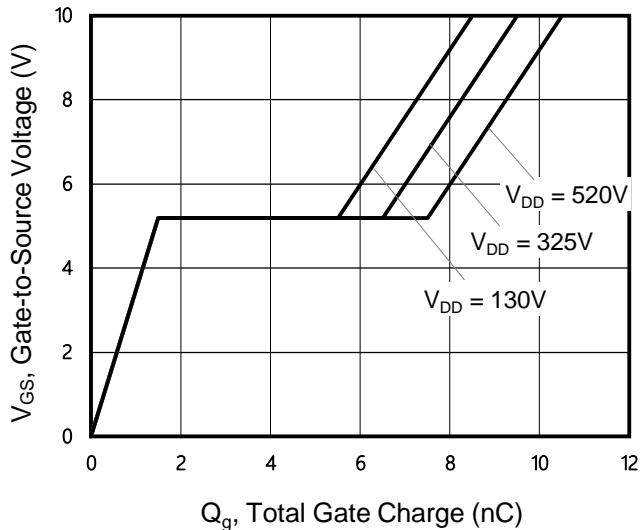
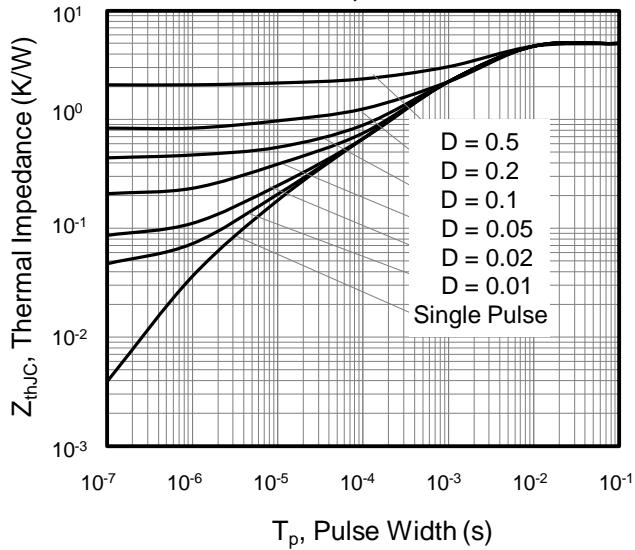


Figure 8. Gate Charge



**Figure 9. Transient Thermal Impedance
TO-251, TO-252**



**Figure 10. Transient Thermal Impedance
TO-220F**

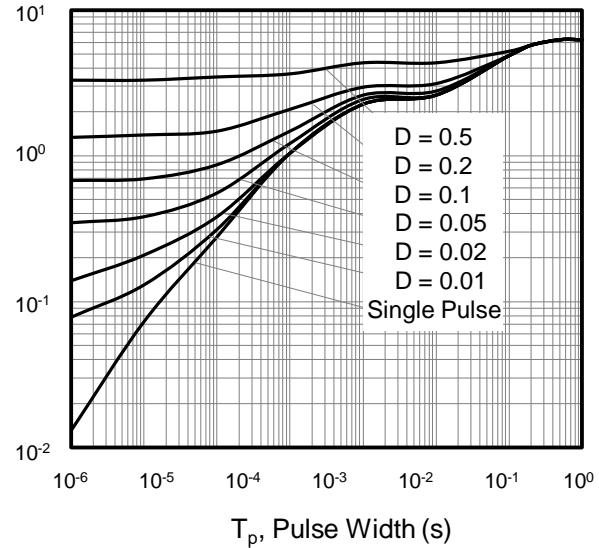


Figure A: Gate Charge Test Circuit and Waveform

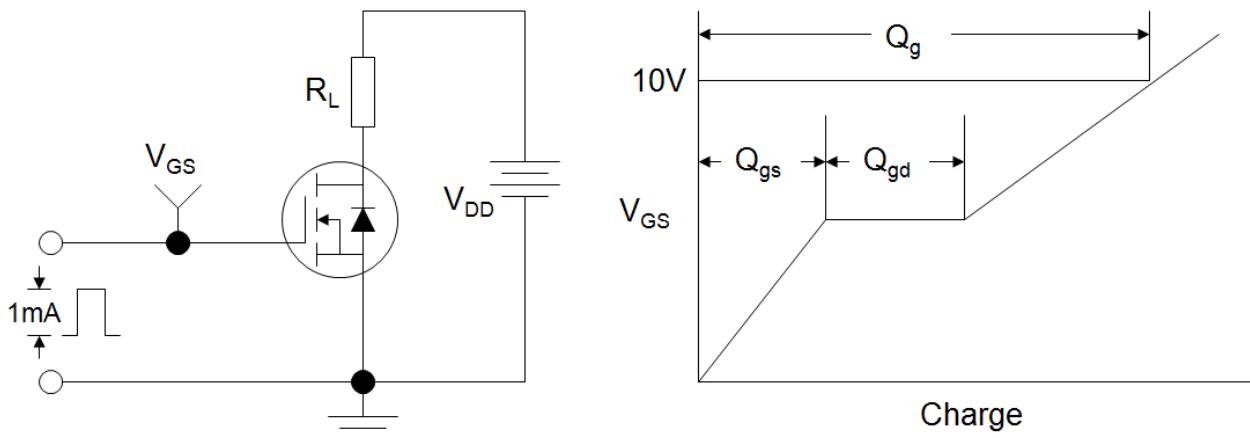


Figure B: Resistive Switching Test Circuit and Waveform

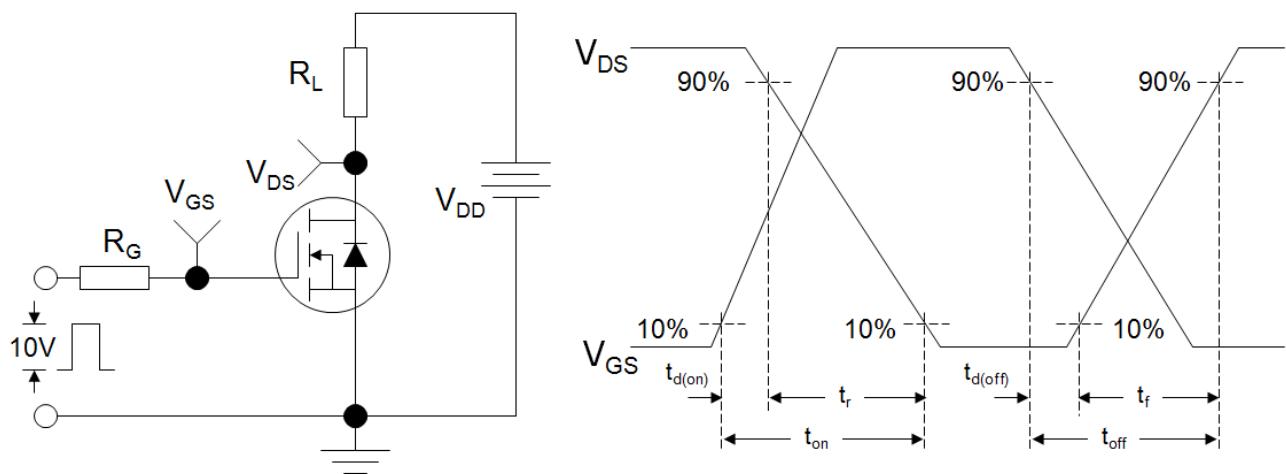
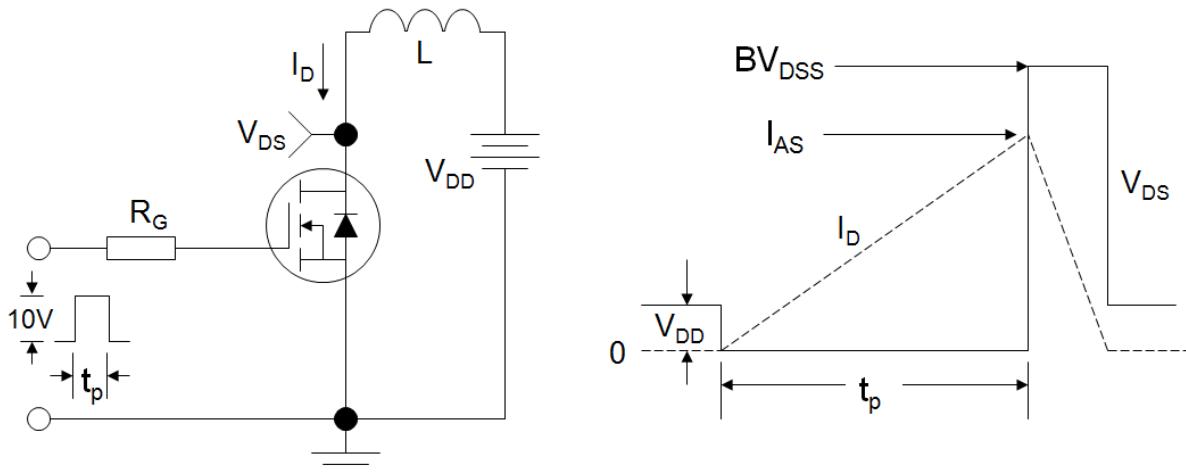
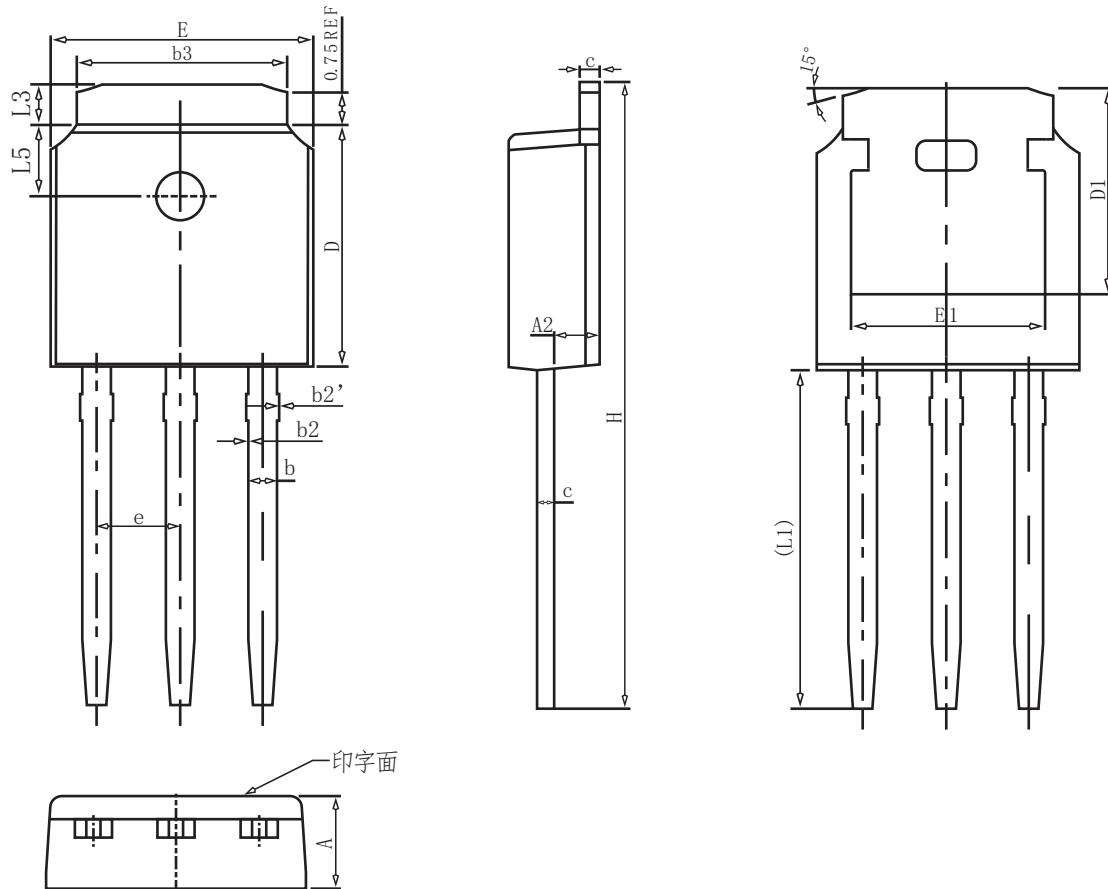


Figure C: Unclamped Inductive Switching Test Circuit and Waveform



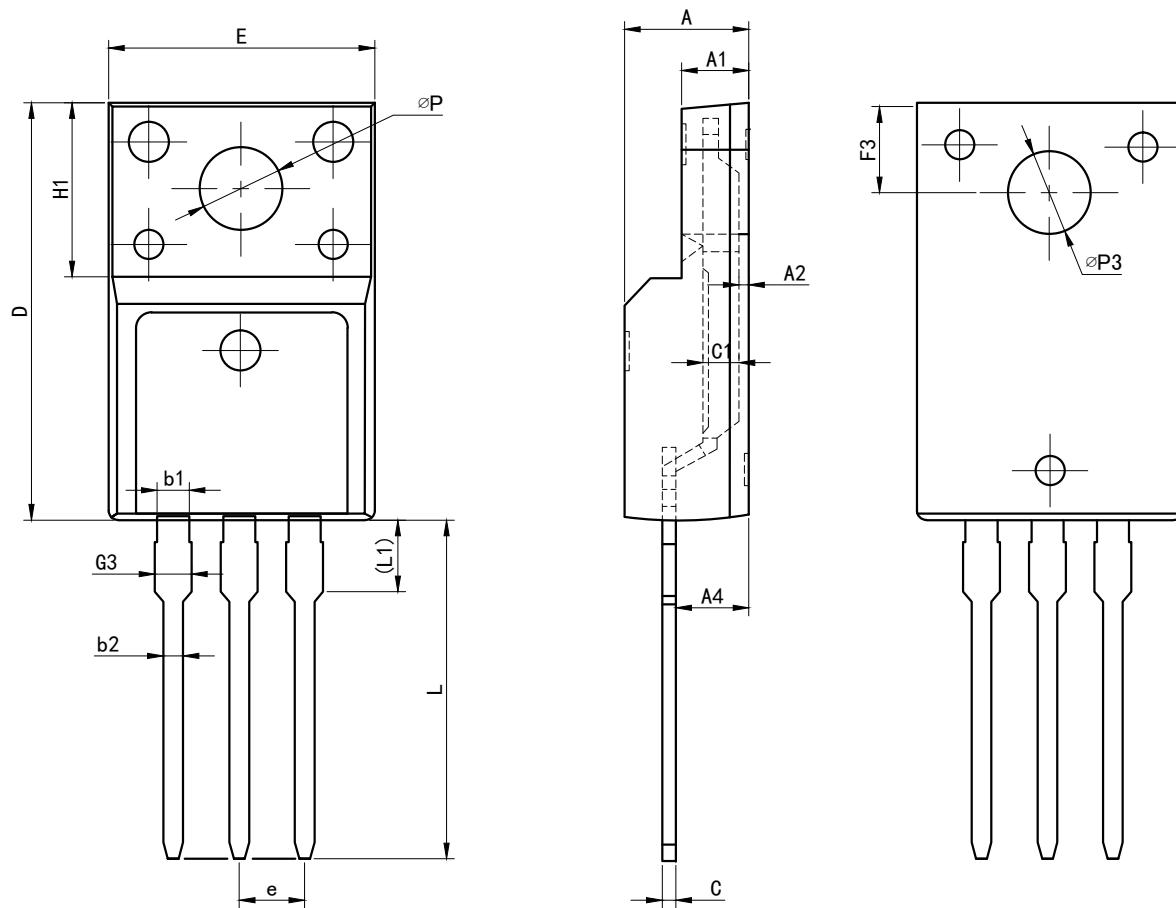
TO-251 Package information



COMMON DIMENSIONS

SYMBOL	mm		
	MIN	NOM	MAX
A	2.20	2.30	2.40
A2	0.97	1.07	1.17
b	0.68	0.78	0.90
b2	0.00	0.04	0.10
b2'	0.00	0.04	0.10
b3	5.20	5.33	5.50
c	0.43	0.53	0.63
D	5.98	6.10	6.22
D1	5.30REF		
E	6.40	6.60	6.80
E1	4.63	-	-
e	2.286BSC		
H	16.22	16.52	16.82
L1	9.15	9.40	9.65
L3	0.88	1.02	1.28
L5	1.65	1.80	1.95

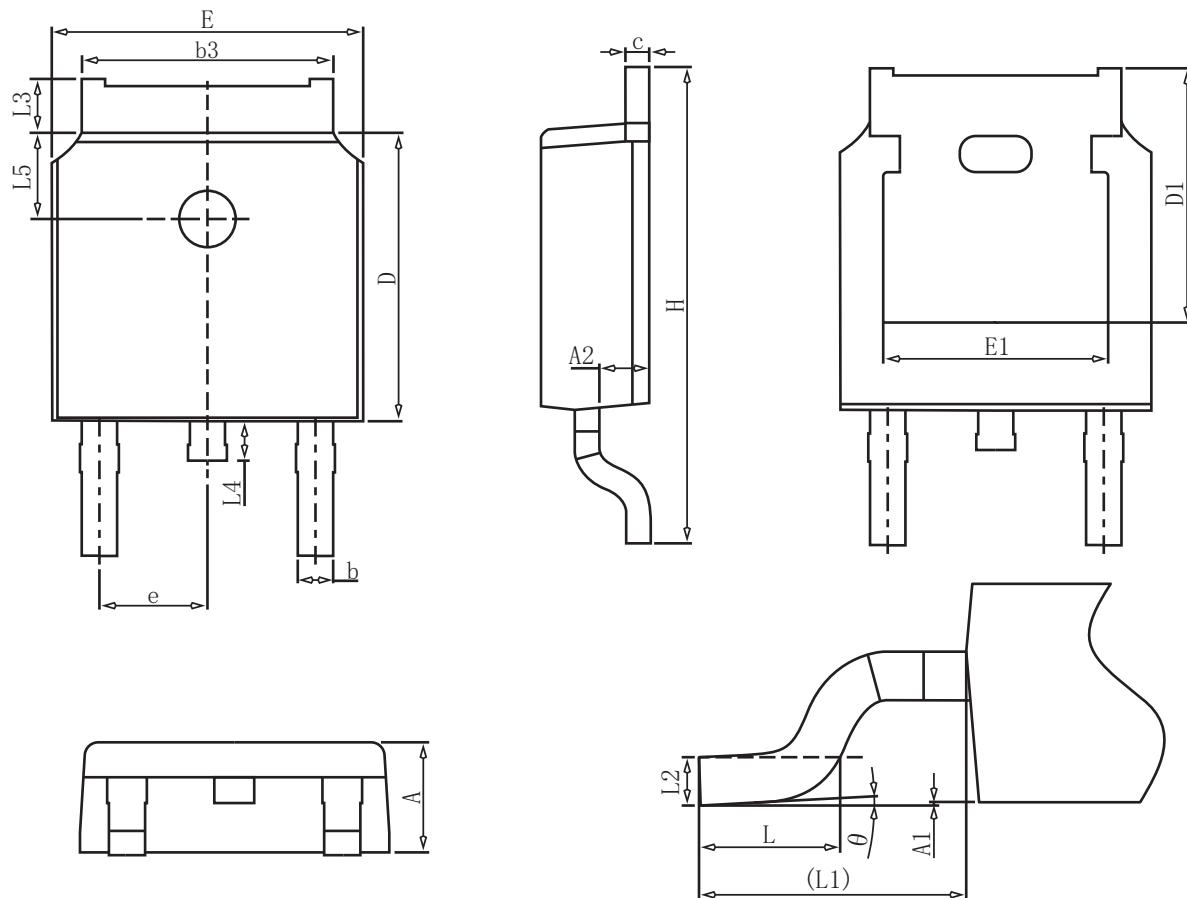
TO-220F Package information



COMMON DIMENSIONS

SYMBO	mm		
	MIN	NOM	MAX
E	9. 96	10. 16	10. 36
A	4. 50	4. 70	4. 90
A1	2. 34	2. 54	2. 74
A2	0. 30	0. 45	0. 60
A4	2. 56	2. 76	2. 96
c	0. 40	0. 50	0. 65
c1	1. 20	1. 30	1. 35
D	15. 57	15. 87	16. 17
H1	6. 70REF		
e	2. 54BSC		
L	12. 68	12. 98	13. 28
L1	2. 93	3. 03	3. 13
ØP	3. 03	3. 18	3. 38
ØP3	3. 15	3. 45	3. 65
F3	3. 15	3. 30	3. 45
G3	1. 25	1. 35	1. 55
b1	1. 18	1. 28	1. 43
b2	0. 70	0. 80	0. 95

TO-252 Package information



COMMON DIMENSIONS

SYMBOL	mm		
	MIN	NOM	MAX
A	2.20	2.30	2.40
A1	0.00	-	0.20
A2	0.97	1.07	1.17
b	0.68	0.78	0.90
b3	5.20	5.33	5.50
c	0.43	0.53	0.63
D	5.98	6.10	6.22
D1		5.30REF	
E	6.40	6.60	6.80
E1	4.63	-	-
e		2.286BSC	
H	9.40	10.10	10.50
L	1.38	1.50	1.75
L1		2.90REF	
L2		0.51BSC	
L3	0.88	-	1.28
L4	0.50	-	1.00
L5	1.65	1.80	1.95
θ	0°	-	8°