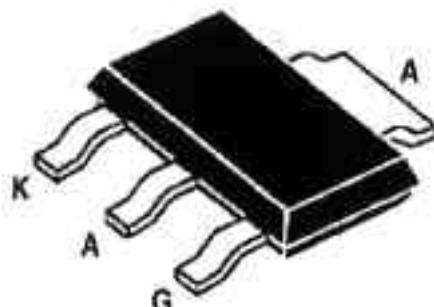


SURFACE MOUNT SCR

SOT223 (Plastic)	On-State Current 1.25 Amp	Gate Trigger Current < 200 μ A
	Off-State Voltage 200V ÷ 800V	
<p>These series of Silicon Controlled Rectifier use a high performance PNPN technology.</p> <p>These parts are intended for general purpose applications where high gate sensitivity is required using surface mount technology.</p>		

Absolute Maximum Ratings, according to IEC publication No. 134

SYMBOL	PARAMETER	CONDITIONS	Min.	Max.	Unit
$I_{T(RMS)}$	On-state Current*	Half Cycle, $\theta = 180^\circ$, $T_{tab} = 95^\circ\text{C}$	1.25		A
$I_{T(AV)}$	Average On-state Current*	Half Cycle, $\theta = 180^\circ$, $T_{tab} = 95^\circ\text{C}$	0.8		A
I_{TSM}	Non-repetitive On-State Current	Half Cycle, 60 Hz, $T_j = 25^\circ\text{C}$	25		A
I_{TSM}	Non-repetitive On-State Current	Half Cycle, 50 Hz, $T_j = 25^\circ\text{C}$	22.5		A
I^2t	Fusing Current	$t_p = 10\text{ms}$, Half Cycle	2.5		A^2s
V_{GRM}	Peak Reverse Gate Voltage	$I_{GR} = 10 \mu\text{A}$, $T_j = 25^\circ\text{C}$	8		V
I_{GM}	Peak Gate Current	20 μs max.		1.2	A
P_{GM}	Peak Gate Dissipation	20 μs max.		3	W
$P_{G(AV)}$	Gate Dissipation	20 ms max.		0.2	W
T_j	Operating Temperature		-40	+125	$^\circ\text{C}$
T_{stg}	Storage Temperature		-40	+150	$^\circ\text{C}$
T_{sld}	Soldering Temperature	10s max.		260	$^\circ\text{C}$

* with 5 cm^2 copper ($e = 35\mu\text{m}$) surface under tab.

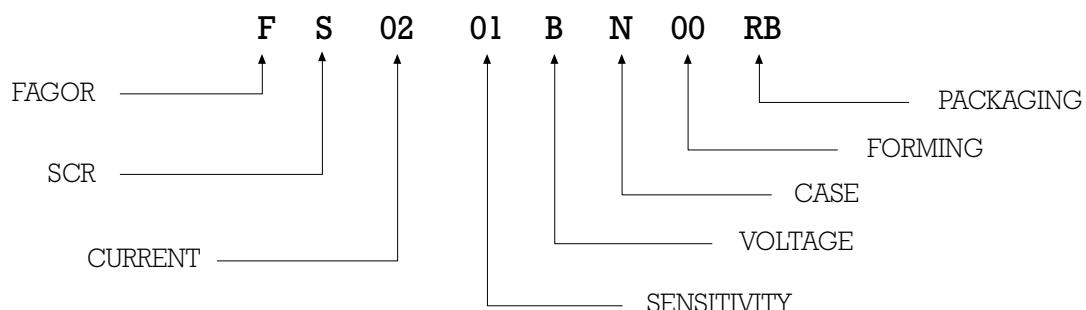
SYMBOL	PARAMETER	CONDITIONS	VOLTAGE				Unit
			B	D	M	N	
V_{DRM}	Repetitive Peak Off State Voltage	$R_{GK} = 1\text{ K}$	200	400	600	800	V
V_{RRM}							

SURFACE MOUNT SCR

Electrical Characteristics

SYMBOL	PARAMETER	CONDITIONS	SENSITIVITY				Unit
			01	04	02	03	
I _{GT}	Gate Trigger Current	V _D = 12 V _{DC} , R _L = 140 , T _j = 25 °C	MIN MAX	1 20	15 50	200 200	μA
I _{DRM} / I _{RRM}	Off-State Leakage Current	V _D = V _{DRM} , R _{GK} = 1K , T _j = 125 °C V _R = V _{RRM} , T _j = 25 °C	MAX MAX		500 5		μA
V _{TM}	On-state Voltage	at I _T = 1.6 Amp, t _p = 380 μs, T _j = 25 °C	MAX		1.45		V
V _{T(O)}	On-state Threshold Voltage	T _j = 125 °C	MAX		0.9		V
r _d	Dinamic Resistance	T _j = 125 °C	MAX		150		m
V _{GT}	Gate Trigger Voltage	V _D = 12 V _{DC} , R _L = 140 , T _j = 25 °C	MAX		0.8		V
V _{GD}	Gate Non Trigger Voltage	V _D = V _{DRM} , R _L = 3.3K , R _{GK} = 1K , T _j = 125 °C	MIN		0.1		V
I _H	Holding Current	I _T = 50 mA, R _{GK} = 1K , T _j = 25 °C	MAX		5		mA
I _L	Latching Current	I _G = 1 mA, R _{GK} = 1K , T _j = 25 °C	MAX		6		mA
dv / dt	Critical Rate of Voltage Rise	V _D = 0.67 x V _{DRM} , R _{GK} = 1K , T _j = 125 °C	MIN	15	15	10	20
di / dt	Critical Rate of Current Rise	I _G = 2 x I _{GT} Tr 100 ns, F = 60 Hz, T _j = 125 °C	MIN		50		A/μs
R _{th(j-l)}	Thermal Resistance Junction-Leads for DC				25		°C/W
R _{th(j-a)}	Thermal Resistance Junction-Ambient				60		°C/W

PART NUMBER INFORMATION



SURFACE MOUNT SCR

Fig. 1: Maximum average power dissipation versus average on-state current

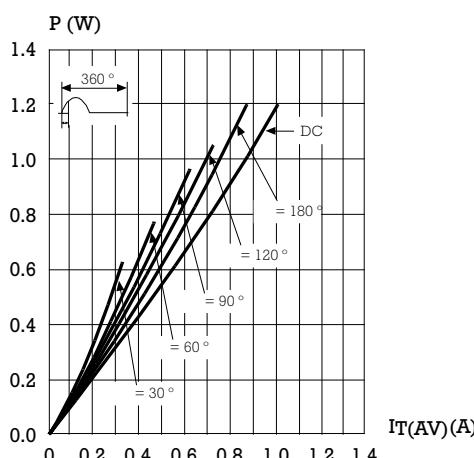


Fig. 3: Average on-state current versus tab temperature

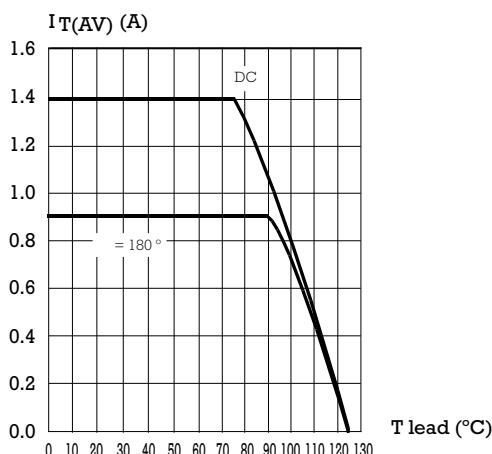


Fig. 5: Relative variation of gate trigger current and holding current versus junction temperature.

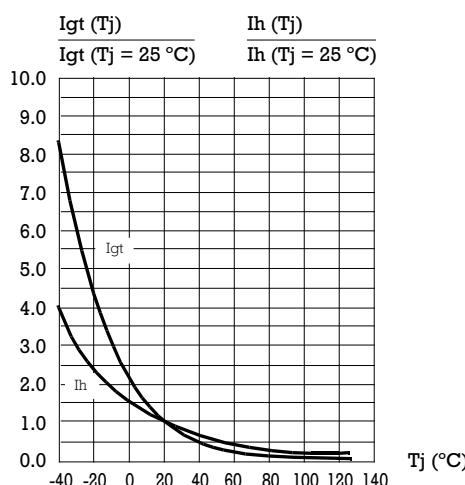


Fig. 2: Correlation between maximum average power dissipation and maximum allowable temperature (Tamb and Tab).

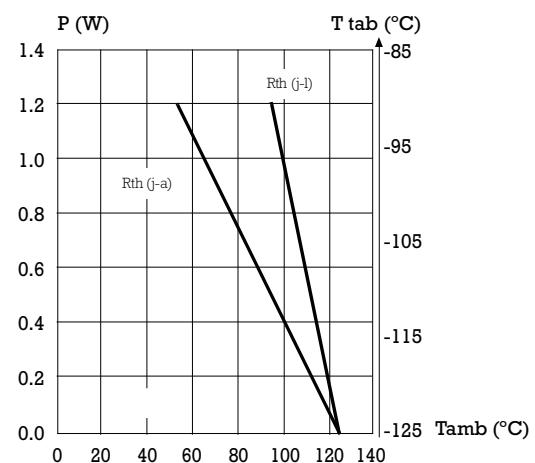


Fig. 4: Relative variation of thermal impedance junction to ambient versus pulse duration.

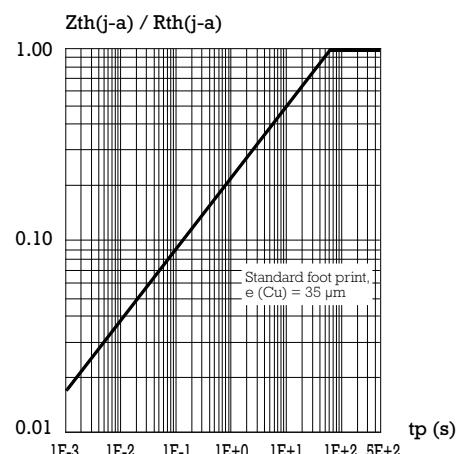
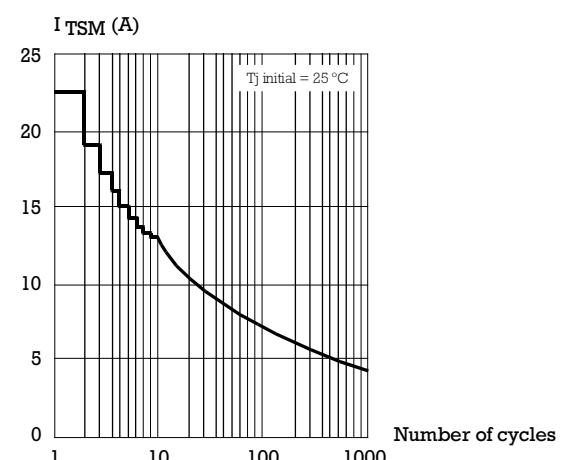


Fig. 6: Non repetitive surge peak on-state current versus number of cycles.



SURFACE MOUNT SCR

Fig. 7: Non repetitive surge peak on-state current for a sinusoidal pulse with width: $t_p = 10 \text{ ms}$, and corresponding value of I^2t .

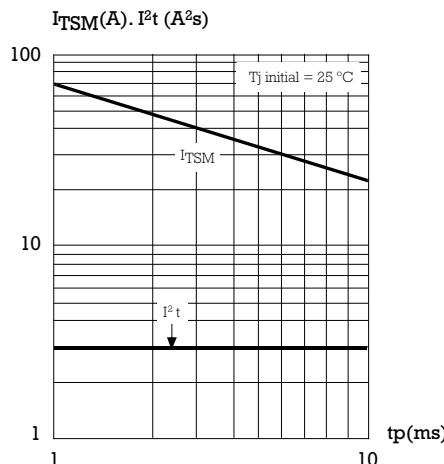
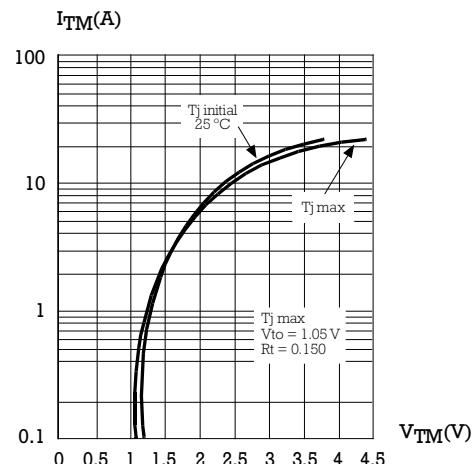
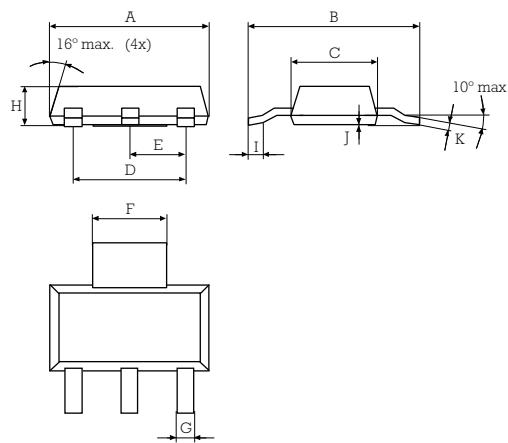


Fig. 8: On-state characteristics (maximum values).



PACKAGE MECHANICAL DATA SOT223 (Plastic)



REF.	DIMENSIONS		
	Milimeters		
	Min.	Typ.	Max.
A	6.30	6.50	6.70
B	6.70	7.00	7.30
C	3.30	3.50	3.70
D	-	4.60	-
E	-	2.30	-
F	2.95	3.00	3.15
G	0.65	0.70	0.85
H	1.50	1.60	1.70
I	0.50	0.60	0.70
J	-	0.02	0.05
K	0.25	0.30	0.35

Weight: 0.11 g

FOOT PRINT

