

12-Element Phototransistor Card Reader Array

Optoelectronic Products

FPA710 FPA710A

General Description

The FPA710 and FPA710A are 12-element npn Planar phototransistor arrays with exceptionally stable characteristics and high illumination sensitivity. Each transistor is electrically isolated and mounted on 250 mil centers. The case is a plastic compound with transparent resin encapsulation that exhibits stable characteristics under high humidity conditions.

High Illumination Sensitivity

Especially Designed For Punched Or Marked Card Reading And Optical Encoder Applications

Absolute Maximum Ratings

Maximum Temperatures and Humidity

Storage Temperature	-40°C to +100°C
Operating Temperature	-40°C to +85°C
Pin Temperature (Soldering, 10 s)	260°C
Relative Humidity at 65°C	85%

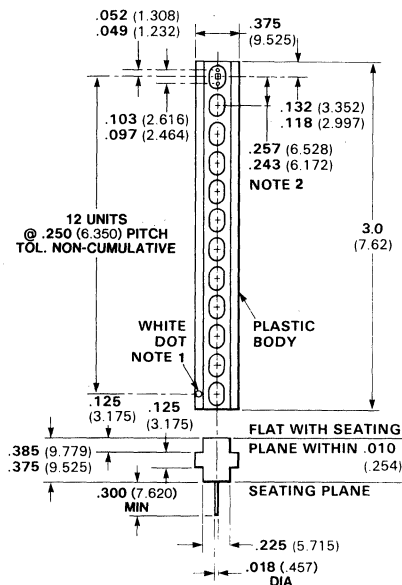
Maximum Power Dissipation per Cell

Total Dissipation at $T_C = 25^\circ\text{C}$	200 mW
Derate Linearly from 25°C	3.33 mW/°C
Total Dissipation at $T_A = 25^\circ\text{C}$	133 mW
Derate Linearly from 25°C	2.22 mW/°C

Maximum Voltages and Currents (Note 1)

$V_{CE(sus)}$	Collector-to-Emitter Sustaining Voltage	20 V
I_C	Collector Current	25 mA

Package Outline



Notes

1. Emitter terminal side of phototransistor (sensor array) or anode terminal side of diode (source array) defined by white dot.
2. The center of each element is aligned to $\pm .010$ " along the length and $\pm .005$ " across the width.
3. All dimensions in inches **bold** and millimeters (parentheses).
4. Tolerance unless specified = ± 0.15 (0.381).
5. Other packages following.

Typical Electrical Characteristics

FPA710 FPA710A

Electrical Characteristics $T_A = 25^\circ\text{C}$

Symbol	Characteristic	Min	Typ	Max	Units	Test Conditions
$V_{CE(sus)}$	Collector-to-Emitter Sustaining Voltage (Note 2)	20	35		V	$I_C = 1.0\text{ mA}$, pulsed
BV_{ECO}	Emitter-to-Collector Breakdown Voltage (Note 2)		7.0		V	$I_{EC} = 100\text{ }\mu\text{A}$
$V_{CE(sat)}$	Collector-to-Emitter Saturation Voltage		0.16	0.33	V	$I_C = 500\text{ }\mu\text{A}$, $H = 20\text{ mW/cm}^2$
I_{CEO}	Collector Dark Current (Note 2)		4.0	100	nA	$V_{CE} = 5.0\text{ V}$
$I_{CE(I_t)}$	Photo Current, Tungsten Source (Note 3)	200	750		μA	$V_{CE} = 5.0\text{ V}$, $H = 5\text{ mW/cm}^2$
$I_{CE(I_t)}$	Photo Current, Tungsten Source (Note 3)		1.75		mA	$V_{CE} = 5.0\text{ V}$, $H = 10\text{ mW/cm}^2$
$I_{CE(I_t)}$	Photo Current, GaAs Source (Note 4)		2.25		mA	$V_{CE} = 5.0\text{ V}$, $H = 5\text{ mW/cm}^2$
t_r	Light Current Rise Time (Note 6)		4.0		μs	GaAs, $I_C = 2.0\text{ mA}$, $R_L = 100\text{ }\Omega$, $V_{CC} = 5.0\text{ V}$
t_f	Light Current Fall Time (Note 6)		4.0		μs	
S_{min}/S_{max}	Matching Factor (Notes 3 and 5) FPA710 FPA710A	0.5 0.75	0.65 0.85	1.0 1.0		$V_{CE} = 5.0\text{ V}$, $H = 5\text{ mW/cm}^2$

Notes

- These are steady-state limits. The factory should be consulted on applications involving pulsed or low duty cycle operation.
- Measured with radiation flux intensity of less than $0.1\text{ }\mu\text{W/cm}^2$ over the spectrum from 0.1 micron to 1.5 microns.
- Measured at noted irradiance as emitted from a tungsten lamp at a color temperature of 2854°K . The effective photosensitive area is (0.8 mm^2) .
Illuminance (in lumens/ft²) = irradiance H (in mW/cm^2) $\times 20$ at a color temperature of 2854°K .
- Measured at an irradiance of 5.0 mW/cm^2 as emitted from a gallium arsenide diode.
- Matching factor is the ratio of minimum sensitivity to maximum sensitivity of any two cells.
- Rise time is defined as the time required for I_{CE} to rise from 10% to 90% of the peak value. Fall time is defined as the time required for I_{CE} to decrease from 90% to 10% of peak value.

Photo Current Characteristics

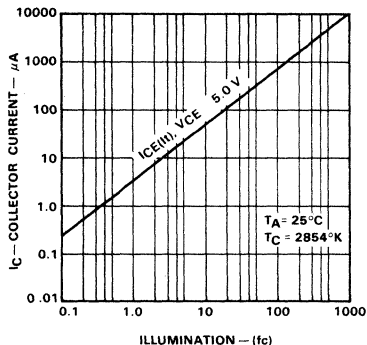
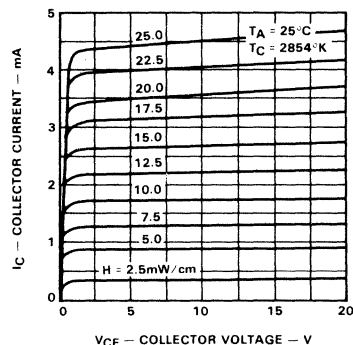


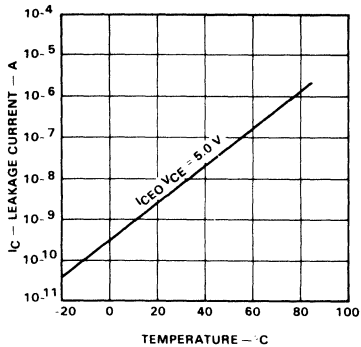
Photo Current vs Collector Voltage



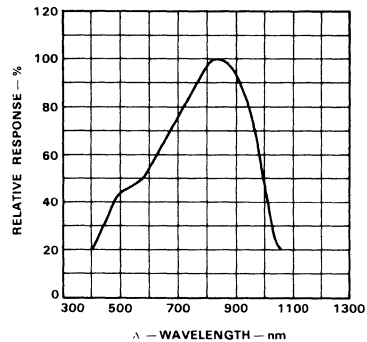
Typical Electrical Characteristic Curves

FPA710 FPA710A

Collector Dark Current vs Temperature

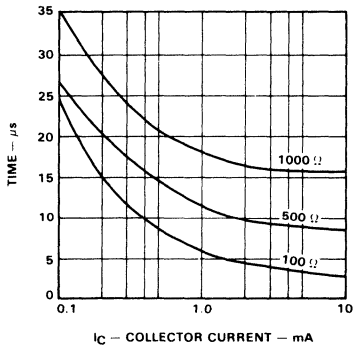


Relative Spectral Response

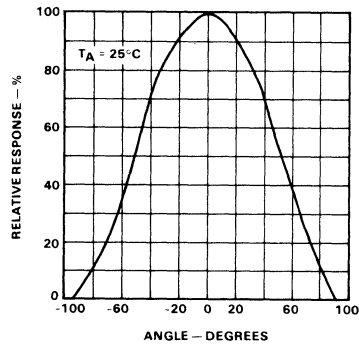


4

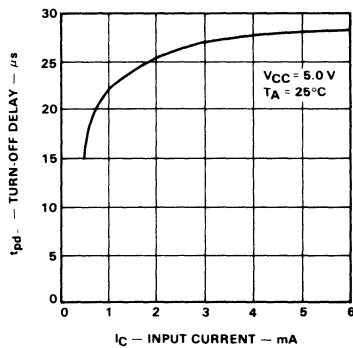
Rise And Fall Time vs Collector Current



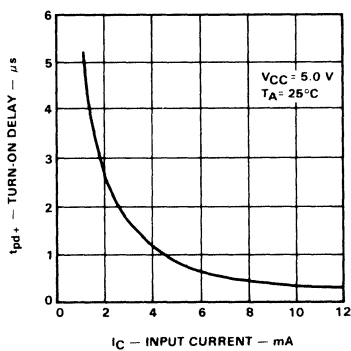
Angular Response



Turn-Off Delay Times



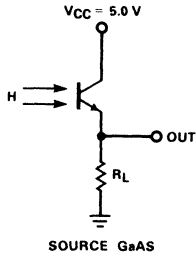
Turn-On Delay Times



Switching Test Circuits

FPA710
FPA710A

Rise And Fall Times



Turn-On And Turn-Off Delay

