

## FNK N-Channel Enhancement Mode Power MOSFET

### Description

The FNK8606 uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a load switch or in PWM applications.

### ● General Features

$V_{DS} = 20V, I_D = 9A$

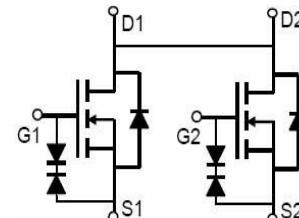
$R_{DS(ON)} < 16m\Omega @ V_{GS}=2.5V$

$R_{DS(ON)} < 11m\Omega @ V_{GS}=4.5V$

- High Power and current handing capability
- Lead free product is acquired
- Surface Mount Package

### Application

- PWM application
- Load switch



Schematic diagram



S1 G1 S2 G2  
Marking and pin Assignment

### Package Marking And Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
8606	FNK8606	PDFN3*3-8L	Ø180mm	8mm	3000 units

### Absolute Maximum Ratings (TA=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	20	V
Gate-Source Voltage	$V_{GS}$	$\pm 12$	V
Drain Current-Continuous	$I_D$	9	A
Drain Current-Pulsed (Note 1)	$I_{DM}$	35	A
Maximum Power Dissipation	$P_D$	1.5	W
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 To 150	°C

### Thermal Characteristic

Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\theta JA}$	83.3	°C/W
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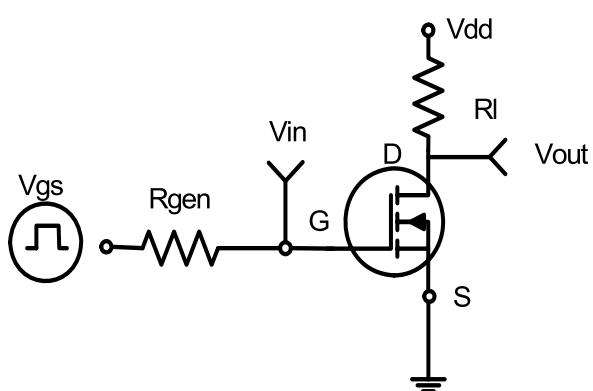
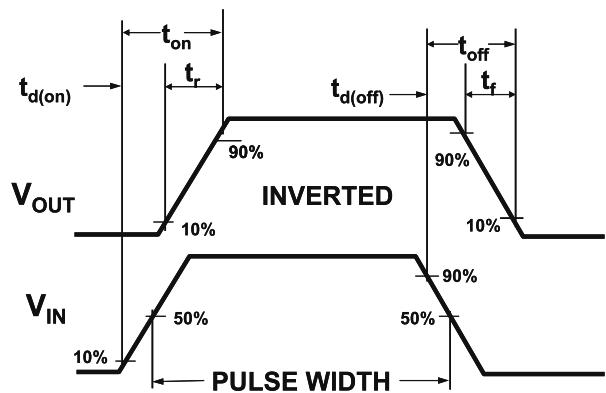
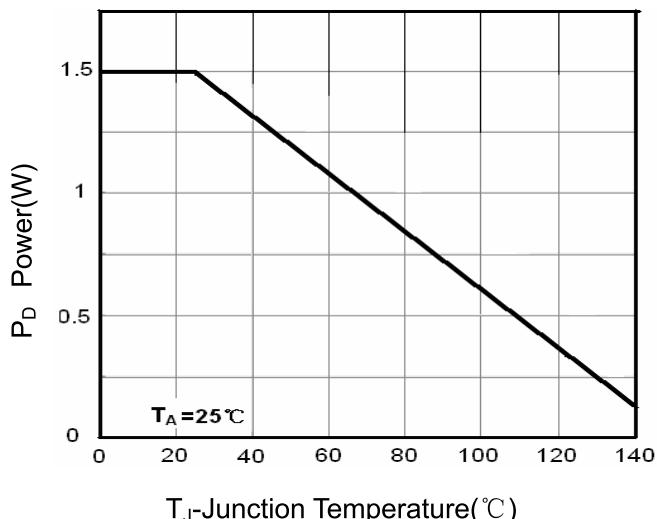
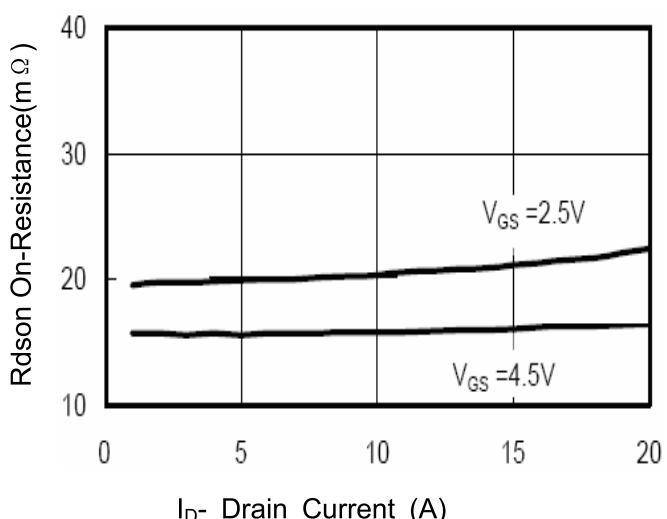
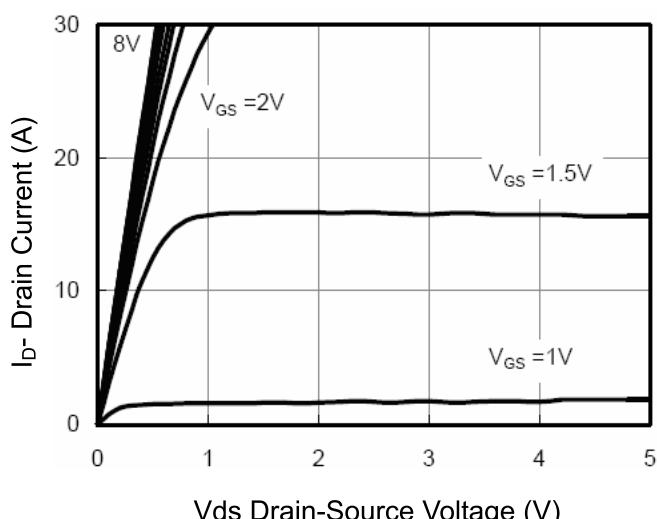
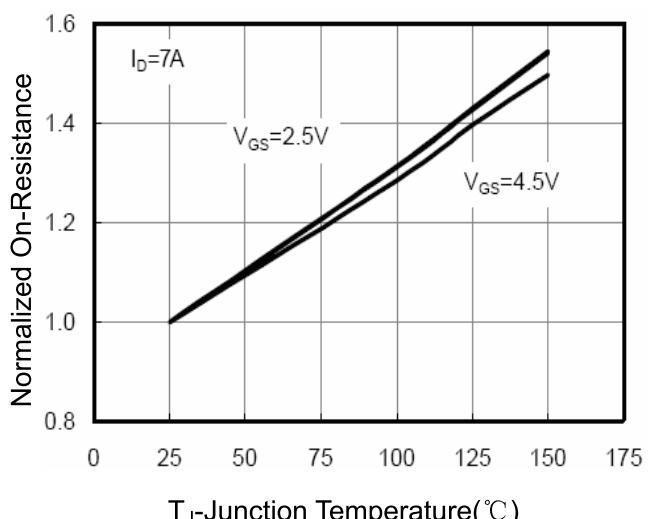
### Electrical Characteristics (TA=25°C unless otherwise noted)

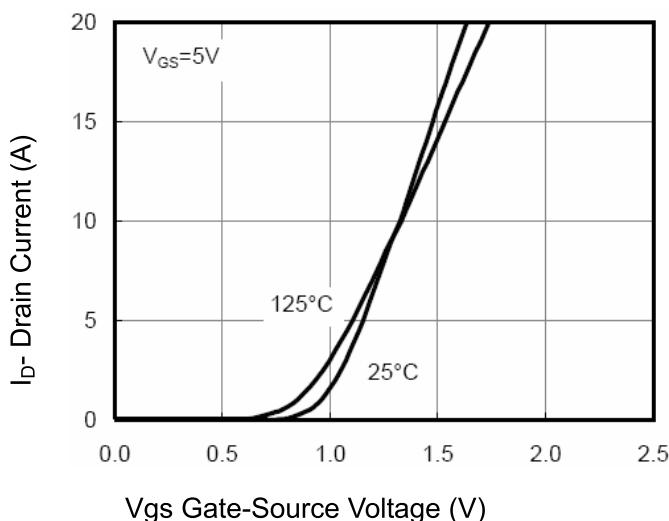
Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V, I_D=250\mu A$	20		-	V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=20V, V_{GS}=0V$	-	-	1	$\mu A$

Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±10V, V <sub>DS</sub> =0V	-	-	±10	μA
<b>On Characteristics (Note 3)</b>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	0.5	0.7	1	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =5A	-	9.1	11	mΩ
		V <sub>GS</sub> =2.5V, I <sub>D</sub> =5A	-	11.5	16	mΩ
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =5V, I <sub>D</sub> =7A	-	20	-	S
<b>Dynamic Characteristics (Note4)</b>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =10V, V <sub>GS</sub> =0V, F=1.0MHz	-	1150	-	PF
Output Capacitance	C <sub>oss</sub>		-	185	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>		-	145	-	PF
<b>Switching Characteristics (Note 4)</b>						
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =10V, R <sub>L</sub> =1.35Ω V <sub>GS</sub> =5V, R <sub>GEN</sub> =3Ω	-	6		nS
Turn-on Rise Time	t <sub>r</sub>		-	13		nS
Turn-Off Delay Time	t <sub>d(off)</sub>		-	52		nS
Turn-Off Fall Time	t <sub>f</sub>		-	16		nS
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =10V, I <sub>D</sub> =7A, V <sub>GS</sub> =4.5V	-	15		nC
Gate-Source Charge	Q <sub>gs</sub>		-	0.8	-	nC
Gate-Drain Charge	Q <sub>gd</sub>		-	3.2	-	nC
<b>Drain-Source Diode Characteristics</b>						
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>s</sub> =1A	-	-	1.2	V
Diode Forward Current (Note 2)	I <sub>s</sub>		-	-	7	A

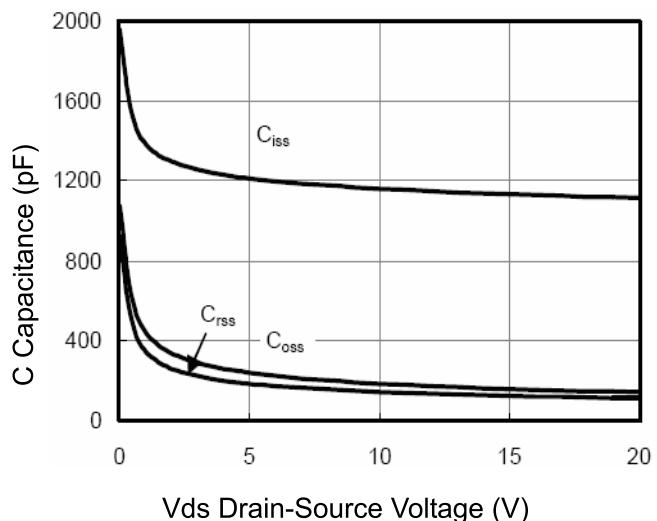
**Notes:**

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, t ≤ 10 sec.
3. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
4. Guaranteed by design, not subject to production

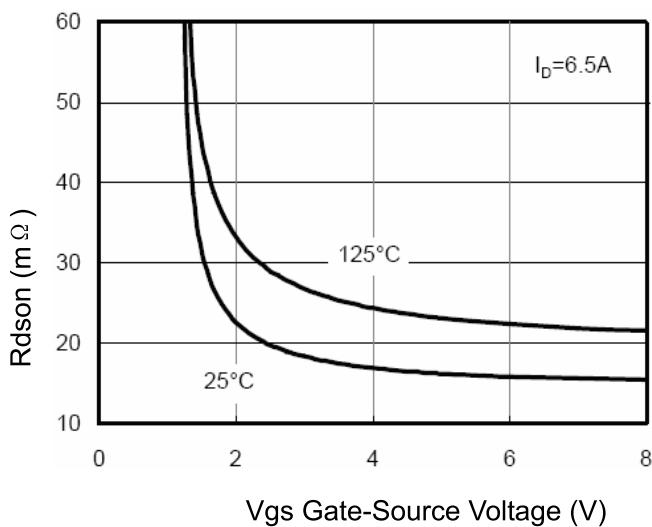
**TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS**

**Figure 1:Switching Test Circuit**

**Figure 2:Switching Waveforms**

**Figure 3 Power Dissipation**

**Figure 6 Drain-Source On-Resistance**

**Figure 5 Output CHARACTERISTICS**

**Figure 8 Drain-Source On-Resistance**



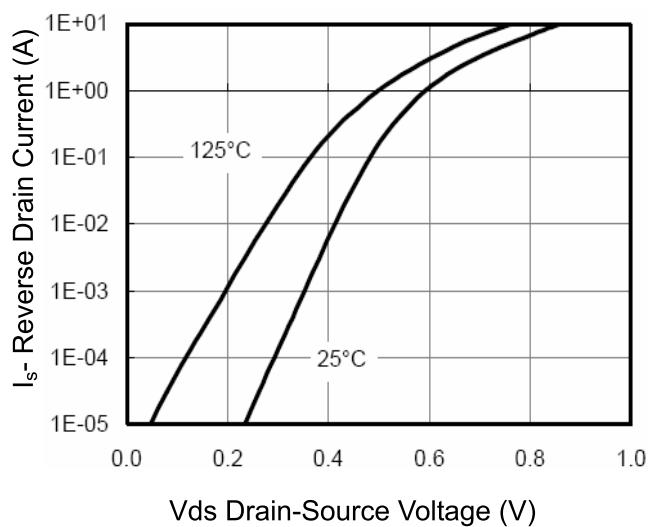
**Figure 7 Transfer Characteristics**



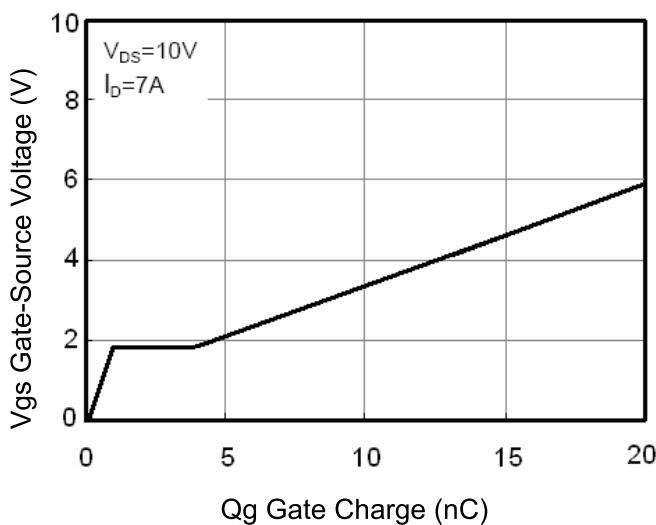
**Figure 8 Capacitance vs Vds**



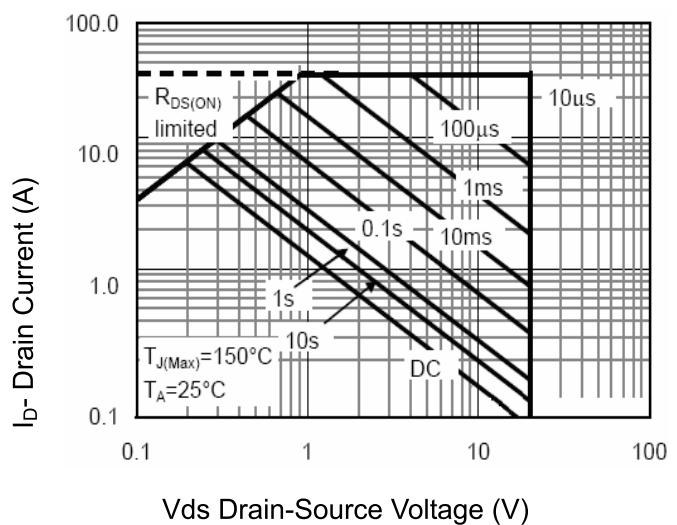
**Figure 9  $R_{DS(on)}$  vs  $V_{GS}$**



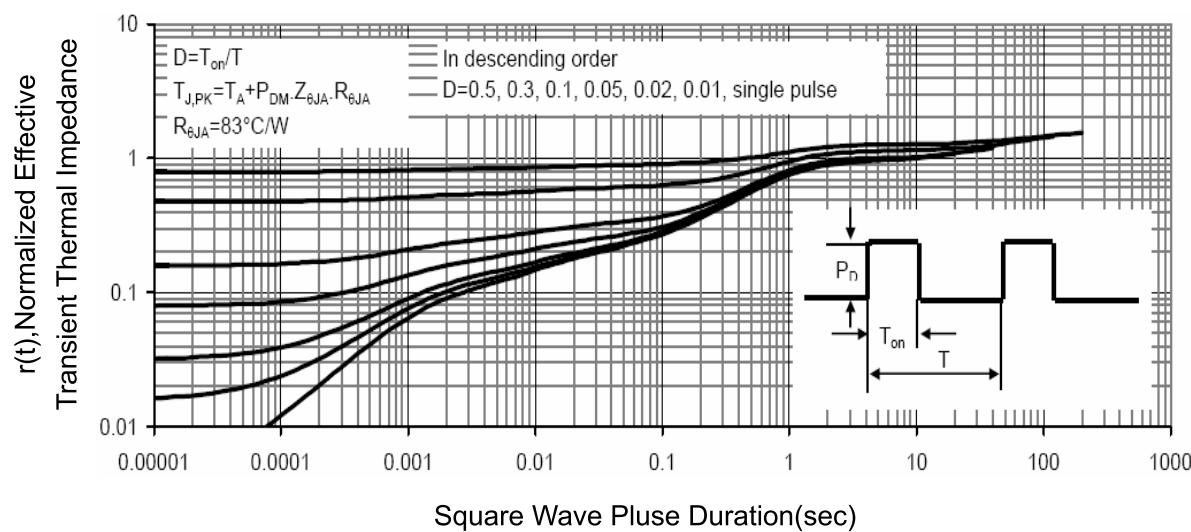
**Figure 10 Capacitance vs  $V_{DS}$**



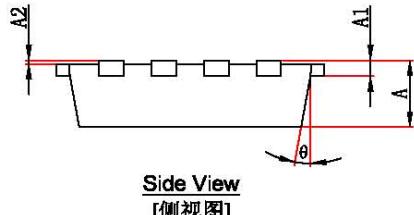
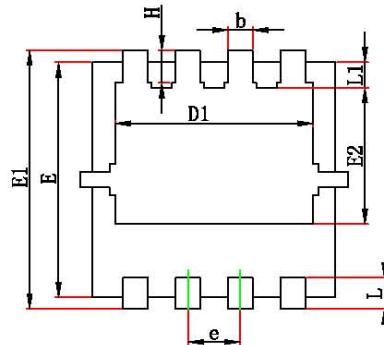
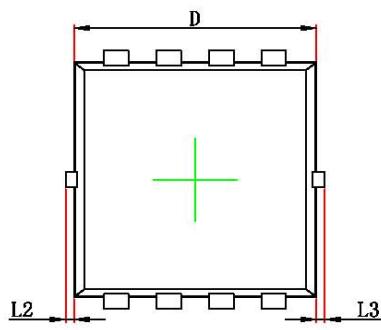
**Figure 11 Gate Charge**



**Figure 13 Safe Operation Area**



**Figure 14 Normalized Maximum Transient Thermal Impedance**

**PDFNWB( 3 × 3 )-8L(P0.65T0.80) PACKAGE OUTLINE DIMENSIONS**


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.650	0.850	0.026	0.033
A1	0.152 REF.		0.006 REF.	
A2	0~0.05		0~0.002	
D	2.900	3.100	0.114	0.122
D1	2.300	2.600	0.091	0.102
E	2.900	3.100	0.114	0.122
E1	3.150	3.450	0.124	0.136
E2	1.535	1.935	0.060	0.076
b	0.200	0.400	0.008	0.016
e	0.550	0.750	0.022	0.030
L	0.300	0.500	0.012	0.020
L1	0.180	0.480	0.007	0.019
L2	0~0.100		0~0.004	
L3	0~0.100		0~0.004	
H	0.315	0.515	0.012	0.020
θ	9°	13°	9°	13°

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