

## 1 Scope

The present specifications shall apply to FMU-26S/R.

## 2 Outline

|              |                              |
|--------------|------------------------------|
| Type         | Silicon Diode                |
| Structure    | Resin Molded                 |
| Applications | High Frequency Rectification |

## 3 Flammability

UL94V-0(Equivalent)

## 4 Absolute maximum ratings

| No. | Item                           | Symbol      | Unit   | Rating      | Conditions                             |
|-----|--------------------------------|-------------|--------|-------------|----------------------------------------|
| 1   | Transient Peak Reverse Voltage | $V_{RSM}$   | V      | 650         |                                        |
| 2   | Peak Reverse Voltage           | $V_{RM}$    | V      | 600         |                                        |
| 3   | Average Forward Current        | $I_{F(AV)}$ | A      | 10          | Refer to derating curve in Section 7   |
| 4   | Peak Surge Forward Current     | $I_{FSM}$   | A      | 40          | 10ms.<br>Half sine wave, one shot      |
| 5   | $I^2t$ Limiting Value          | $I^2t$      | $A^2s$ | 8.0         | $1\text{ ms} \leq t \leq 10\text{ ms}$ |
| 6   | Junction Temperature           | $T_j$       | °C     | -40 to +150 |                                        |
| 7   | Storage Temperature            | $T_{stg}$   | °C     | -40 to +150 |                                        |

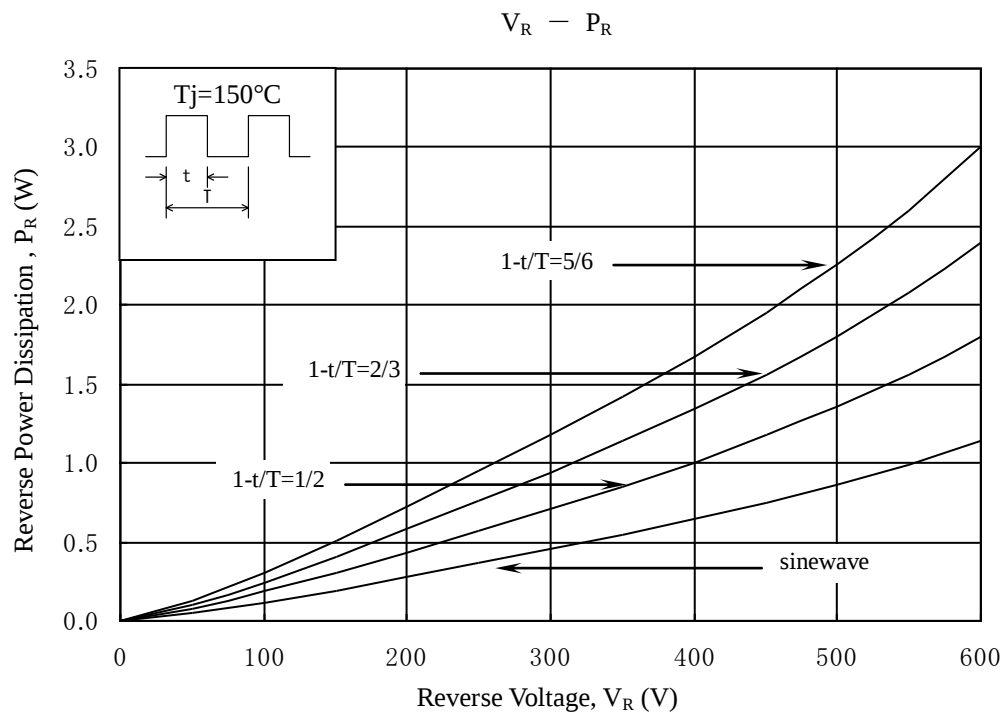
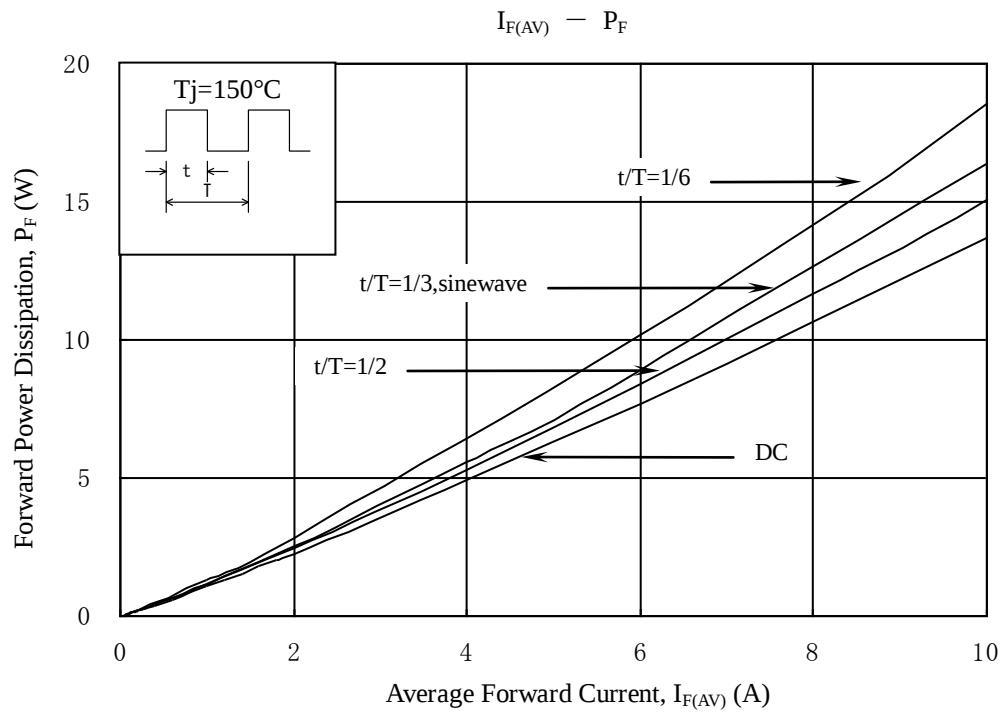
No.1, 2, 4 and 5 show ratings per one chip.

5 Electrical characteristics ( $T_a=25^\circ\text{C}$ , unless otherwise specified)

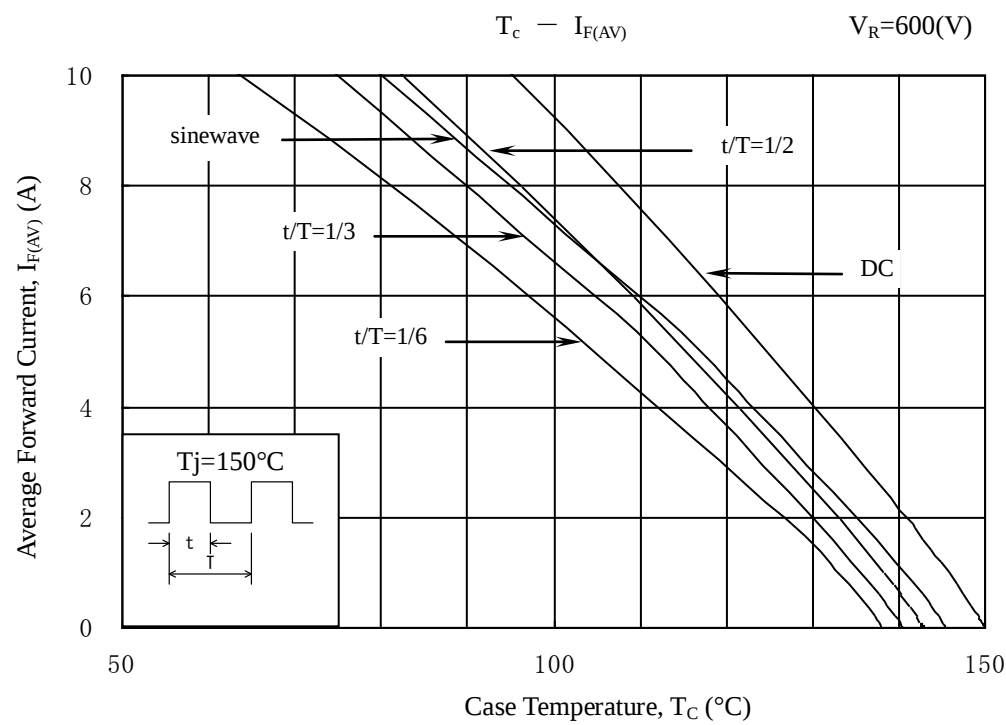
| No. | Item                                           | Symbol           | Unit          | Value    | Conditions                                                                            |
|-----|------------------------------------------------|------------------|---------------|----------|---------------------------------------------------------------------------------------|
| 1   | Forward Voltage Drop                           | $V_F$            | V             | 1.5 max. | $I_F=5.0\text{A}$                                                                     |
| 2   | Reverse Leakage Current                        | $I_R$            | $\mu\text{A}$ | 50 max.  | $V_R=V_{RM}$                                                                          |
| 3   | Reverse Leakage Current Under High Temperature | $H \cdot I_{R1}$ | $\mu\text{A}$ | 500 max. | $V_R=V_{RM}, T_j=100^\circ\text{C}$                                                   |
|     |                                                | $H \cdot I_{R2}$ | mA            | 3.0 max. | $V_R=V_{RM}, T_j=150^\circ\text{C}$                                                   |
| 4   | Reverse Recovery Time                          | trr1             | nS            | 400 max. | $I_F=I_{RP}=100\text{mA}$<br>90% Recovery point, $T_j=25^\circ\text{C}$               |
|     |                                                | trr2             | nS            | 180 max. | $I_F=100\text{mA}, I_{RP}=200\text{mA}$<br>75% Recovery point, $T_j=25^\circ\text{C}$ |
| 5   | Forward Voltage Drop                           | $R_{th(j-c)}$    | °C /W         | 4.0 max. | Between Junction and case                                                             |

No.1, 2, 3 and 4 show characteristics per one chip.

6 Characteristics

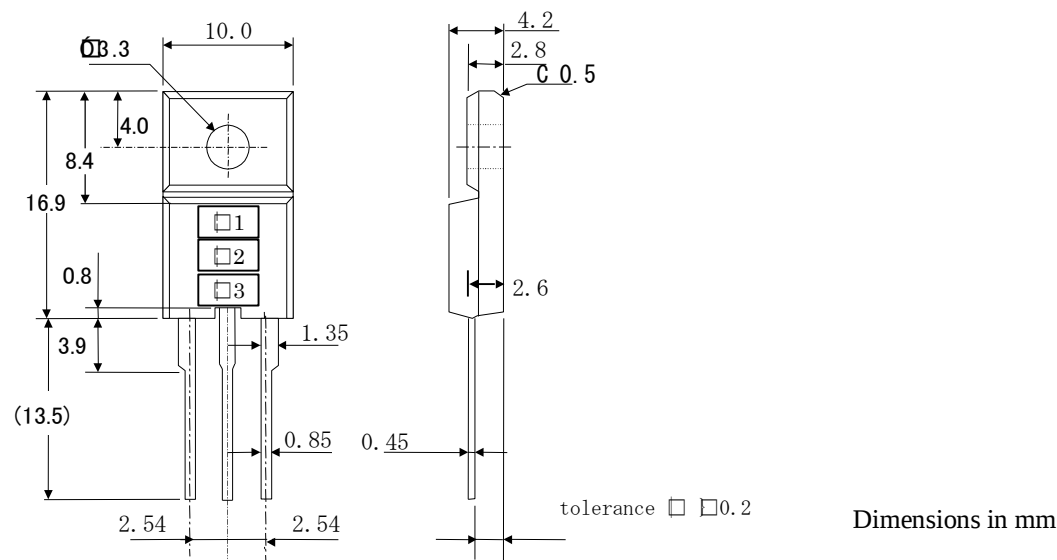


7 Derating



8 Package information

8-1 Package type, physical dimensions and material



8-2 Appearance

The body shall be clean and shall not bear any stain, rust or flaw.

8-3 Marking

| Type Name | Marking         |                |                                                                                                                                                                              |
|-----------|-----------------|----------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
|           | *1 is type name | *2 is polarity | *3 is lot number                                                                                                                                                             |
| FMU-26S   | FMU26S          |                | 1st letter: Last digit of year<br>2nd letter: Month From 1 to 9 for Jan. to Sep.,<br>O for Oct., N for Nov., D for Dec.<br>3rd & 4th letter: Day<br>ex. 2117 (Jan. 17, 2002) |
| FMU-26R   | FMU26R          |                |                                                                                                                                                                              |