

FMH07N90E

FUJI POWER MOSFET

Super FAP-E³ series

N-CHANNEL SILICON POWER MOSFET

■ Features

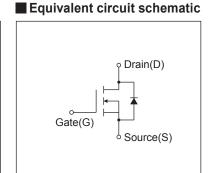
Maintains both low power loss and low noise Lower $R_{DS}(on)$ characteristic More controllable switching dv/dt by gate resistance Smaller V_{GS} ringing waveform during switching Narrow band of the gate threshold voltage (4.0±0.5V) High avalanche durability

Applications

Switching regulators UPS (Uninterruptible Power Supply) DC-DC converters

■ Maximum Ratings and Characteristics

● Absolute Maximum Ratings at Tc=25°C (unless otherwise specified)



Description	Symbol	Characteristics	Unit	Remarks	
Drain Course Valters	V _{DS}	900	V		
Drain-Source Voltage	V _{DSX}	900	V	V _{GS} = -30V	
Continuous Drain Current	ID	±7	А		
Pulsed Drain Current	IDP	±28	А		
Gate-Source Voltage	V _{GS}	±30	V		
Repetitive and Non-Repetitive Maximum AvalancheCurrent	Iar	7	Α	Note*1	
Non-Repetitive Maximum Avalanche Energy	Eas	396.3	mJ	Note*2	
Repetitive Maximum Avalanche Energy	Ear	14.5	mJ	Note*3	
Peak Diode Recovery dV/dt	dV/dt	2.1	kV/μs	Note*4	
Peak Diode Recovery -di/dt	-di/dt	100	A/µs	Note*5	
Maximum Power Dissipation	Po	2.5	10/	Ta=25°C	
		145	W	Tc=25°C	
O	Tch	150	°C		
Operating and Storage Temperature range	Tstg	-55 to + 150	°C		

Electrical Characteristics at Tc=25°C (unless otherwise specified)

Description	Symbol	Conditions		min.	typ.	max.	Unit	
Drain-Source Breakdown Voltage	BVDSS	I _D =250µA, V _{GS} =0V		900	-	-	V	
Gate Threshold Voltage	V _{GS} (th)	I _D =250µA, V _{DS} =V _{GS}		3.5	4.0	4.5	V	
Zero Gate Voltage Drain Current		V _{DS} =900V, V _{GS} =0V	T _{ch} =25°C	-	-	25		
	IDSS	V _{DS} =720V, V _{GS} =0V	T _{ch} =125°C	-	-	250	μA	
Gate-Source Leakage Current	Igss	V _{GS} =±30V, V _{DS} =0V	V _{GS} =±30V, V _{DS} =0V		10	100	nA	
Orain-Source On-State Resistance	R _{DS} (on)	I _D =3.5A, V _{GS} =10V		-	1.65	2.0	Ω	
Forward Transconductance	g _{fs}	I _D =3.5A, V _{DS} =25V		4.2	8.4	-	S	
nput Capacitance	Ciss	V _{DS} =25V V _{GS} =0V f=1MHz		-	1200	1800	pF	
Output Capacitance	Coss			-	115	175		
Reverse Transfer Capacitance	Crss			-	8.5	13		
Turn-On Time td tr tr	td(on)	V _{cc} =600V V _{ss} =10V I _D =3.5A R _G =36Ω		-	33	53	ns	
	tr			-	32	45		
Turn-Off Time	td(off)			-	110	165		
	tf			-	32	45		
Total Gate Charge	Q _G	V _{cc} =450V I _D =7A V _{GS} =10V		-	39	59	nC	
Gate-Source Charge	QGS			-	10	15		
Drain-Source Crossover Charge	Qsw			-	3.6	5.5		
Gate-Drain Charge	Q _{GD}			-	15	23		
Avalanche Capability	lav	L=5.93mH, T _{ch} =25°C		7	-	-	Α	
Diode Forward On-Voltage	V _{SD}	I _F =7A, V _{GS} =0V, T _{ch} =25°C	I _F =7A, V _{GS} =0V, T _{ch} =25°C		0.90	1.35	V	
Reverse Recovery Time	trr	I _F =7A, V _{GS} =0V		-	1.65	-	μS	
Reverse Recovery Charge	Qrr	-di/dt=100A/µs, Tch=25°C		-	11	-	μC	

Thermal Characteristics

Description	Symbol	Test Conditions	min.	typ.	max.	Unit
Thermal resistance	Rth (ch-c)	Channel to case			0.862	°C/W
	Rth (ch-a)	Channel to ambient			50.0	°C/W

Note *1 : Tch≤150°C

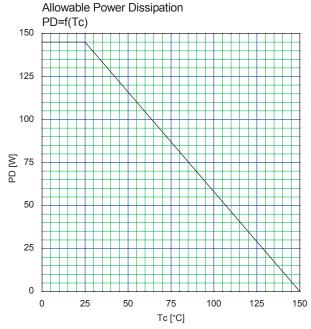
Note *2 : Stating Tch=25°C, Ias=2.8A, L=92.7mH, Vcc=90V, Re=10Ω
Eas limited by maximum channel temperature and avalanche current.
See to 'Avalanche current' graph.

Note *3 : Repetitive rating : Pulse width limited by maximum channel temperature.

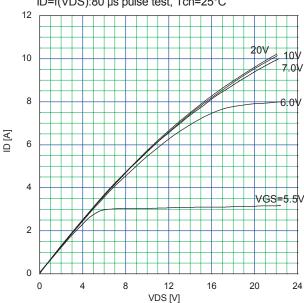
See to the 'Transient Themal impeadance' graph.

Note *4 : Ir≤-Ip, -di/dt=100A/µs, Vcc≤BVbss, Tch≤150°C.

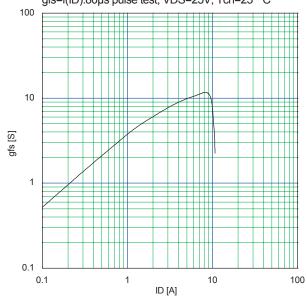
Note *5 : Ir≤-Ip, dv/dt=2.1kV/µs, Vcc≤BVbss, Tch≤150°C.



Typical Output Characteristics ID=f(VDS):80 µs pulse test, Tch=25°C



Typical Transconductance gfs=f(ID):80 μ s pulse test, VDS=25V, Tch=25 °C



Safe Operating Area
I_D=f(V_{DS}):Duty=0(Single pulse), Tc=25°c

10²

10¹

10¹

Power loss waveform:
Square waveform
Square waveform

10²

10²

Typical Transfer Characteristic ID=f(VGS):80µs pulse test, VDS=25V, Tch=25 °C

VDS [V]

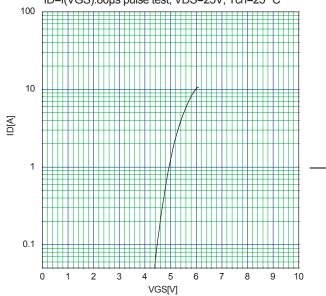
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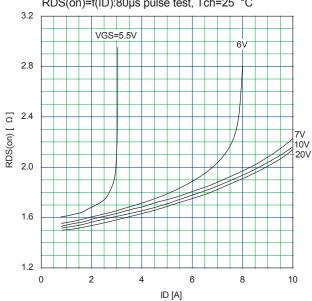
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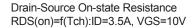
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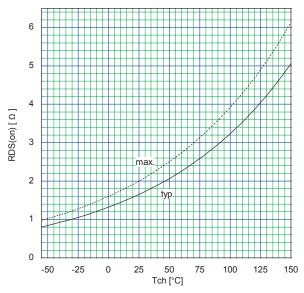
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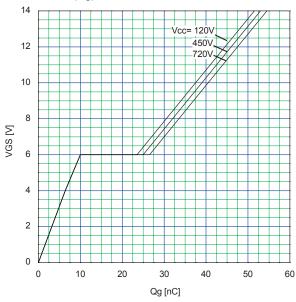
Typical Drain-Source on-state Resistance RDS(on)=f(ID):80µs pulse test, Tch=25 °C



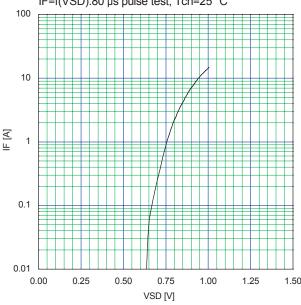




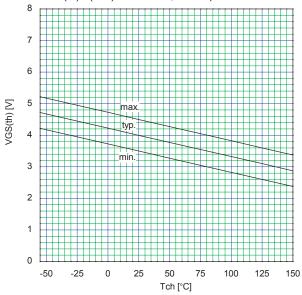
Typical Gate Charge Characteristics VGS=f(Qg):ID=7A, Tch=25°C



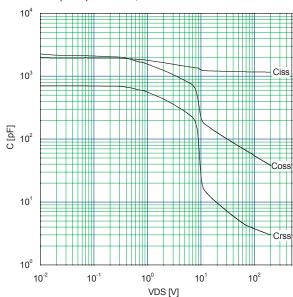
Typical Forward Characteristics of Reverse Diode IF=f(VSD):80 μ s pulse test, Tch=25 °C



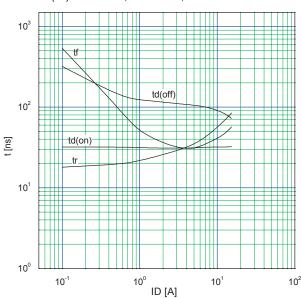
Gate Threshold Voltage vs. Tch VGS(th)=f(Tch):VDS=VGS, ID=25QuA

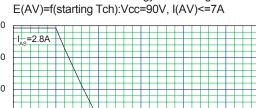


Typical Capacitance C=f(VDS):VGS=0V, f=1MHz

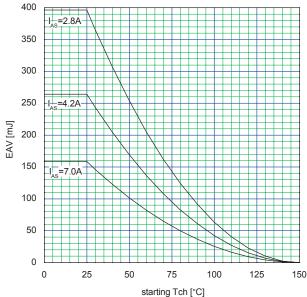


Typical Switching Characteristics vs. ID t=f(ID):Vcc=600V, VGS=10V, RG=3 Ω

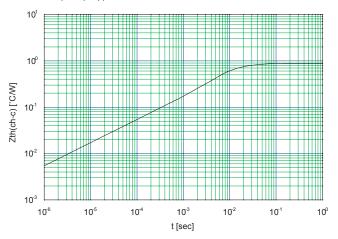




Maximum Avalanche Energy vs. starting Tch



$\label{eq:maximum Transient Thermal Impedance Zth(ch-c)=f(t):D=0} \label{eq:maximum Transient Thermal Impedance Zth(ch-c)=f(t):D=0}$



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- Measurement equipment

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