

1A LOW DROPOUT POSITIVE REGULATOR

DESCRIPTION & FEATURES 概述及特点	
The FH1117 series of positive adjustable and fixed regulators is designed to provide 1A for applications requiring high efficiency. All internal circuitry is designed to operated down to 800mV input to output differential and the dropout voltage is fully specified as a function of load current. The FH1117 offers current limiting and thermal protection. The on chip trimming adjusts the reference voltage accuracy to 1%.	FH1117 系列三端可调、固定电压调整器，可以提供 1A 的电流，内部电路设计运用于 800mA 输入→输出微分电路，压差由负载电流确定。 FH1117 产品可提供限流及热保护功能，调整参考电压精度达 1%
- Output current of 1A typical - Three-terminal adjustable or fixed 1.5V,1.8V, 2.5V, 2.85V, 3.3V, 5.0V outputs - Low dropout of typical 800mV - Thermal protection built in - Typical 0.015% line regulation - Typical 0.01% load regulation - Fast transient response	- 输出电流（典型值 1A） - 三端可调或固定输出 1.5V,1.8V, 2.5V, 2.85V, 3.3V, 5.0V 输出 - 低压差（典型值 800mV） - 内置热保护 - 线性调整（典型值 0.015%） - 负载调整（典型值 0.01%） - 快速瞬态响应
Applications 应用	
- PC Motherboard - LCD Monitor - DVD-Video Player - NIC / Switch - Telecom Equipment - High Efficiency Linear Regulator - Printer and other Peripheral Equipment - Battery Charger	- 电脑主板 - LCD 显示器 - DVD 视频播放器 - 网络接口卡/开关 - 通讯设备 - 高效率线性调整器 - 打印机及其它外设 - 充电器

1A 低压差三端稳压器 FH1117

Package 封装类型	Temperature Range 温度范围	Part Number 产品型号	Packing Type 包装类型
SOT-223	0°C ~ 125°C	FH1117SADJ	Tape
		FH1117S15	Tape
		FH1117S18	Tape
		FH1117S25	Tape
		FH1117S285	Tape
		FH1117S33	Tape
		FH1117S5	Tape

FH1117

S

18

T

Type 产品型号

Package 封装形式 S: SOT-223

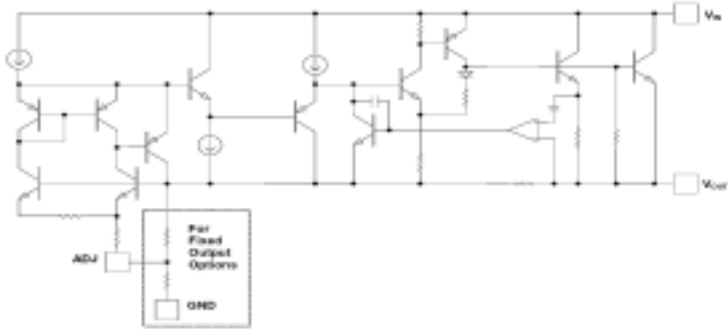
Voltage

Packing Type 包装形式 T: Tape 编带
B: Bulk 散装

ADJ	ADJ
15	1.5V
18	1.8V
25	2.5V
285	2.85V
33	3.3V
5	5.0V

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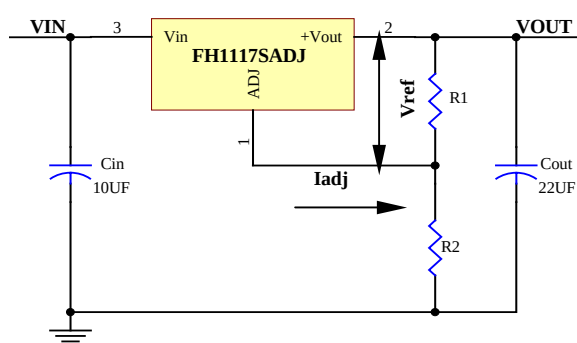
BLOCK DIAGRAM (功能框图)

MAXIMUM RATINGS (T _a =25°C) 最大额定值 (Note 1)				
Characteristic 特性参数	Symbol 符号	Rating 额定值		Unit 单位
		Min 最小	Max 最大	
Input Voltage 输入电压	V _{IN}		13	V
Operating Junction Temperature结温	T _J	0	150	°C
Storage Temperature Range储存温度	T _{STG}	-65	150	°C
Lead Temperature 焊接温度(soldering, 10 seconds)		—	260	°C
Note 1: Exceeding these ratings could cause damage to the device. All voltages are with respect to Ground. Currents are positive into, negative out of the specified terminal.				

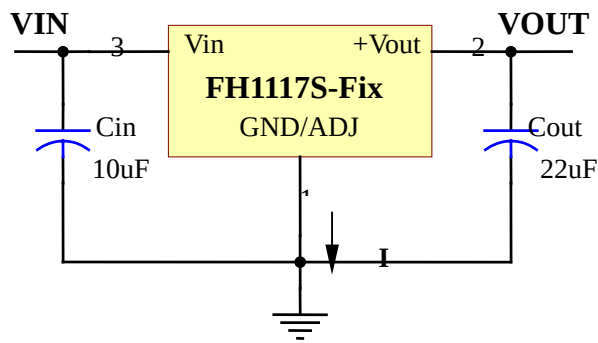
POWER DISSIPATION TABLE 耗散功率					
Package	θ JA (°C /W)	Derating factor (mW/ °C)T _A ≥25 °C	T _A ≤25 °C Power rating (mW)	T _A =70 °C Power rating (mW)	T _A = 85 °C Power rating (mW)
SOT-223	136	7.35	919	588	478
Note : θ J _A : Thermal Resistance-Junction to Ambient, D _F : Derating factor, P _o : Power consumption. Junction Temperature Calculation: T _J = T _A + (P _D × θ J _A), P _o = D _F × (T _J -T _A) The θ J _A numbers are guidelines for the thermal performance of the device/PC-board system. All of the above assume no ambient airflow.					

RECOMMENDED OPERATING CONDITIONS 推荐使用条件		Symbol 符号	Min 最小	Max 最大	Unit 单位
Input Voltage 输入电压		V _{IN}	2.7	12	V
Load Current (with adequate heat sinking) 负载电流		I _O	5	—	mA
Input Capacitor V _{IN} to GND 输入电容			1.0	—	μF
Output Capacitor with ESR of 10Ω max 输出电容			4.7	—	μF
Junction temperature 结温		T _J	—	125	°C

Typical Applications 典型应用图例



Adjustable Voltage Regulator



Fixed Voltage Regulator

ELECTRICAL CHARACTERISTICS 电特性						
Unless otherwise specified, 如无特殊说明, $V_{IN} = V_{OUT} + 2V$, $I_O = 10\text{ mA}$, and $T_J = 25\text{ }^\circ\text{C}$.						
Parameter 参数	Symbol 符号	Test Conditions 测试条件	Min. 最小值	Typ. 典型值	Max. 最大值	Unit 单位
Reference Voltage 参考电压	V_{REF}	$I_O = 10\text{ mA}$, $V_{IN} - V_{OUT} = 2V$	1.238	1.250	1.262	V
		$10\text{ mA} \leq I_O \leq 1\text{ A}$, $V_{OUT} + 1.5V \leq V_{IN} \leq 12V$	1.230	1.250	1.270	
Output Voltage 输出电压	V_{OUT}	$I_O = 10\text{ mA}$, $V_{IN} = 2.5V$	1.485	1.500	1.515	V
		$10\text{ mA} \leq I_O \leq 1\text{ A}$, $3.0V \leq V_{IN} \leq 12V$	1.476	1.500	1.524	
		$I_O = 10\text{ mA}$, $V_{IN} = 3.8V$	1.782	1.800	1.818	
		$10\text{ mA} \leq I_O \leq 1\text{ A}$, $3.2V \leq V_{IN} \leq 12V$	1.764	1.800	1.836	
		$I_O = 10\text{ mA}$, $V_{IN} = 4.5V$	2.475	2.500	2.525	
		$10\text{ mA} \leq I_O \leq 1\text{ A}$, $4.25V \leq V_{IN} \leq 12V$	2.450	2.500	2.550	
		$I_O = 10\text{ mA}$, $V_{IN} = 4.85V$	2.821	2.850	2.879	
		$10\text{ mA} \leq I_O \leq 1\text{ A}$, $4.35V \leq V_{IN} \leq 12V$	2.805	2.850	2.895	
		$I_O = 10\text{ mA}$, $V_{IN} = 5.0V$	3.267	3.300	3.333	
		$10\text{ mA} \leq I_O \leq 1\text{ A}$, $4.75V \leq V_{IN} \leq 12V$	3.235	3.300	3.365	
Line Regulation 线性调整率	ΔV_{OI}	$I_O = 10\text{ mA}$, $V_{OUT} + 1.5V \leq V_{IN} \leq 12V$	—	0.04	0.20	%
		$I_O = 10\text{ mA}$, $V_{OUT} + 1.5V \leq V_{IN} \leq 12V$	—	1.0	6.0	mV
Load Regulation 负载调整率	ΔV_{OL}	$10\text{ mA} \leq I_O \leq 1\text{ A}$, $V_{IN} - V_{OUT} = 3V$	—	0.10	0.40	%
		$10\text{ mA} \leq I_O \leq 1\text{ A}$, $V_{IN} = V_{OUT} + 1.5V$	—	1.0	10.0	mV
Dropout Voltage 压降	ΔV	$I_O = 10\text{ mA}$	—	0.8	1.15	V
		$I_O = 1\text{ A}$	—	0.8	1.30	
Minimum Load Current 最小负载电流(Note 1)	I_{min}	$V_{IN} \leq 12V$	—	1.7	5	mA
Quiescent Current 静态电流	I_Q	$V_{IN} \leq 12V$	—	6	10	mA
Current Limit 电流范围	I_{CL}	$V_{IN} - V_{OUT} = 3V$	1	1.2	—	A
Adjust Pin Current 调整端电流		$I_O = 10\text{ mA}$, $V_{IN} - V_{OUT} = 2V$	—	50	120	μA
Thermal Regulation 温度特性 (Note 2)		$T_A = 25\text{ }^\circ\text{C}$, 30 ms pulse	—	0.01	0.1	%/W
Ripple rejection (Note 2) 纹波抑制比	R_R	$f_O = 120\text{ Hz}$, $1V_{RMS}$, $I_O = 400\text{ mA}$, $V_{IN} - V_{OUT} = 3V$	60	75	—	dB

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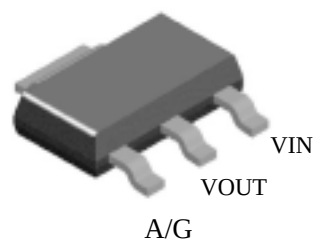
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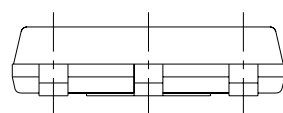
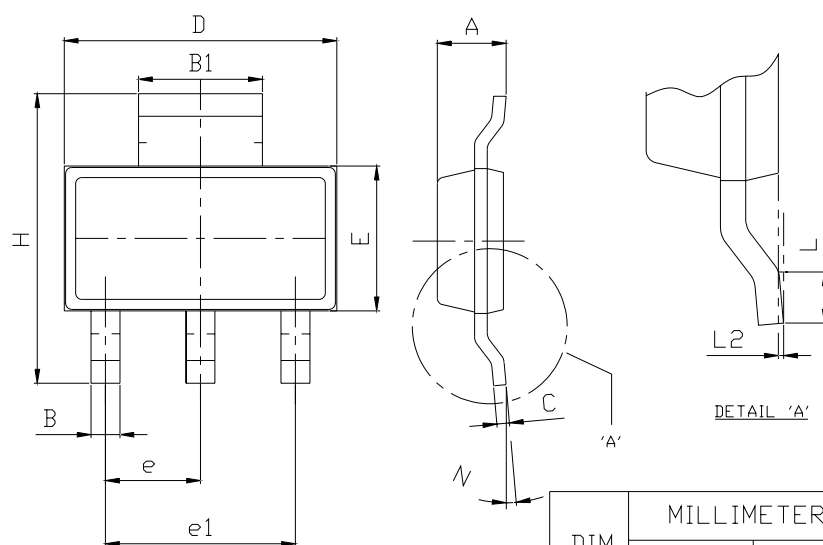
TEL: 0758-2865088 2865091 FAX: 0758-2849749

Note 1: For the adjustable device, the minimum load current is the minimum current required to maintain regulation. Normally the current in the resistor divider used to set the output voltage is selected to meet the minimum load current requirement.

Note 2: These parameters, although guaranteed, are not tested in production.

SOT-223 封装外形尺寸 (SOT-223 DIMENSION)





1.ADJ/GND
2.VOUT
3.VIN

DIM	MILLIMETERS	
	MIN.	MAX.
A	1.80REF	
B	0.60	0.80
B1	2.90	3.10
C	0.24	0.32
D	6.30	6.70
E	3.30	3.70
e	2.30REF	
e1	4.60REF	
H	6.70	7.30
L	0.91	---
L2	0.02	0.10
N	0°	10°

SOT-223(TO-261)