Panasonic FC6A21060L

FC6A21060L

Gate Resistor installed Dual N-Channel MOS FET

For lithium-ion secondary battery protection circuit

■ Features

- Low Source-source On-state Resistance:RSS(on)typ. = 8.7 m Ω (VGS = 4.5 V)
- CSP package:smallest & thinnest size
- Halogen-free / RoHS compliant (EU RoHS / UL-94 V-0 / MSL:Level 1 compliant)
- Marking Symbol:31

■ Packaging

FC6A21060L Embossed type (Thermo-compression sealing): 5 000 pcs / reel (standard)

■ Absolute Maximum Ratings Ta = 25 °C

	Parameter	Symbol	Rating	Unit
FET2	Source-source Voltage	VSS	12	V
	Gate-source Voltage	VGS	±12	V
	Source Current (DC) *1	IS	6	Α
	Source Current (Pulsed) *1,*2	ISp	60	Α
Overall	Total Power Dissipation *1	PD	0.45	W
	Channel Temperature	Tch	150	°C
	Storage Temperature Range	Tstg	-55 to +150	°C

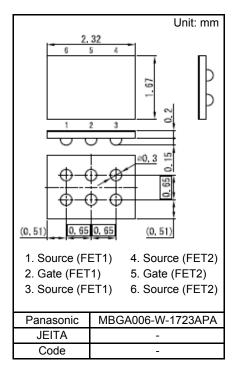
■ Thermal Characteristics

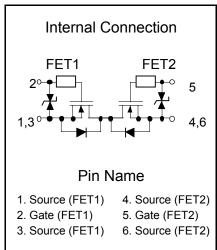
Note

Parameter	Symbol	Rating	Unit	
Thermal Resistance, Channel to Ambient	Rth (ch-a)	278	°C/W	

*1 Mounted on FR4 board (25.4 mm × 25.4 mm × t1.0 mm). Surface Mounted on FR4 Board using the minimum recommended pad size(Cu area = 47 mm² including traces).

*2 $t = 10 \mu s$, Duty Cycle $\leq 1 \%$





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■ Electrical Characteristics Ta = 25 °C ± 3 °C

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Source-source Breakdown Voltage	VSSS	IS = 1 mA, VGS = 0 V	12			V
Zero Gate Voltage source Current	ISSS	VSS = 12 V, VGS = 0 V			1.0	μΑ
	IGSS1	VGS = ±5.0 V, VSS = 0 V		±0.50	±1.0	μА
	IGSS2	VGS = ±4.3 V, VSS = 0 V		±0.25	±0.5	
	IGSS3	VGS = ±4.1 V, VSS = 0 V		±0.20	±0.4	
Gate-source Leakage Current	IGSS4	VGS = ±3.8 V, VSS = 0 V		±0.18	±0.36	
	IGSS5	VGS = ±3.5 V, VSS = 0 V		±0.15	±0.3	
	IGSS6	VGS = ±3.0 V, VSS = 0 V		±0.10	±0.2	
	IGSS7	VGS = ±2.5 V, VSS = 0 V		±0.05	±0.1	
Gate-source Threshold Voltage	Vth	IS = 1.0 mA, VSS = 10.0 V	0.4	0.85	1.4	V
	RSS(on)1	IS = 3.5 A, VGS = 4.5 V	6.0	8.7	12.0	mΩ
Source-source On-State Resistance	RSS(on)2	IS = 3.5 A, VGS = 3.8 V	6.5	9.0	12.5	
Source-source On-State Resistance	RSS(on)3	IS = 3.5 A, VGS = 3.1 V	7.0	10.5	15.5	
	RSS(on)4	IS = 3.5 A, VGS = 2.5 V	8.0	12.5	19.0	
Input Capacitance *1	Ciss			2 300		
Output Capacitance *1	Coss	VSS = 10 V, VGS = 0 V, f = 1MHz		680		pF
Reverse Transfer Capacitance *1	Crss			670		
Turn-on Delay Time *1,*2	td(on)	VDD = 10 V, VGS = 0 to 4.0 V		5.2		
Rise Time *1,*2	tr	IS = 3.5 A		19.0		μs
Turn-off Delay Time *1,*2	td(off)	VDD = 10 V, VGS = 4.0 to 0 V		3.5		
Fall Time *1,*2	tf	IS = 3.5 A		8.2		μs
Total Gate Charge *1	Qg	VDD = 10 V, VGS = 0 to 4.0 V IS = 6 A		30.0		nC
Gate-source Charge *1	Qgs			6.5		
Gate-drain Charge *1	Qgd			10.0		
Body Diode Forward Voltage	VF(s-s)	IF = 6.0 A, VGS = 0 V		0.8	1.2	V

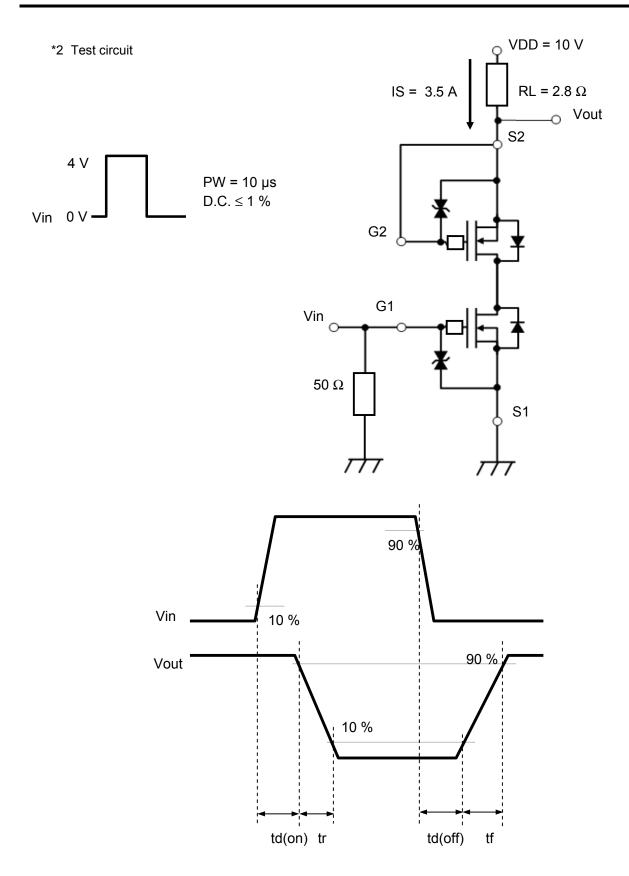
Note: *1 Assured by design

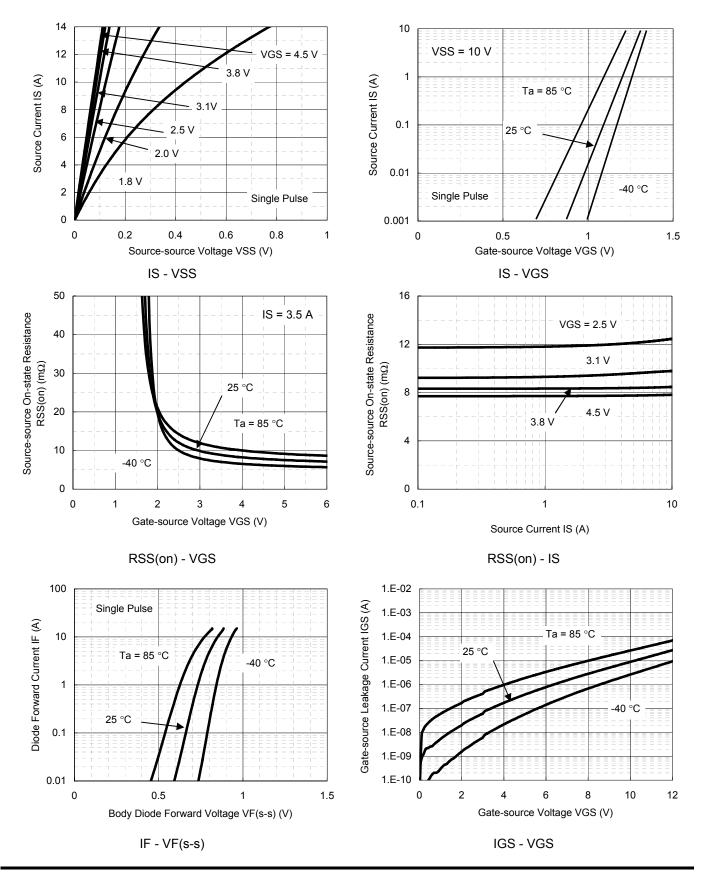
DESTRUCTION CURRENT

	Condition	Result
Operation test	VGS = 3.8 V t = 3 ms, IS = 40 A	PASS
Operation test	VGS = 3.8 V t = 11 ms, IS = 15 A	PASS
Destruction current	VGS = 3.8 V t = 3 ms	95 A
Destruction current	VGS = 3.8 V t = 11 ms	63 A

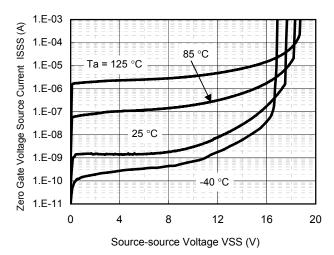
Ta = 25 °C, Mounted on FR4 board (25.4 \times 25.4 \times t1.0 mm) Surface Mounted on FR4 Board using the minimum recommended pad size (Cu area = 47 mm² including traces)

^{*2} See Test circuit

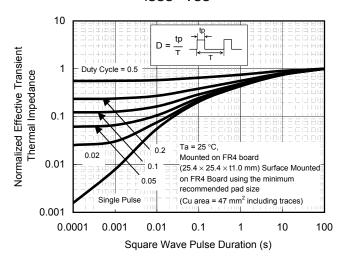




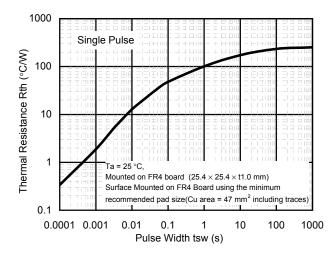
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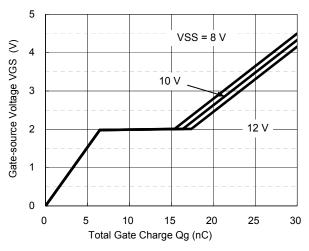




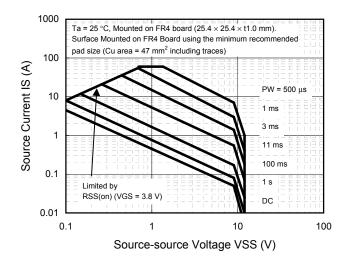
Thermal Response



Rth - tsw



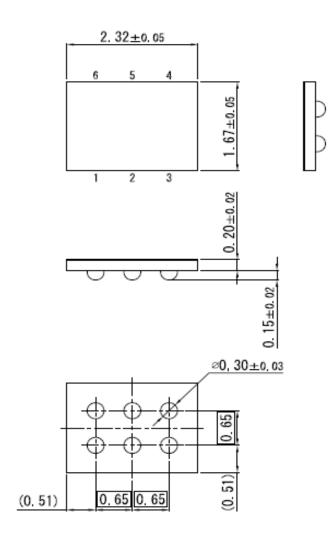
Dynamic Input/Output Characteristics



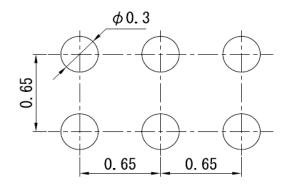
Safe Operating Area

MBGA006-W-1723APA

Unit: mm



■ Land Pattern (Reference) (Unit: mm)



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