

1.8A Line Switch with Low On-Resistance

DESCRIPTION

The EUP3504 is a low dropout line switch IC with ON/OFF control and output current protection which integrates a P-channel MOSFET.

By connecting the EUP3504 to the output pin of a step-down DC/DC converter, the EN pin controls ON/OFF for each distribution switch to deliver power per requirements and maximize total power efficiency. As a result, the EUP3504 helps to extend battery life and product operation time.

The EUP3504 contains a current limit protection circuit so these are not required externally unlike discrete circuit solutions where MOSFETs and resistors are used. Externally programmable soft-start allows for proper power on sequencing with respect to other power supplies and avoids inrush current during startup.

When a low signal is input to the EN pin, the IC enters shutdown mode. Even where a load capacitor is connected to the output pin during shutdown, the electric charge stored at the load capacitor is discharged through the internal switch. As a result, the VOUT pin voltage falls quickly to the GND level. The EUP3504 contains thermal shutdown protection.

FEATURES

- 3.3V to 13.5V Input Voltage Range
- 110mΩ On Resistance
- Low 90μA Quiescent Current
- Low Shutdown Current : <4μA
- ON/OFF Control Function
- Externally Programmable Current Soft-Start
- High Speed Auto Discharge Function
- Operating Temperature Range: -40°C ~+85°C
- 2.35A Output Current Limit
- Thermal Shutdown
- RoHS Compliant and 100% Lead (Pb)-Free Halogen-Free

APPLICATIONS

- Portable Equipment
- Laptop, Palmtops, Notebook Computers
- LCD Monitor TV

Typical Application Circuit

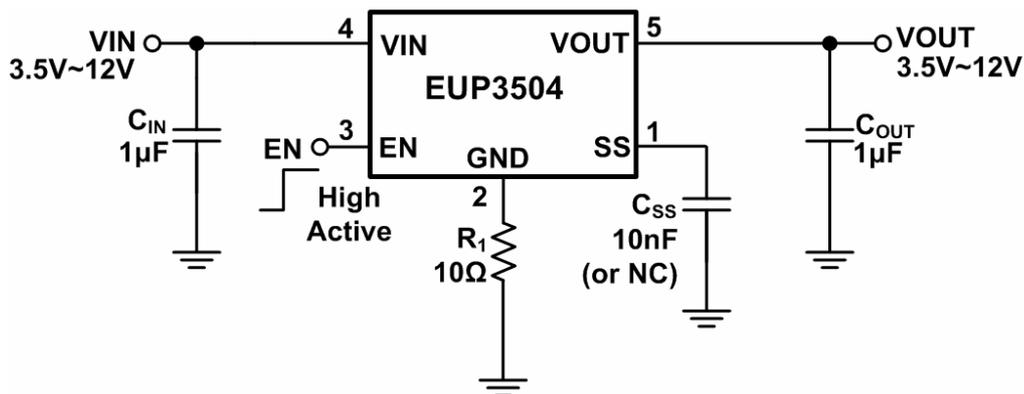


Figure 1.

Block Diagram

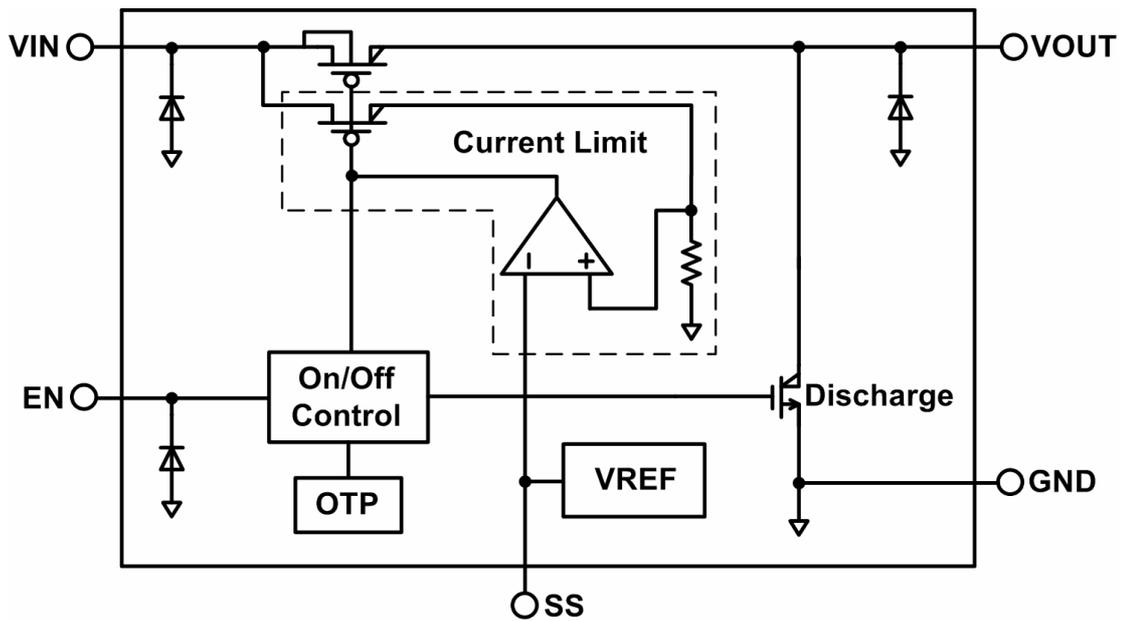


Figure 2.

Pin Configurations

Package Type	Pin Configurations
SOT23-5	<p>(TOP VIEW)</p>

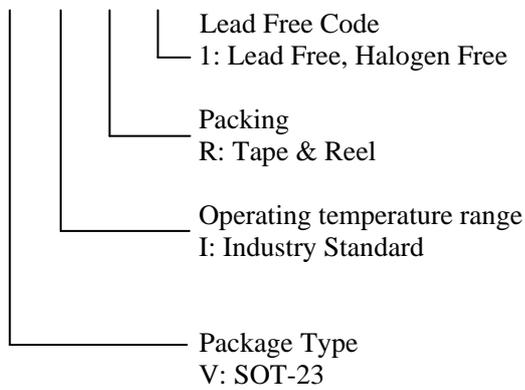
Pin Description

PIN	SOT23-5	DESCRIPTION
SS	1	Soft-start control. Connect a capacitor from SS to GND to set the soft-start period. See <i>Soft-start Capacitor</i> .
GND	2	Ground. Connect a 10Ω resistor from GND pin to PCB ground for short circuit protection.
EN	3	ON/OFF Control. Make sure EN pin never floating.
VIN	4	Input voltage of the line switch.
VOUT	5	Output voltage of the line switch.

Ordering Information

Order Number	Package Type	Marking	Current Limit	Continuous Load Current	Operating Temperature Range
EUP3504VIR1	SOT23-5	xxxxx A500	2.35A	1.8A	-40°C to +85°C

EUP3504



Absolute Maximum Ratings

■	V_{IN}, V_{EN}	-0.3 to 15V
■	V_{OUT}	-0.3 to ($V_{IN}+0.3$)
■	I_{OUT}	2A
■	Junction Temperature	150°C
■	Storage Temperature Range	-65°C to +150°C
■	Lead Temperature	260°C
■	Thermal Resistance θ_{JA} (SOT23-5)	205°C /W
■	ESD Rating	
	Human Body Model	2kV

Recommend Operating Conditions

■	V_{IN}	3.3 to 13.5V
■	V_{EN}	0 to 13.5V
■	Operating Temperature Range	-40°C to +85°C

Electrical Characteristics

$V_{IN}=12V, C_{IN}=1\mu F, C_{OUT}=1\mu F, C_{SS}=22nF, R_I=10\Omega, V_{EN}=V_{IN}, T_A=+25^\circ C$, unless otherwise noted ⁽¹⁾.

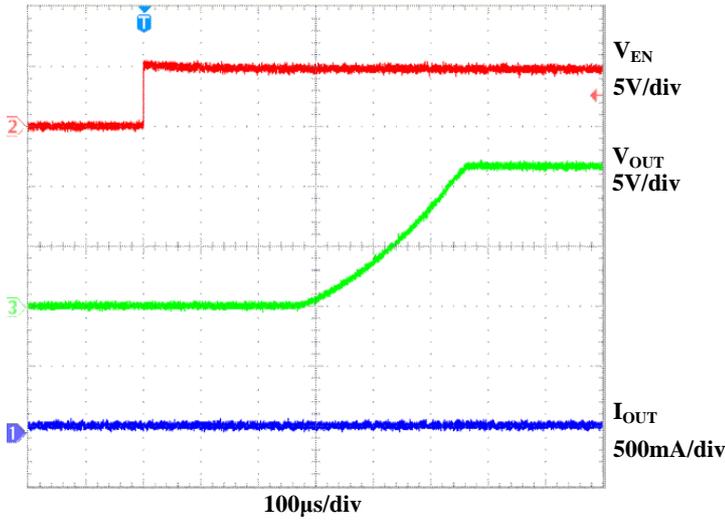
Symbol	Parameter	Conditions	EUP3504			Unit
			Min.	Typ.	Max.	
V_{IN}	Input Voltage		3.3		13.5	V
I_Q	Quiescent Current	$V_{IN}=12V, V_{EN}=V_{IN}, I_{OUT}=0mA$		90	180	μA
I_{SHDN}	Shutdown Current	$V_{IN}=12V, V_{EN}=GND, V_{OUT}=OPEN$	--	2	4	
R_{ON}	On Resistance	$V_{IN}=12V, V_{EN}=V_{IN}$		110		m Ω
V_{ENH}	EN High Level Voltage		2			V
I_{ENH}	EN High Input Current	$V_{EN}=12V$		0.3	1	μA
V_{ENL}	EN Low Level Voltage				0.4	V
I_{ENL}	EN Low Input Current	$V_{EN}=GND$	-1		1	μA
I_{DCHG}	Auto Discharge Current	$V_{EN}=GND, V_{OUT}=12V$		170		mA
I_{LIM}	Current Limit		1.8	2.35	2.8	A
T_{SD}	Thermal Shutdown Temperature			160		°C
T_{SDHYS}	Thermal Shutdown Hysteresis			20		

Note 1: Stresses listed as the above “Absolute Maximum Ratings” may cause permanent damage to the device. These are for stress ratings. Functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may remain possibility to affect device reliability.

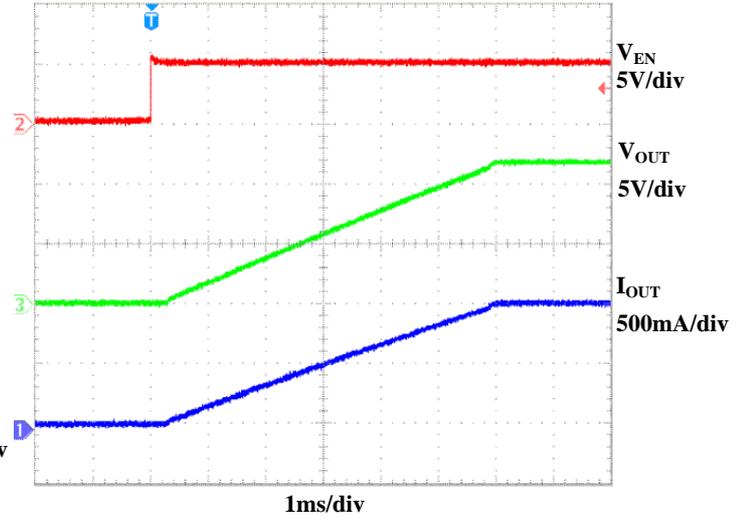
Typical Operating Characteristics

($V_{IN}=12V$, $C_{IN}=1\mu F$, $C_{OUT}=1\mu F$, $C_{SS}=22nF$, $R_I=10\Omega$, unless otherwise noted.)

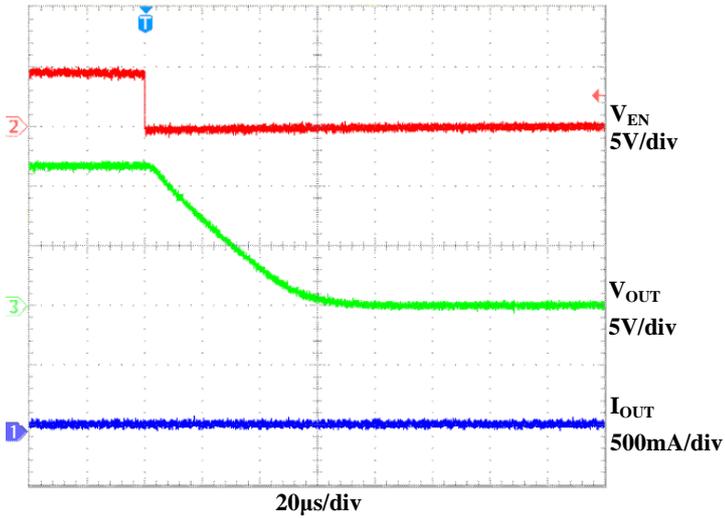
Enable Start-up without Load



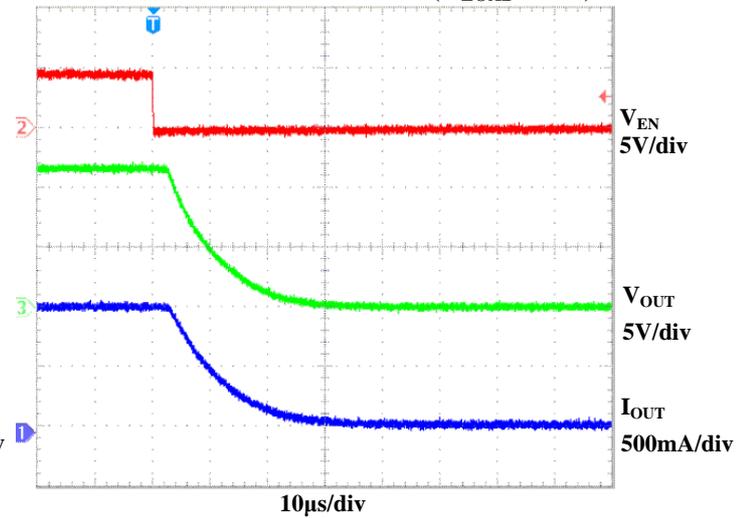
Enable Start-up with Load ($R_{LOAD}=12\Omega$)



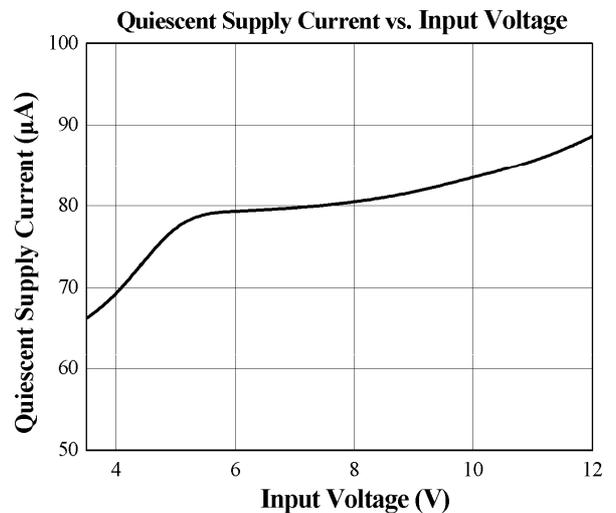
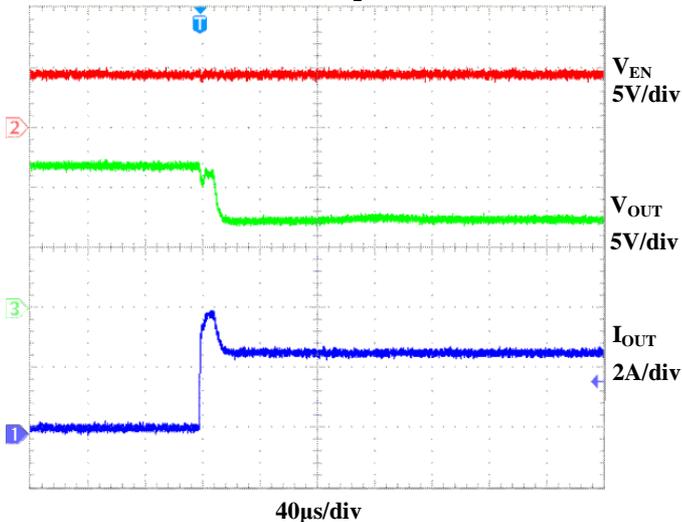
Enable Shutdown without Load



Enable Shutdown with Load ($R_{LOAD}=12\Omega$)

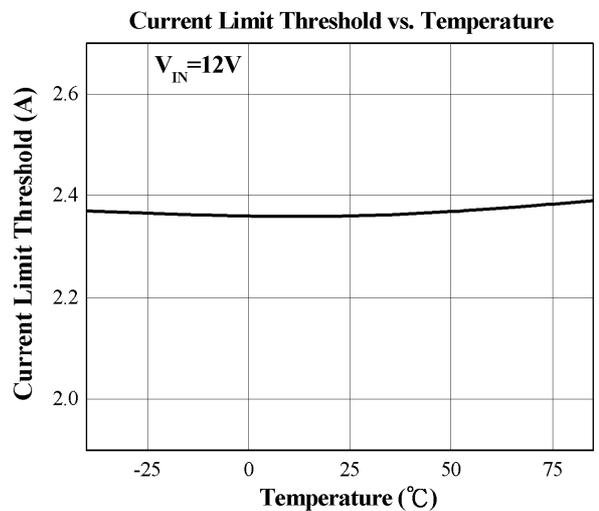
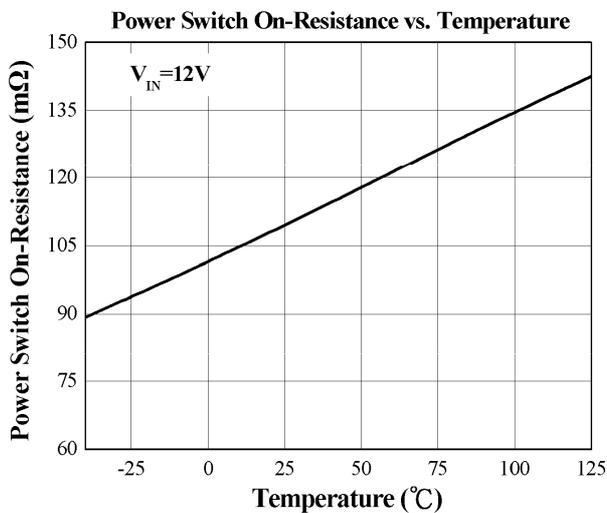
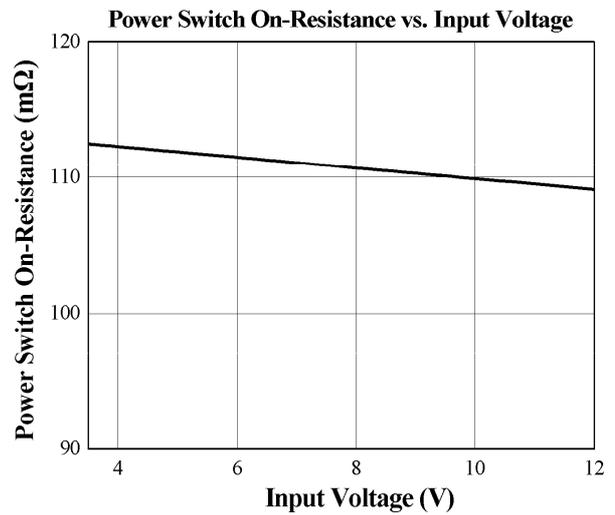
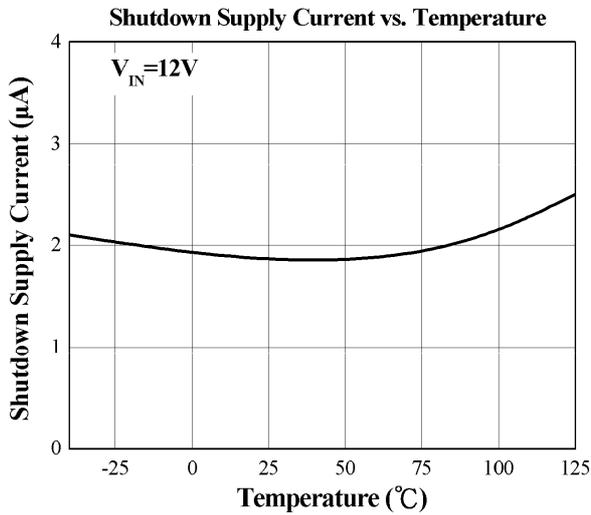
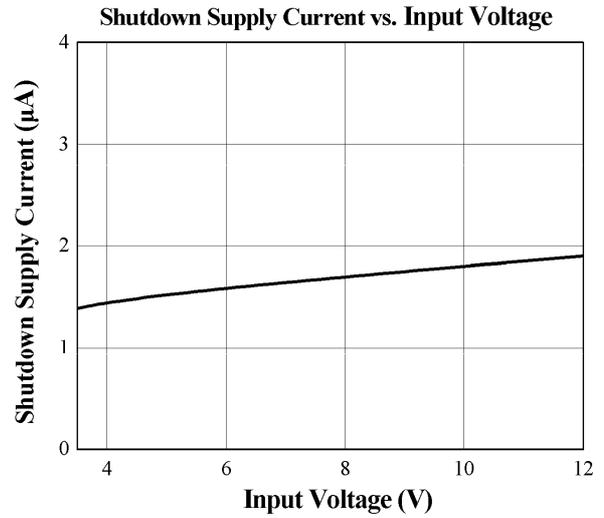
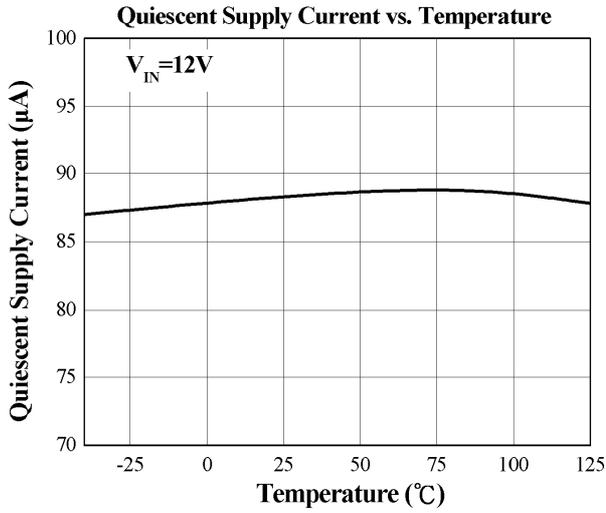


Resistance Load Inrush Response (No Load $\rightarrow R_{LOAD}=3\Omega$)



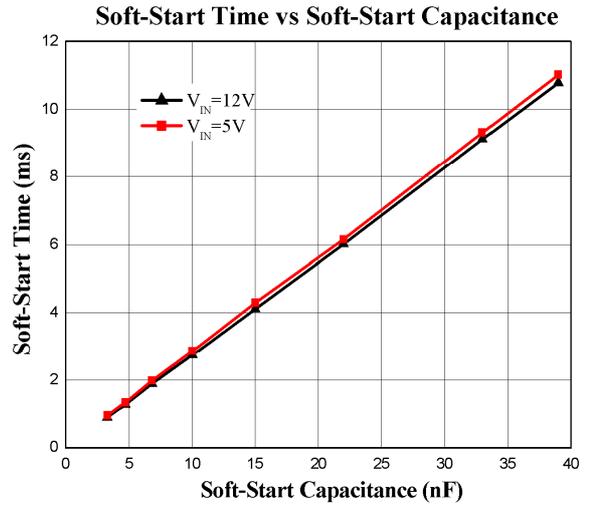
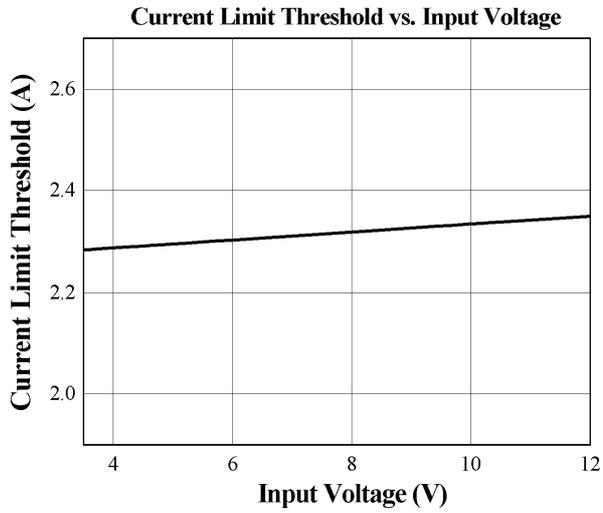
Typical Operating Characteristics (continued)

($V_{IN}=12V$, $C_{IN}=1\mu F$, $C_{OUT}=1\mu F$, $C_{SS}=22nF$, $R_1=10\Omega$, unless otherwise noted.)



Typical Operating Characteristics (continued)

($V_{IN}=12V$, $C_{IN}=1\mu F$, $C_{OUT}=1\mu F$, $C_{SS}=22nF$, $R_I=10\Omega$, unless otherwise noted.)



Functional Description

Input and Output

V_{IN} (input) is the power supply connection to the logic circuitry and the source of the power MOSFET. V_{OUT} (output) is the drain of the power MOSFET. In a typical application, current flows through the switch from V_{IN} to V_{OUT} toward the load. All V_{OUT} pins must connect together to the load.

On/Off Control

The EUP3504 enables an output P-channel MOSFET switch and the IC internal circuitry to turn off by the signal to the EN pin. In the shutdown mode, the V_{OUT} pin will be pulled down to the GND by the auto-discharge function.

The output voltage becomes unstable when the EN pin is opened. If the input voltage to the EN pin is within the specified threshold voltages, the logic is fixed and the EUP3504 will operate normally. However, supply current may increase as a result of the shoot-through current of internal circuitry when the medium level voltage is input to the EN pin.

Auto-Discharge Function

The EUP3504 contains an auto-discharge N-channel transistor between V_{OUT} and GND. The device quickly discharges the electric charge in the output capacitor when a low signal to the EN pin is input to turn off the whole IC. The auto-discharge resistance R_{DCHG} is typically set at 30Ω (with $V_{IN}=V_{OUT}=3.5V$). Discharge time of the output capacitor is determined by the R_{DCHG} and output capacitor value. The discharge time constant, τ , can be written as

$$\tau = C_{OUT} \times R_{DCHG}$$

Current Limiting

With the present of a sense FET, over-current conditions can be detected without increasing the series resistance of the current path. Under over-current condition, the device maintains a constant output current and reduces the output voltage accordingly. Complete shutdown occurs only if the fault is presented long enough to activate thermal limiting. There are several possible over-current conditions can occur.

- The output has been shorted before the device is enabled or before V_{IN} has been applied, the EUP3504 senses the short immediately switches into a constant-current limit mode.
- A short or an overload occurs while the device is enabled. At the instant the overload occurs, high currents may flow for a short period of time before the current-limit circuit can react. After the current limit circuit has tripped (reached the over-current trip threshold), the device switches into constant current mode.

- The load has been gradually increased beyond the recommended operating current. The current is permitted to rise until the current limit threshold is reached or until the thermal limit of the device is exceeded. The EUP3504 is capable of delivering current up to the current limit threshold without damaging the device. Once the threshold has been reached, the device switches into its constant current mode.

The current limit value refer to typical operating characteristics.

Thermal Shutdown

Thermal shutdown protects EUP3504 from excessive power dissipation. If the die temperature exceeds $160^{\circ}C$, the MOSFETS switch is shut off. $20^{\circ}C$ of hysteresis prevents the switch from turning on until the die temperature drops to $140^{\circ}C$. Thermal shutdown circuit functions only when the switch is enabled.

Under-Voltage Lockout

Whenever the input voltage falls below approximately 2.1V, the power switch is quickly turned off. This facilitates the design of hot-insertion systems where it is not possible to turn off the power switch before input power is removed. The UVLO also keeps the switch from being turned on until the power supply has reached at least 2.3V, even if the switch is enabled.

Application Information

Input Power Supply and Input Capacitor

Connect all V_{IN} inputs together externally. V_{IN} powers the internal control circuitry. A 1 μ F bypass capacitor from V_{IN} to GND, located near the EUP3504, is strongly recommended to control supply transients. When driving inductive loads or operating from inductive sources, which may occur when the EUP3504 is powered by long leads or PC traces, larger input bypass capacitance is required to prevent voltage spikes from exceeding the EUP3504's absolute maximum ratings ($V_{INMAX}=13.5V$) during short-circuit events.

Output Capacitor

Bypass V_{OUT} to GND with a 1 μ F ceramic capacitor for local decoupling. Placing a high-value electrolytic capacitor on the output pin(s) is recommended when the output load is heavy. This precaution reduces power-supply transients that may cause ringing on the input and reduces output voltage transients under dynamic load conditions.

Soft-start Capacitor

EUP3504 integrates a current soft-start function to avoid the inrush current at turn-on time. When EUP3504 is powered on or enabled, its current limit threshold is raised slowly to realize the current soft-start. The soft-start capacitor connected from SS pin to GND sets the soft-start time. Larger soft-start capacitance gives longer soft-start time. The recommend soft-start capacitor for different soft-start time is given by the curve of "Soft-Start Time vs. Soft-Start Capacitance" in "Typical Operating Characteristics".

Layout and Thermal Dissipation

Keep all traces as short as possible to reduce the effect of undesirable parasitic inductance and optimize the switch response time to output short circuit conditions. Place input and output capacitors no more than 5mm from device leads. Connect V_{IN} and V_{OUT} to the power bus with short traces. Wide power bus planes at V_{IN} and V_{OUT} provide superior heat dissipation as well.

An active switch dissipates little power with minimal change in package temperature. Calculate the power dissipation for this condition as follows:

$$P = I_{OUT}^2 \times R_{(DS)ON}$$

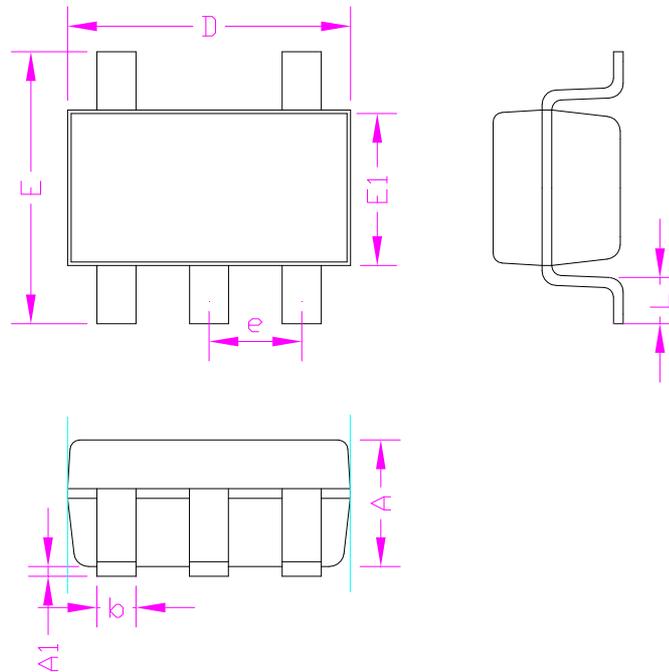
Where I_{OUT} is the output current, $R_{(DS)ON}$ is the on resistance of the switch whose typical value is 110m Ω . The thermal resistance θ_{JA} of SOT23-5 package is 205 $^{\circ}C/W$ and the EUP3504's thermal shutdown threshold T_{SD} is 135 $^{\circ}C$. If we are operating the EUP3504 at room temperature ($T_A=25^{\circ}C$), the maximum power dissipation P_{MAX} can be calculated as below:

$$P_{MAX} = \frac{(T_{SD} - T_A)}{\theta_{JA}} = \frac{(135 - 25)}{205} = 0.54(W)$$

In this case, the EUP3504 die temperature exceeds the 160 $^{\circ}C$ thermal shutdown threshold, and the switch output shuts down until the junction temperature cools by 20 $^{\circ}C$. The duty cycle and period are strong functions of the ambient temperature and the PC board layout (see the Thermal Shutdown section).

Packaging Information

SOT23-5



SYMBOLS	MILLIMETERS			INCHES		
	MIN.	Normal	MAX.	MIN.	Normal	MAX.
A	-	-	1.40	-	-	0.055
A1	0.00	-	0.15	0.000	-	0.006
D	2.65	2.90	3.15	0.104	0.114	0.124
E1	1.40	1.60	1.80	0.055	0.063	0.071
E	2.60	2.80	3.00	0.102	0.110	0.118
L	0.30	0.45	0.60	0.012	0.018	0.024
b	0.30	-	0.50	0.012	-	0.020
e	0.95 REF			0.037 REF		