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David Chang		VERSION : 4
CUSTOMER	ACCEPTANCE SPEC	CIFICATIONS
МО	DEL NO.:	
	ETQ570G0DH6	
	(RoHS)	
FOR	MESSRS:	
		_)
CUSTOMER'S APPROV	AL	
DATE:		
BY:		
		

EMERG	ING D	ISPLAY	MO	DEL NO).			VERSION	PAGE
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RECORD	S OF R	EVISION						M	AR.04, 2009
DATE	REVISED PAGE NO.				SUMI	MARY	Y		
APR.08, 2009	7	7. OUTLINE DIM MARK △: MO			YPE				
AUG.21, 2009	3	4. ELECTRICAL OPOWER SUPPL	Y CU	RRENT	FOR VO		IVER :		
	7	TYP.= $(630) \rightarrow 4$ 7. OUTLINE DIM MARK \triangle : AD	ENSI	ONS	$(20) \rightarrow 58$	30			
FEB.11, 2010	2	3.2 ENVIRONME	NTAL					NGS.	
		I T E M AMBIENT TEMPERA	ATURE	MIN. -10°C	MAX. 60°C	MIN. -20°C	RAGE MAX. 70°C	REMARK NOTE(1),(2)	
		I T E M AMBIENT TEMPERA	ATURE	MIN. -20°C	AATING MAX. 70°C	MIN. -30°C	RAGE MAX. 80°C	REMARK NOTE(1),(2)	
	26	NOTE (1) : Ta A						TY OF LCD N	MODULE
	20	NO ITEM I TEMPERATUR OPERATION TOPERATION TOPERATI	RE HRS THE HRS	SAMPLE SHO	DESC DULD BE ALLO DULD BE ALLO OULD BE ALLO	OWED TO STANDWED	ND AT +60°C I ND AT -10°C I ND AT -70°C I TAND AT -20°C I ND AT -20°C I ND AT +80°C I	FOR 240	WODULE

 MODEL NO.
 VERSION
 PAGE

 E T Q 5 7 0 G 0 D H 6
 4
 0-2

TABLE OF CONTENTS

NO.	I T E M	PAGE
1.	GENERAL SPECIFICATIONS	1
2.	MECHANICAL SPECIFICATIONS	1
3.	ABSOLUTE MAXIMUM RATINGS	2
4.	ELECTRICAL CHARACTERISTICS	3
5.	TIMING CHARACTERISTICS	4
6.	OPTICAL CHARACTERISTICS	5,6
7.	OUTLINE DIMENSIONS	7
8.	BLOCK DIAGRAM	8
9.	DETAIL DRAWING OF DOT MATRIX	9
10.	INTERFACE SIGNALS	10,11
11.	POWER SUPPLY	12
12.	TOUCH PANEL SPECIFICATION	13 ~ 18
13.	INSPECTION CRITERION	19 ~ 28

EMERGING DISPLAY	MODEL NO.	VERSION	PAGE
TECHNOLOGIES CORPORATION	ETQ570G0DH6	4	1

1. GENERAL SPECIFICATIONS

1.1 APPLICATION NOTES FOR CONTROLLER/DRIVER PLEASE REFER TO :

HIMAX HX8218 HIMAX HX8615

1.2 MATERIAL SAFETY DESCRIPTION

ASSEMBLIES SHALL COMPLY WITH EUROPEAN ROHS REQUIREMENTS, INCLUDING PROHIBITED MATERIALS/COMPONENTS CONTAINING LEAD, MERCURY, CADMIUM, HEXAVALENT CHROMIUM, POLYBROMINATED BIPHENYLS (PBB) AND POLYBROMINATED DIPHENYL ETHERS (PBDE)

2. MECHANICAL SPECIFICATIONS

(1) DIAGONALS	5.7 inch
(2) NUMBER OF DOTS	320W * (RGB) * 240H DOTS
(3) MODULE SIZE	124.7W * 100H * 12.3D(MAX.) mm
	(WITHOUT FPC)
(4) EFFECTIVE AREA	117.2W * 88.4H mm (T/P)
(5) ACTIVE AREA	115.2W * 86.4H mm (LCD)
	116.2W * 87.4H mm (T/P)
(6) DOT SIZE	0.12W * 0.36H mm
(7) PIXEL SIZE	0.36W * 0.36H mm
(8) LCD TYPE	TFT, TRANSMISSIVE
(9) COLOR	262K
(10) VIEWING DIRECTION	6 O'CLOCK
(11) BACK LIGHT	LED , COLOR : WHITE
(12) INTERFACE MODE	RGB 18 BIT PARALLEL

MODEL NO.	VERSION	PAGE
ETQ570G0DH6	4	2

3. ABSOLUTE MAXIMUM RATINGS

3.1 ELECTRICAL ABSOLUTE MAXIMUM RATINGS.

PARAMETER	SYMBOL	MIN.	MAX.	UNIT	REMARK
POWER SUPPLY VOLTAGE	VDD-VSS	-0.3	7.0	V	
FOWER SUFFLY VOLTAGE	VCC-VSS	-0.3	7.0	V	
INPUT SIGNAL VOLTAGE	VL-VSS	-0.3	VCC+0.3	V	
STATIC ELECTRICITY				V	NOTE (1)
LED BACKLIGHT POWER DISSIPATION	PD		1.28	W	
LED BACKLIGHT FORWARD CURRENT	IF		0.06	A	
LED BACKLIGHT REVERSE VOLTAGE	VR	_	45	V	

NOTE (1): LCM SHOULD BE GROUNDED DURING HANDING LCM.

3.2 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS.

ITEM	OPERATING		STOR	RAGE	REMARK	
I I E WI	MIN.	MAX.	MIN.	MAX.	KEMAKK	
AMBIENT TEMPERATURE	-20°C	70°C	-30°C	80°C	NOTE(1),(2)	
HUMIDITY	NOTI	E(3)	NOTI	E (2)	WITHOUT	
HOMBIT I	NOTI	E(3)	NOTI	D(3)	CONDENSATION	
VIBRATION	_	2.45 m/s ² (0.25 G)	_	11.76 m/s ² (1.2 G)	5~20Hz, 1HR 20~500Hz(20Hz), 1HR 20~500Hz(500Hz), 1HR X, Y, Z, TOTAL 3HRS	
SHOCK	_	29.4 m/s ² (3 G)	_	490 m/s ² (5 0 G)	10 m SECONDS XYZ DIRECTIONS 1 TIME EACH	
CORROSIVE GAS	NOT ACC	EPTABLE	NOT ACCEPTABLE			

NOTE (1): Ta AT -30°C: 48HRS MAX.

80°C:168HRS MAX.

NOTE (2): BACKGROUND COLOR CHANGES SLIGHTLY DEPENDING ON AMBIENT

TEMPERATURE THIS PHENOMENON IS REVERSIBLE.

NOTE (3): $Ta \le 60^{\circ}C : 90\%RH MAX (96HRS MAX)$.

Ta > 60°C: ABSOLUTE HUMIDITY MUST BE LOWER THAN THE HUMIDITY

OF 90%RH AT 60°C(96HRS MAX).

E M E R G I N G D I S P L A Y TECHNOLOGIES CORPORATION

MODEL NO.	VERSION	PAGE
ETO570G0DH6	4	3

4. ELECTRICAL CHARACTERISTICS

 $Ta = 25 \, ^{\circ}C$

							1a - 25 °C
PARAMETER	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	REMARK
POWER SUPPLY VOLTAGE FOR DIGITAL	VDD-VSS	_	3	3.3	3.6	V	
POWER SUPPLY VOLTAGE FOR VCOM DRIVER	VCC-VSS		3	3.3	3.6	V	
POWER SUPPLY CURRENT FOR DIGITAL	IDD	VDD-VSS =3.3V		8	11	mA	NOTE (1)
POWER SUPPLY CURRENT FOR VCOM DRIVER	ICC	VCC-VSS = 3.3V LED B/L=ON	_	450	580	mA	
LOW LEVEL INPUT VOLTAGE	VIL		0	_	0.3*VDD	V	NOTE (2)
HIGH LEVEL INPUT VOLTAGE	VIH		0.7*VDD	_	VDD	V	NOTE (2)
LOW LEVEL OUTPUT VOLTAGE	VOL	$IOL = 400 \mu A$	0		0.2*VDD	V	NOTE (3)
HIGH LEVEL OUTPUT VOLTAGE	VOH	$IOH = -400 \mu A$	0.8*VDD	_	VDD	V	NOTE (3)
FRAME FREQUENCY	fFRAME	_		83	92	Hz	
DOT DATA CLOCK	DCLK			6.4	7.1	MHz	
POWER SUPPLY FOR LED BACKLIGHT	V_{F}	I _F =40mA	28	30	32	V	NOTE (4)
LED LIFE TIME	_	_	30000	40000		HRS	

NOTE (1): THE DISPLAY PATTERN IS ALL "WHITE".

NOTE (2): APPLIED TO TERMINALS /RESET, HSYNC, VSYNC, ENB, DCLK, B5~B0, G5~G0, R5~R0.

NOTE (3): APPLIED TO TERMINALS B5~B0, G5~G0, R5~R0.

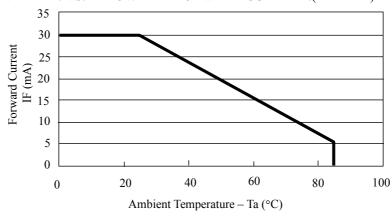
NOTE (4): INTERNAL CIRCUIT DIAGRAM OF BACKLIGHT

(VF=VRI+(A)—VRI 1–(K1)=VRI+(A)—VRI 2–(K2))

(VF=VBL+(A)—VBL1-(K1)=VBL+(A)—VBL2-(K2))



NOTE (5): AMBIENT TEMP. VS. ALLOWABLE FORWARD CURRENT.(PER LED)

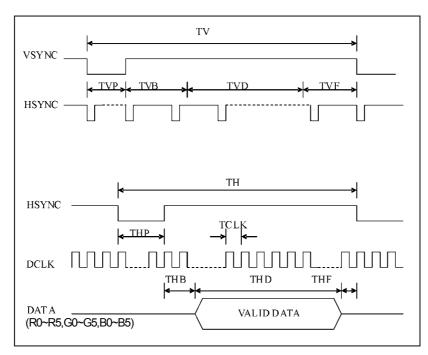


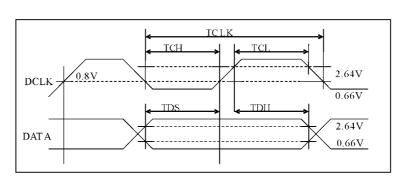
MODEL NO.	VERSION	PAGE
ETO570G0DH6	4	4

5. TIMING CHARACTERISTICS

5.1 DIGITAL PARALLEL RGB INTERFACE

SIGNAL	ITEM		SYMBOL	MIN.	TYP.	MAX.	UNIT
	FREQUENCY		TCLK		6.4	7.1	MHz
DCLK	HIGH TIME		TCH		78	_	ns
	LOW TIME		TCL		78	_	ns
DATA	SETUP TIME		TDS	12	_	_	ns
DATA	HOLD TIME		TDH	12	_	_	ns
	PERIOD		TH		408	_	DCLK
	PULSE WIDTH		THP		30	_	DCLK
HSYNC	BACK-PORCH	BACK-PORCH		_	38	_	DCLK
	DISPLAY PERIOD		THD	_	320	_	DCLK
	FRONT-PORCH		THF		20	_	DCLK
	PERIOD	NTSC PAL	TV		262.5 312.5	_	TH
	PULSE WIDTH		TVP	1	3	5	TH
VSYNC	BACK-PORCH	NTSC	TVB		15		TH
VSTNC	BACK-PORCH	PAL	1 4 D		23		111
	DISPLAY PERIOD		TVD	_	240	_	TH
	FRONT-PORCH	NTSC PAL	TVF	_	4.5 46.5	_	TH





MODEL NO.	VERSION	PAGE
ETQ570G0DH6	4	5

6. OPTICAL CHARACTERISTICS (NOTE 1)

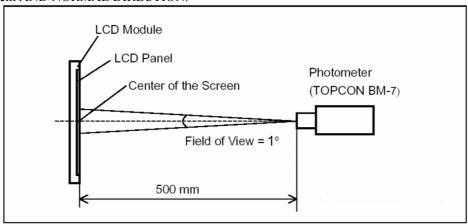
6.1 OPTICAL CHARACTERISTICS

 $Ta = 25 \pm 2$ °C

-								1 a	$= 23 \pm 2$ °C						
I T E	M	SYMBOL CONDITION		MIN.	TYP.	MAX.	UNIT	REMARK							
		θ_{y^+}		0 -00	55	60									
MIEWING ANG	T.F.	$\theta_{ ext{y-}}$	CD > 10	$\theta_{x}=0$ °	70	75		4	NOTE (2)						
VIEWING ANG	LE	$\theta_{\mathrm{x}^{+}}$	CR ≥ 10	CR ≥ 10	CK 2 10	CR ≥ 10	CR ≥ 10	CK ≥ 10	CK ≥ 10	0 -00	70	75		deg.	NOTE (3)
		θ_{x}		$\theta_{y}=0^{\circ}$	70	75									
CONTRAST RAT	TIO	CR	θx=0°,	θy=0°	300	400			NOTE (3)						
RESPONSE TIM	Е	T _R (rise)	000	000		15	30		NOTE (4)						
RESPONSE TIM	.E	T _F (fall)	θx=0°, θy=0°			35	50	msec	NOTE (4)						
	WHITE	Wx			0.27	0.32	0.37								
		Wy			0.30	0.35	0.40								
COLOD OF	DED	Rx	θx=0°,		0.58	0.63	0.68								
COLOR OF	RED	Ry			0.31	0.36	0.41		NOTE (5)						
CIE COORDINATE	GREEN	Gx	$I_F = 4$ NTSC	0mA C:60%	0.28	0.33	0.38	NOTE	NOTE (5)						
COORDINATE	GREEN	Gy	1,150		0.55	0.60	0.65								
	DLUE	Bx			0.09	0.14	0.19								
	BLUE	Ву				0.11	0.16								
THE BRIGHTNESS OF MODULE THE UNIFORMITY OF MODULE		В	θx=0°, θy=0°		360	400	_	cd/m ²	NOTE (6)						
		_		0mA	75	80		%	NOTE (6)						

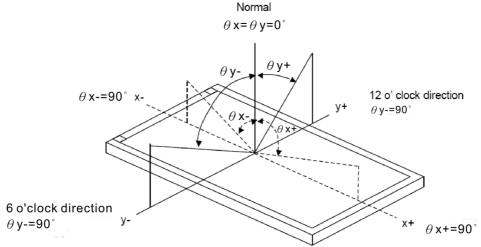
NOTE (1): TEST EQUIPMENT SETUP:

AFTER STABILIZING AND LEAVING THE PANEL ALONE AT A GIVEN TEMPERATURE FOR 30 MINUTES, THE MEASUREMENT SHOULD BE EXECUTED. MEASUREMENT SHOULD BE EXECUTED IN A STABLE, WINDLESS, AND DARK ROOM. OPTICAL SPECIFICATIONS ARE MEASURED BY TOPCON BM-7 (FAST) WITH A VIEWING ANGLE OF 1° AT A DISTANCE OF 50cm AND NORMAL DIRECTION.



MODEL NO.	VERSION	PAGE
ETQ570G0DH6	4	6

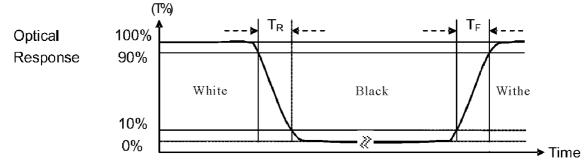
NOTE (2): DEFINITION OF VIEWING ANGLE:



NOTE (3): DEFINITION OF CONTRAST RATIO:

CONTRAST RATIO(CR) = $\frac{\text{BRIGHTNESS MEASURED WHEN LCD IS AT "WHITE STATE"}}{\text{BRIGHTNESS MEASURED WHEN LCD IS AT "BLACK STATE"}}$

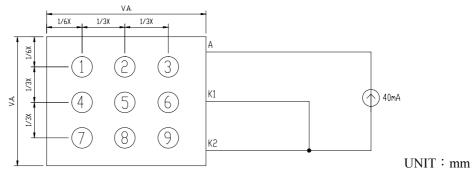
NOTE (4) : DEFINITION OF RESPONSE TIME : T_R AND T_F THE FIGURE BELOW IS THE OUTPUT SIGNAL OF THE PHOTO DETECTOR.



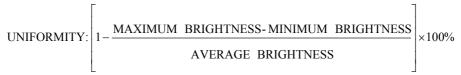
NOTE (5) : THE 100% TRANSMISSION IS DEFINED AS THE TRANSMISSION OF LCD PANEL WHEN ALL THE INPUT TERMINALS OF MODULE ARE ELECTRICALLY OPENED.

NOTE (6) : BRIGHTNESS MEASURED WHEN LCD IS AT " WHITE STATE "

6.2 THE TEST METHOD OF BRIGHTNESS AND UNIFORMITY



6.3 THE CALCULATING METHOD OF UNIFORMITY

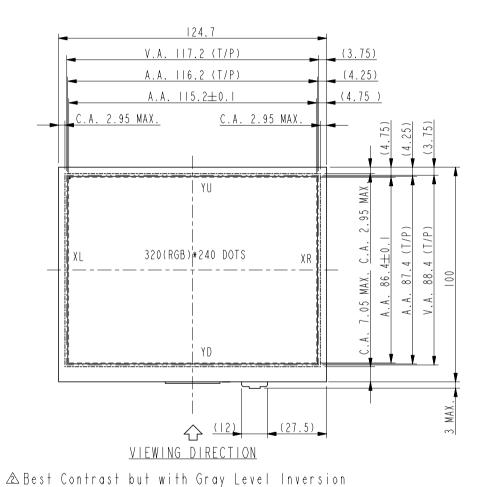


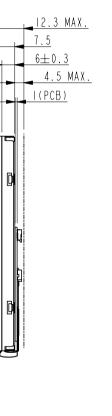
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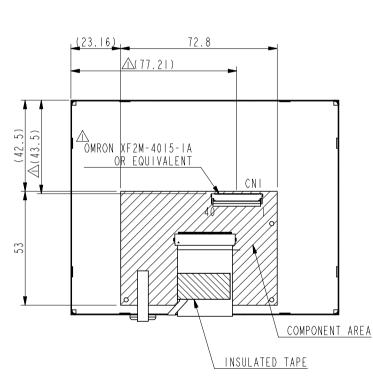
MODEL NO. E T Q 5 7 0 G 0 D H 6 VERSION 4

PAGE

7. OUTLINE DIMENSIONS







UNIT : mm SCALE : NTS

NOT SPECIFIED TOLERANCE IS ± 0.5

NOTE : MARK 🛆 MODIFY (NUMBER NOTE MODIFY VERSION)

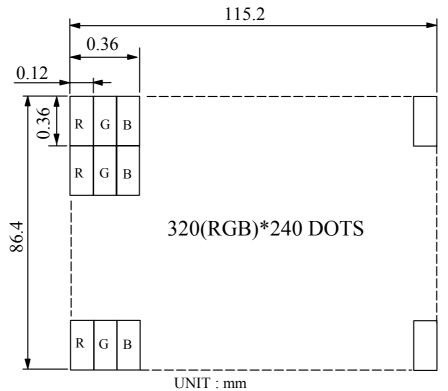
MODEL NO. VERSION PAGE EMERGING DISPLAY TECHNOLOGIES CORPORATION ETQ570G0DH6 4 8 8. BLOCK DIAGRAM TFT LCD PANEL GATE SOURCE DRIVER DRIVER T-CDN FRAME GROUND PCB LED VGL, VGH, AVDD TOUCH PANEL BACKLIGHT VCOM,POL,VDD,VCC

EMERGING	DISPLAY
TECHNOLOGIES	CORPORATION

 MODEL NO.
 VERSION
 PAGE

 ETQ570G0DH6
 4
 9





SCALE : NTS

NOT SPECIFIED TOLERANCE IS \pm 0.1 DOTS MATRIX TOLERANCE IS \pm 0.01

MODEL NO.	VERSION	PAGE
ETQ570G0DH6	4	10

10. INTERFACE SIGNALS

PIN NO	SYMBOL	I/O	FUNCTION
1	/RESET	I	HARDWARE RESET
2	VSS	P	GROUND (VSS IS CONNECTED TO METAL HOUSING WITH CONDUCTIVE TAPE)
3	B5	Ι	BLUE DATA BIT 5
4	B4	I	BLUE DATA BIT 4
5	В3	Ι	BLUE DATA BIT 3
6	B2	Ι	BLUE DATA BIT 2
7	B1	Ι	BLUE DATA BIT 1
8	В0	Ι	BLUE DATA BIT 0
9	VSS	P	GROUND (VSS IS CONNECTED TO METAL HOUSING WITH CONDUCTIVE TAPE)
10	G5	I	GREEN DATA BIT 5
11	G4	I	GREEN DATA BIT 4
12	G3	Ι	GREEN DATA BIT 3
13	G2	Ι	GREEN DATA BIT 2
14	G1	Ι	GREEN DATA BIT 1
15	G0	Ι	GREEN DATA BIT 0
16	VSS	P	GROUND (VSS IS CONNECTED TO METAL HOUSING WITH CONDUCTIVE TAPE)
17	R5	I	RED DATA BIT 5
18	R4	I	RED DATA BIT 4
19	R3	I	RED DATA BIT 3
20	R2	I	RED DATA BIT 2
21	R1	I	RED DATA BIT 1
22	R0	I	RED DATA BIT 0
23	VSS	P	GROUND (VSS IS CONNECTED TO METAL HOUSING WITH CONDUCTIVE TAPE)
24	DCLK	I	DOT DATA CLOCK
25	VSS	P	GROUND (VSS IS CONNECTED TO METAL HOUSING WITH CONDUCTIVE TAPE)
26	HSYNC	I	HORIZONTAL SYNC INPUT
27	VSYNC	I	VERTICAL SYNC INPUT
28	ENB	I	DATA ENABLE INPUT

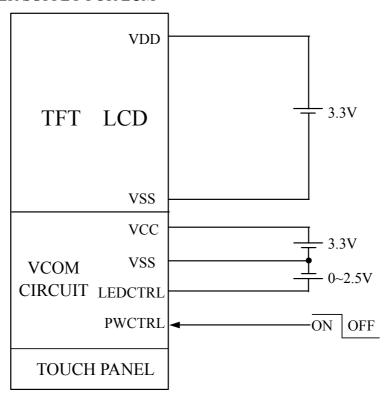
PIN NO	SYMBOL	I/O	FUNCTION			
				PWCTI	RL	REMARK
			LOGIC LEVEL	Н		POWER ON
29	PWCTRL	I	H=3.3V L=0V	L		SHUTDOWN
			WHEN INTERNAL LE	D DRIVER : JI	P15 1-2(DE	FAULT)
			WHEN EXTERNAL LE	ED DRIVER : J	P15 2-3	
30	VDD	P	POWER SUPPLY FOR	DIGITAL CIR	CUIT	
31	VSS	P	GROUND (VSS IS CON CONDUCTIVE TAPE)	NNECTED TO	METAL H	OUSING WITH
32	VSS	P	GROUND (VSS IS CONNECTED TO METAL HOUSING WITH CONDUCTIVE TAPE)			
33	VCC	P	POWER SUPPLY FOR VCOM DRIVER CIRCUIT			
34	VCC	P	POWER SUPPLY FOR VCOM DRIVER CIRCUIT			
35	NC		NON CONNECTION(USING INTERNAL LED DRIVER) OR ANODE (USING EXTERNAL LED DRIVER) WHEN INTERNAL LED DRIVER: JP5 1-2 (DEFAULT) WHEN EXTERNAL LED DRIVER: JP5 2-3			
36	LEDCTRL	I	BRIGHTNESS CONTROL FOR LED BACKLIGHT; LEDCTRL (USING INTERNAL LED DRIVER) OR CATHODE (USING EXTERNAL LED DRIVER) WHEN INTERNAL LED DRIVER: JP6 1-2 (DEFAULT) JP14 1-2 (DEFAULT) WHEN EXTERNAL LED DRIVER: JP6 2-3 JP14 NON CONNECTION			
37	YU	_	TOP PANEL			
38	XL	_	LEFT PANEL			
39	YD				TOUCH PA	ANEL
40	XR		RIGHT PANEL			

 MODEL NO.
 VERSION
 PAGE

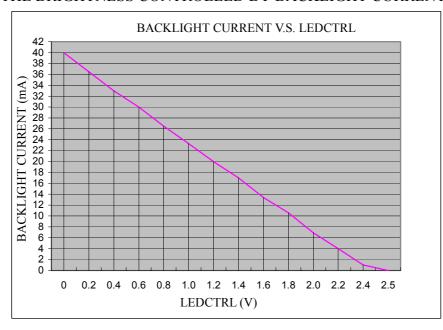
 ETQ570G0DH6
 4
 12

11. POWER SUPPLY

11.1 POWER SUPPLY FOR LCM



11.2 THE BRIGHTNESS CONTROLLED BY BACKLIGHT CURRENT OF LEDCTRL



MODEL NO.	VERSION	PAGE
ETQ570G0DH6	4	13

12. TOUCH PANEL SPECIFICATION

12.1 ELECTRICAL CHARACTERISTICS

 $Ta = 25^{\circ}C$

ITEM	CONDITION	SPEC.	UNIT
LINEARITY	_	≤ 1.5	%
TRANSMISSION	ASTM D1003	80 OR MORE	%
ON LOAD	POLYACETAL PEN INPUT	15 ~ 80	g
TERMINAL RESISTANCE	X AXIS	400 ~ 1000	Ω
TERMINAL RESISTANCE	Y AXIS	200 ~ 700	22
INSULATION RESISTANCE	DC25V	≥ 10	$ ext{M}\Omega$
INPUT VOLTAGE		5	V

12.2 PRECAUTIONS IN USE OF TOUCH PANEL

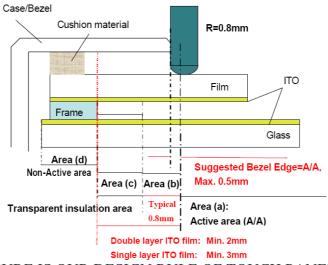
12.2.1 PURPOSE:

IN ORDER TO PREVENT ACCIDENTAL USE AND PERFORMANCE DETERIORATION, PLEASE KEEP THE FOLLOWING PRECAUTIONS AND INHIBITED POINTS.

12.2.2 ITEM AND ILLUSTRATION:

(1) STRUCTURE, AREA DEFINITION

THE STRUCTURE AND THE PERFORMANCE GUARANTEED AREA OF THIS TOUCH PANEL ARE DEFINED BELOW:



THE ABOVE FIGURE IS OUR DESIGN RULE OF TOUCH PANEL. IF IT CANNOT MEET YOUR REQUIREMENT, PLEASE CONTACT WITH OUR ENGINEERS FOR FURTHER DISCUSSION.

ABOVE FIGURE ILLUSTRATES THE RECOMMENDED BEZEL AND CUSHION DESIGN. IN ORDER TO PREVENT

UNUSUAL PERFORMANCE DEGRADATION AND MALFUNCTION OF A TOUCH PANEL, PLEASE CARRY OUT THE SET

CASE DESIGNING AND A TOUCH PANEL ASSEMBLING METHOD AFTER SURELY CONSIDERING THE DEFINITION OF EACH AREA ILLUSTRATED IN ABOVE FIGURE.

E M E R G I N G D I S P L A Y TECHNOLOGIES CORPORATION

 MODEL NO.
 VERSION
 PAGE

 ETQ570G0DH6
 4
 14

AREA(a): ACTIVE AREA

THE ACTIVE AREA IS GUARANTEED THE POSITION DATA DETECTABLE PRECISION, OPERATION FORCE AND OTHER OPERATIONS. IT IS STRONGLY RECOMMENDED TO PLACE THE OPERATION BUTTON OR MENU KEYS WITHIN THE ACTIVE AREA. DUE TO STRUCTURE, THE ACTIVE AREA IS LESS DURABLE AT THE EDGE OR CLOSE TO THE EDGE.

AREA(b): OPERATION NON-GUARANTEED AREA

THIS AREA DOES NOT GUARANTEE A TOUCH PANEL OPERATION AND
ITS FUNCTION. WHEN THIS AREA IS PRESSED, TOUCH PANEL SHOWS
DEGRADATION OF ITS PERFORMANCE AND DURABILITY SUCH AS A PEN
SLIDING DURABILITY BECOMES ABOUT ONE-TENTH COMPARED WITH
THE ACTIVE AREA (AREA-(A) AS GUARANTEED AREA) AND ITS
OPERATION FORCE REQUIRES ABOUT DOUBLE. ABOUT 0.5 MM OUTSIDE
FROM A BOUNDARY OF THE ACTIVE AREA CORRESPONDS TO THIS AREA.

AREA(c): PRESSING PROHIBITION AREA

THE AREA WHICH FORBIDS PRESSING, BECAUSE AN EXCESSIVE LOAD IS
APPLIED TO A TRANSPARENT ELECTRODE (ITO) AND A SERIOUS DAMAGE
IS GIVEN TO A TOUCH PANEL FUNCTION BY PRESSING.

AREA(d): NON-ACTIVE AREA
THE AREA DOES NOT ACTIVATE EVEN IF PRESSED.

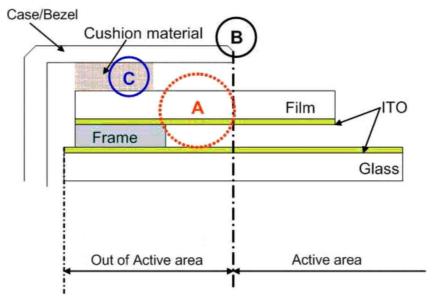
- (2) CAUTIONS FOR INSTALLING AND ASSEMBLING
 - (i) DO NOT GIVE EXCESSIVE STRAIN TO THE PRODUCT.

IT MAY CAUSE THE DAMAGE OF THE ITO FILM.

- (ii) FLEXIBLE PATTERN CABLE IS CONNECTED TO THE BODY BY THERMAL PRESSURE METHOD. SO, DO NOT APPLY EXCESSIVE FORCES TO THE FLEXIBLE PATTERN. DO NOT ADD AN EXCESSIVE FORCE TO A FPC(FLEX TAIL) THAT MAKES PEELING OFF OF THE FPC FROM THE PRODUCT. DO NOT FIX, ADHERE OR MOUNT ANY ADDITIONAL GOODS ON THE FPC SUCH AS ADDITIONAL FILM/PLATE ON THE FPC, BECAUSE SUCH ADDITIONAL GOODS WILL APPLY A STRESS AT THE FPC BONDING AREA. IT MAY AFFECT THE CONDUCTIVITY OF FPC WITH TOUCH PANEL.
- (iii) IN ORDER NOT TO APPLY LOAD ON THE DISPLAY, PROVIDE A CLEARANCE OF AT LEAST 0.3MM BETWEEN THE PRODUCT AND DISPLAY.
- (iv) WE RECOMMEND THE DESIGN OF A CASE OR BEZEL SHOULD COVERS THE BOUNDARY OF THE ACTIVE AREA INSIDE IN ORDER TO PREVENT AN OPERATION AT OUTSIDE OF THE ACTIVE AREA WHICH CAN NOT GUARANTEE THE FUNCTION OR DURABILITY (REFER TO ITEM 5.1.2. STRUCTURE, AREA DEFINITION).

 BEZEL'S EDGE PART MAY GUIDE THE PEN SLIDING ON THE SAME POSITION REPEATEDLY. IF THE BEZEL IS PLACED OUTSIDE OF THE ACTIVE AREA,

(v) PRESSING INSIDE OF BOUNDARY OF THE FRAME(PART (A) AS SHOWN IN BELOW) MAY CAUSES FAULT OPERATION, SO PLEASE DESIGN TO AVOID PRESSING OF TOUCH PANEL AT PART (A) SUCH AS HAVING GASKET/CUSHION AT PART (C). PARTICULARLY THE AREA (B) SHALL BE FREE FROM BURR. THE GASKET/CUSHION MATERIAL AT THE PART (C) SHOULD NOT BE EXCEEDED TO INSIDE OF THE BOUNDARY OF THE FRAME.



- (vi) TO PREVENT GIVING DISTORTION TO THE FILM OF THE PRODUCT AND PEELING OFF OF THE FILM FROM THE PRODUCT, DO NOT FIX THE FILM AND A SET CASE OR A SHOCK ABSORBING MATERIAL ADHERED TO A SET CASE BY ADHESION.
- (vii) WIPE OFF THE STAIN ON THE PRODUCT BY USING SOFT CLOTH MOISTENED WITH ETHANOL. TAKE CARE NOT TO ALLOW ETHANOL TO SOAK INTO THE JOINT OF UPPER FILM AND BOTTOM GLASS. IT MAY OTHERWISE CAUSE PEELING OR DEFECTIVE OPERATION. DO NOT USE ANY ORGANIC SOLVENT OR DETERGENT OTHER THAN ETHANOL.
- (viii) THE CORNERS OF THE PRODUCT ARE NOT CHAMFERED AND ARE SHARP. WHEN POSITIONING AND FIXING THE PRODUCT ON THE CASE, PROVIDE A ROUND PART ON THE CORNER OF THE CASE SO AS NOT TO APPLY LOAD ON THE CORNER OF THE TRANSPARENT TOUCH PANEL.
 - (ix) DO NOT PRESS THE FILM OF THE PRODUCT WHEN THIS PRODUCT IS BUILT INTO A SET.
- (3) CAUTIONS FOR OPERATION
 - (i) OPERATE IT WITH A POLYACETAL PEN (TIP R0.8 OR OVER) OR A BELLY OF A FINGER WITHOUT APPLYING EXCESSIVE LOAD. NEVER USE ANY MECHANICAL PENCILS, BALL POINT PENS AND HARD FINGERTIPS WHO'S TIP IS HARD FOR INPUT, OTHERWISE MALFUNCTIONS MAY RESULT.

MODEL NO.	VERSION	PAGE
ETQ570G0DH6	4	16

- (ii) THE INPUT POSITION MAY BE FLUCTUATED A LITTLE THROUGH LONG-TIME USE. IT IS DESIRABLE TO PROVIDE A ZERO-ADJUSTMENT FUNCTION BY USING A CIRCUIT AND SOFTWARE.
- (iii) OPERATION AT THE OUT OF ACTIVE AREA IS OUT OF OUR GUARANTEE. IT CAUSES A SERIOUS DAMAGE OF A TRANSPARENT ELECTRODE. DO NOT OPERATE AT THE OUT OF ACTIVE AREA.
- (iv) IN CASE OF CLEANING THE PART OF THE CASE BOUNDARY OF ACCOMPLISHED SET, USE A SOFT CLOTH WITH A FINGER BERRY OR A COTTON BUD. DO NOT CLEAN WITH A THI NG OTHER THAN THE FINGER SUCH AS HARD OR SHARP EDGES LIKE A FINGER NAIL ETC. ON THE CLOTH, BECAUSE IT CAUSE TRANSPARENT CONDUCTIVE FILM CRACKS. PLEASE ADVISE THIS PROHIBITION TO YOUR LAST CUSTOMERS.

12.3 DURABILITY

12.3.1 STYLUS HITTING:

ONE MILLION TIMES OR OVER NO DAMAGE ON FILM SURFACE PEN: R8 mm SILICON RUBBER

LOAD: 250g

FREQUENCY: 240 times/min MEASUREMENT POSITION:

1 POINT OF TOUCH PANEL ACTIVE AREA

REPEATED: OVER 1,000,000 TIMES

12.3.2 PEN TOUCH SLIDING DURABILITY:

100,000 TIMES OR OVER

WRITING WITH R0.8mm PLASTIC STYLUS PEN; WRITING FORCE 150g IN ACTIVE AREA.

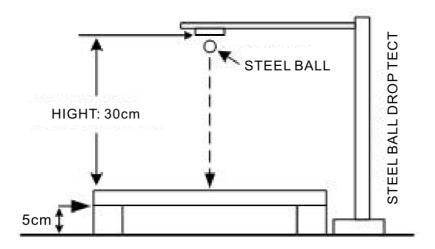
SPEED IS 60mm/sec.

MODEL NO.	VERSION	PAGE
ETQ570G0DH6	4	17

12.4 STEEL BALL DROP TEST

BY USING F9mm STEEL BALL FROM THE HEIGHT OF 30cm AND FALLING ON TOUCH PANEL SURFACE, MUST PASS BELOW CONDITIONS:

APPEARANCE: THE APPEARANCE WITHOUT ANY CHANGE, INCLUDING THE PANEL BROKEN.



12.5 APPEARANCE INSPECTION

PURPOSE:

TO ESTABLISH APPEARANCE STANDARD AND MAINTAIN PRODUCT QUALITY \circ

SCOPE:

TOUCH PANEL VIEW AREA WITHIN TOUCH PANEL •

12.5.1 RULE:

INSPECTION CONDITION

- (A) ENVIRONMENTAL LUMINANCE: 500 LUX •
- (B) DISTANCE BETWEEN HUMAN EYES AND PANEL: 30 CM (PANEL MUST BE TESTED UNDER LIGHT TRANSPARENT) •
- (C) VISUAL ANGEL : $> 60^{\circ}$
- (D) LIGHT SOURCE: FLUORESCENT LIGHT SOURCE •

12.5.2 JUDGE CRITERION:

JUDGEMENT UNDER ABOVE MENTIONED CRITERION (PANEL MUST BE TESTED UNDER LIGHT TRANSPARENT),

TESTING GOODS DEFECT CAN BE VISIBLE WITHIN 10 SECONDS, WHICH WILL BE JUDGED AS MAJOR DEFECTS •

SAMPLING STANDARD:

THE SAMPLING STANDARD WILL BE CONFIRMED BY BOTH OF EDT AND CUSTOMER.

MODEL NO. VERSION PAGE ETQ570G0DH6 4 18

INSPECTION ITEMS	SEPC.		JUDGE CRITERION	OPERATION GUIDELINE	
SCRATCH	W ≤ 0.05mm & L≤10mm		ACCEPTABLE	REFL	
SCRATCH	W > 0.051	mm or $L > 10$ mm	NOT ACCEPTABLE	BACK GROUND TESTING GOODS	
LINEAR FOREIGN	$W \leq 0.03$	5mm & L ≤5mm	ACCEPTABLE		
OBJECT	W > 0.05	5mm or L >5mm	NOT ACCEPTABLE	300mm = = →	
	D:	≤ 0.25mm	ACCEPTABLE	60° ENVIRONMENTAL IUMINANCE : 500Lux	
GRANULAR FOREIGN OBJECT	0.25mm < D ≤0.30mm		MAX. 2 EA	FLUORESCENT LIGHT SOURCE TESTING GOODS 300mm	
OBJECT	D >0.30mm		NOT ACCEPTABLE	ENVIRONMENTAL IUMINANCE : 500Lux	
PET BUBBLES	D ≤0.5mm D >0.5mm		ACCEPTABLE	D	
TET BOBBLES			NOT ACCEPTABLE	D	
CHIP ON GLASS	CORNER	$X \le 3 \text{mm}$ $Y \le 3 \text{mm}$ $Z < t$ (t = /thickness)	ACCEPTABLE	Chip of glass	
	EDGE	$W \le 3mm \cdot Y \le 3mm \cdot Z < t$		X X X X	

MODEL NO.	VERSION	PAGE
ETQ570G0DH6	4	19

13. INSPECTION CRITERION

13.1 APPLICATION

THIS INSPECTION STANDARD IS TO BE APPLIED TO THE LCD MODULE DELIVERED FROM EMERGING DISPLAY TECHNOLOGIES CORP.(E.D.T) TO CUSTOMERS

13.2 INSPECTION CONDITIONS

13.2.1 (1)OBSERVATION DISTANCE: 35cm±5cm

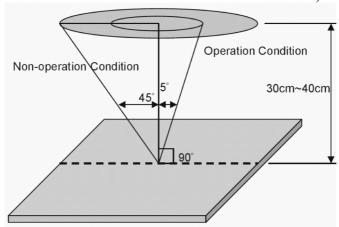
(2) VIEW ANGLE:

NON-OPERATION CONDITION: ±5°

(PERPENDICULAR TO LCD PANEL SURFACE)

OPERATION CONDITION: ±45°

(PERPENDICULAR TO LCD PANEL SURFACE)



13.2.2 ENVIRONMENT CONDITIONS:

AMBIEN	20°C~25°C	
AMBI	65±20%RH	
AMBIENT	COSMETIC INSPECTION	MORE THAN 600Lux
ILLUMINATION	FUNCTIONAL INSPECTION	300~500 Lux

13.2.3 INSPECTION LOT QUANTITY PER DELIVERY LOT FOR EACH MODEL

13.2.4 INSPECTION METHOD

A SAMPLING INSPECTION SHALL BE MADE ACCORDING TO THE FOLLOWING PROVISIONS TO JUDGE THE ACCEPTABILITY

(a)APPLICABLE STANDARD:

MIL-STD-105E

NORMAL INSPECTION, SINGLE SAMPLING

LEVEL II

(b)AQL: MAJOR DEFECT: AQL 0.65 MINOR DEFECT: AQL 1.0

MODEL NO.	VERSION	PAGE
ETQ570G0DH6	4	20

13.3 INSPECTION STANDARDS

13.3.1 VISUAL DEFECTS CLASSIFICATION

TYPE OF DEFECT	INSPECTION ITEM	DEFECT FEATURE	AQL
	1.DISPLAY ON	DEFECT TO MISS SPECIFIED DISPLAY FUNCTION, FOR ALL AND SPECIFIED DOTS EX: DISCONNECTION, SHORT CIRCUIT ETC	0.65
MAJOR DEFECT	2.BACKLIGHT	NO LIGHT FLICKERING AND OTHER ABNORMAL ILLUMINATION	0.65
	3.DIMENSIONS	• SUBJECT TO INDIVIDUAL ACCEPTANCE SPECIFICATIONS	
	1.DISPLAY ZONE	 BLACK/WHITE SPOT BUBBLES ON POLARIZER NEWTON RING BLACK/WHITE LINE SCRATCH CONTAMINATION LEVER COLOR SPREED 	
MINOR DEFECT	2.BEZEL ZONE	STAINSSCRATCHESFOREIGN MATTER	1.0
	3.SOLDERING	 INSUFFICIENT SOLDER SOLDERED IN INCORRECT POSITION CONVEX SOLDERING SPOT SOLDER BALLS SOLDER SCRAPS 	
	4.DISPLAY ON (ALL ON)	• LIGHT LINE	

MODEL NO.	VERSION	PAGE
ETQ570G0DH6	4	21

13.3.2 MODULE DEFECTS CALSSIFICATION

NO.	ITEM		CRI	TERIA	
1.	DISPLAY ON INSPECTION	(1)INCORRECT PATTERN (2)MISSING SEGMENT (3)DIM SEGMENT (4)OPERATING VOLTAGE BEYOND SPEC			
2.	OVERALL DIMENSIONS	(1)OVERALL DIMENSION BEYOND SPEC			
3.	DOT DEFECT	AND BLUE SC (2) I BRIGHT DOT DARK DOT TOAL BRIGHT NOTE: 1. THE DEFINITIO THE SIZE OF A REGARDED AS 2. BRIGHT DOT: DOTS APPEAR PANEL IS DISP. 3. DARK DOT: DOTS APPEAR	TEMS TAND DARK DOTS ON OF DOT: DEFECTIVE DOT ONE DEFECTIVE BRIGHT AND UNCLAYING UNDER E	CHANGED IN SIZE IN	E DOT IS N WHICH LCD
4.	FOREIGN BLACK/WHITE/ BRIGHT LINE/ SCRATCH OF VIEWING AREA		WIDTH: W $W \le 0.05$ $0.05 < W \le 0.1$ 0.1 < W LENGH: L.mm	PERMISSIBLE NO. IGNORE 4 NONE	
5.	FOREIGN MATTER \ BLACK SPOTS \ WHITE SPOTS \ DENT (INCLUDING LIGHT LEAKAGE DUE TO POLARIZING PLATES PINHOLES, ETC.)	<u> </u>		NUMBER OF PIECES IGNORI 4 NONE	

MODEL NO. VERSION PAGE ETQ570G0DH6 4 22

NO.	ITEM		CRITERIA	
			AVERAGE DIAMETER (mm): D	NUMBER OF PIECES PERMITTED
		DUDDI E ON THE	D ≤ 0.25	IGNORE
		BUBBLE ON THE POLARIZER	$0.25 < D \le 0.5$	N ≤ 5
		TOLINGLER	0.5 < D	NOTE
		SURFACE STATUS	D < 0.1 mm	IGNORE
			0.1 < D ≤ 0.3mm	N ≤ 3
		CF FAIL / SPOT	D < 0.1 mm $0.1 < D \le 0.3 \text{mm}$	IGNORE N ≤ 3
6.	BUBBLES OF POLARIZER /DIRT/CF FAIL /SURFACE STAINS	NOTE: (1)POLARIZER BUBBLE IS DEFINED AS THE BUBBLE APPEAR ON ACTIVE DISPLAY AREA. THE DEFECT OF POLARIZER BUBBLE SHALL BE IGNORED IF THE POLARIZER BUBBLE APPEARS ON THE OUTSIDE OF ACTIVE DISPLAY AREA. (2)THE EXTRANEOUS SUBSTANCE IS DEFINED AS IT CAN BE OBSERVED WHEN THE MODULE IS POWER ON. (3)THE DEFINITION OF AVERAGE DIAMETER, D IS DEFINED AS FOLLOWING. AVERAGE DIAMETER (D)=(a+b)/2		
7.	LINE DEFECT ON DISPLAY	OBVIOUS VERTICAL OR HORIZONTAL LINE DEFECT IS NOT ALLOW		
8.	MURA ON DISPLAY	IT'S OK IF MURA IS	SLIGHT VISIBLE THROU	NG 6% ND FILTER
9.	UNEVEN COLOR SPREAD, COLORATION	(1)TO BE DETERMINED BASED UPON THE STANDARD SAMPLE.		
10.	BEZEL APPEARANCE	(1)BEZEL MAY NOT HAVE RUST, BE DEFORMED OR HAVE FINGER PRINTS STAINS OF OTHER CONTAMINATION. (2)BEZEL MUST COMPLY WITH JOB SPECIFICATIONS.		
11	(1)THERE MAY NOT BE MORE THAN 2mm OF SEALANT OUTSIDE THE SEAL AREA ON THE PCB, AND THERE SHOULD BE NO MORE THAN THREE PLACES. (2)NO OXIDATION OR CONTAMINATION PCB TERMINALS. (3)PARTS ON PCB MUST BE THE SAME AS ON THE PRODUCTION CHARACTERISTIC CHART. THERE SHOULD BE NO WRONG PARTS, MISSING PARTS OR EXCESS PARTS. (4)THE JUMPER ON THE PCB SHOULD CONFORM TO THE PRODUCT CHARACTERISTIC CHART. (5)IF SOLDER GETS ON BEZEL TAB PADS, LED PAD, ZEBRA PAD OR SCREW HOLD PAD; MAKE SURE IT IS SMOOTHED DOWN.			

NO.	ITEM	CRITERIA
		(1)NO SOLDERING FOUND ON THE SPECIFIED PLACE (2)INSUFFICENT SOLDER
		(a)LSI, IC A POOR WETTING OF SOLDER IS BETWEEN LOWER BEND OR "HEEL" OF LEAD AND PAD
		SOLDER FILLET
		(b)CHIP COMPONENT • SOLDER IS LESS THAN 50% OF SIDES AND FRONT FACE WETTING
		SOLDER FILLET
12. SOLDE	ERING	SOLDER WETS 3 SIDES OF TERMINAL, BUT LESS THAN 25% OF SIDES AND FRONT SURFACE AREA ARE COVERED
		SOLDER
		(3)PARTS ALIGMENT (a)LSI, IC LEAD WIDTH IS MORE THAN 50% BEYOND PAD OUTLINE

NO.	ITEM	CRITERIA
		(b)CHIP COMPONENT COMPONENT IS OFF CENTER, AND MORE THAN 50% OF THE LEADS IS OFF THE PAD OUTLINE
12. SOLDE	RING	
		 (4)NO UNMELTED SOLDER PASTE MAY BE PRESENT ON THE PCB. (5)NO COLD SOLDER JOINTS, MISSING SOLDER CONNECTIONS, OXIDATION OR ICICLE. (6)NO RESIDUE OR SOLDER BALLS ON PCB. (7)NO SHORT CIRCUITS IN COMPONENTS ON PCB.
13. BACKL	IGHT	(1)NO LIGHT (2)FLICKERING AND OTHER ABNORMAL ILLUMINATION (3)SPOTS OR SCRATCHES THAT APPEAR WHEN LIT MUST BE JUDGED USING LCD SPOT, LINES AND CONTAMINATION STANDARDS. (4)BACKLIGHT DOESN'T LIGHT OR COLOR IS WRONG.
14. GENER APPEA	.AL RANCE	 (1)NO OXIDATION, CONTAMINATION, CURVES OR, BENDS ON INTERFACE PIN (OLB) OF TCP. (2)NO CRACKS ON INTERFACE PIN (OLB) OF TCP. (3)NO CONTAMINATION, SOLDER RESIDUE OR SOLDER BALLS ON PRODUCT. (4)THE IC ON THE TCP MAY NOT BE DAMAGED, CIRCUITS. (5)THE UPPERMOST EDGE OF THE PROTECTIVE STRIP ON THE INTERFACE PIN MUST BE PRESENT OR LOOK AS IF IT CAUSE THE INTERFACE PIN TO SEVER. (6)THE RESIDUAL ROSIN OR TIN OIL OF SOLDERING (COMPONENT OR CHIP COMPONENT) IS NOT BURNED INTO BROWN OR BLACK COLOR. (7)SEALANT ON TOP OF THE ITO CIRCUIT HAS NOT HARDENED. (8)PIN TYPE MUST MATCH TYPE IN SPECIFICATION SHEET. (9)LCD PIN LOOSE OR MISSING PINS. (10)PRODUCT PACKAGING MUST THE SAME AS SPECIFIED ON PACKAGING SPECIFICATION SHEET. (11)PRODUCT DIMENSION AND STRUCTURE MUST CONFORM TO PRODUCT SPECIFICATION SHEET. (12)THE APPEARANCE OF HEAT SEAL SHOULD NOT ADMIT ANY DIRT AND BREAK.

NO.	ITEM	CRITERIA			
		THE LCD WITH EXTENSIVE OF GENERAL GLASS CHIP:	$ \begin{array}{c c} a \\ & \leq t/2 \\ \hline & t/2 > , \leq 2t \\ \hline *W=DISTANCI \end{array} $	b < VIEWING AREA ≤ W/2 E BETWEEN AREA AND LOOGE E LENGTH	c ≤ 1/8X ≤ 1/8X
15. CI	RACKED GLASS	CORNER PART: CHIP ON ELECTRODE PAD a	PANEL ED $X = LCD SIDI$ $t = GLASS TI$ a $\leq t$ $* X=LCD SIDE$	AREA AND LODGE E LENGTH HICKNESS b ≤ 0.5mm WIDTH	c ≤ 1/8X ≤ 1/8X CD
		c a	©IF GLASS CH TERMINAL, REMAIN AN ACCORDING TERMINAL ©IF THE PROD SEALED BY	b ≤ 1/8X WIDTH HICKNESS DE PAD LENGT HIPPING THE I' OVER 2/3 OF ' ND BE, INSPEC G TO ELECTRO SPECIFICATIO DUCT WILL BE THE CUSTOM MENT MARK N	TO THE ITO MU TED DDE DNS THEAT ER,

MODEL NO.	VERSION	PAGE
ETQ570G0DH6	4	26

13.4 RELIABILITY TEST

13.4.1 STANDARD SPECIFICATIONS FOR RELIABILITY OF LCD MODULE

NO	ITEM	DESCRIPTION
1	HIGH TEMPERATURE OPERATION	THE SAMPLE SHOULD BE ALLOWED TO STAND AT +70°C FOR 240 HRS
2	LOW TEMPERATURE OPERATION	THE SAMPLE SHOULD BE ALLOWED TO STAND AT -20°C FOR 240 HRS
3	HIGH TEMPERATURE STORAGE	THE SAMPLE SHOULD BE ALLOWED TO STAND AT +80°C FOR 240 HRS
4	LOW TEMPERATURE STORAGE	THE SAMPLE SHOULD BE ALLOWED TO STAND AT -30°C FOR 240 HRS
5	HIGH TEMP / HUMIDITY TEST STORAGE	THE SAMPLE SHOULD BE ALLOWED TO STAND AT 60°C , 90% RH 240 HRS
6	THERMAL SHOCK (NOT OPERATED)	THE SAMPLE SHOULD BE ALLOWED TO STAND THE FOLLOWING 10 CYCLES OF OPERATION: -40°C FOR 30 MINUTES ~ +80°C FOR 30 MINUTES
7	(EEEE TROSTATIC	AIR DISCHARGE ± 12KV CONTACT DISCHARGE ± 8KV

NOTE (1): THE TEST SAMPLES HAVE RECOVERY TIME FOR 2 HOURS AT ROOM TEMPERATURE BEFORE THE FUNCTION CHECK. IN THE STANDARD CONDITIONS, THERE IS NO DISPLAY FUNCTION NG ISSUE OCCURRED.

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MODEL NO.	VERSION	PAGE
ETQ570G0DH6	4	27

13.5 TESTING CONDITIONS AND INSPECTION CRITERIA

FOR THE FINAL TEST THE TESTING SAMPLE MUST BE STORED AT ROOM TEMPERATURE FOR 24 HOURS, STANDARD SPECIFICATIONS FOR RELIABILITY HAVE BEEN EXECUTED IN ORDER TO ENSURE STABILITY.

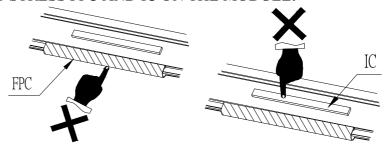
NO	ITEM	TEST MODEL	INSPECTION CRITERIA
1	CURRENT CONSUMPTION	REFER TO SPECIFICATION	THE CURRENT CONSUMPTION SHOULD CONFORM TO THE PRODUCT SPECIFICATION.
2	CONTRAST	REFER TO SPECIFICATION	AFTER THE TESTS HAVE BEEN EXECUTED, THE CONTRAST MUST BE LARGER THAN HALF OF ITS INITIAL VALUE PRIOR TO THE TESTS.
3	APPEARANCE	VISUAL INSPECTION	DEFECT FREE

13.6 OPERATION

- 13.6.1 DO NOT CONNECT OR DISCONNECT MODULES TO OR FROM THE MAIN SYSTEM WHILE POWER IS BEING SUPPLIED .
- 13.6.2 USE THE MODULE WITHIN SPECIFIED TEMPERATURE; LOWER TEMPERATURE CAUSES THE RETARDATION OF BLINKING SPEED OF THE DISPLAY; HIGHER TEMPERATURE MAKES OVERALL DISPLAY DISCOLOR. WHEN THE TEMPERATURE RETURNS TO NORMALITY, THE DISPLAY WILL OPERATE NORMALLY.
- 13.6.3 ADJUST THE LC DRIVING VOLTAGE TO OBTAIN THE OPTIMUM CONTRAST .
- 13.6.4 POWER ON SEQUENCE INPUT SIGNALS SHOULD NOT BE SUPPLIED TO LCD MODULE BEFORE POWER SUPPLY VOLTAGE IS APPLIED AND REACHES THE SPECIFIED VALUE.

 IF ABOVE SEQUENCE IS NOT FOLLOWED, CMOS LSIS OF LCD MODULES MAY BE DAMAGED DUE TO LATCH UP PROBLEM.
- 13.6.5 NOT ALLOWED TO INFLICT ANY EXTERNAL STRESS AND TO CAUSE ANY MECHANICAL INTERFERENCE ON THE BENDING AREA OF FPC DURING THE TAIL BENDING BACKWARDS!

 DO NOT STRESS FPC AND IC ON THE MODULE!



 MODEL NO.
 VERSION
 PAGE

 ETQ570G0DH6
 4
 28

13.7 NOTICE

- 13.7.1 USE A GROUNDED SOLDERING IRON WHEN SOLDERING CONNECTOR I/O TERMINALS . FOR SOLDERING OR REPAIRING, TAKE PRECAUTION AGAINST THE TEMPERATURE OF THE SOLDERING IRON AND THE SOLDERING TIME TO PREVENT PEELING OFF THE THROUGH-HOLE-PAD .
- 13.7.2 DO NOT DISASSEMBLE . EDT SHALL NOT BE HELD RESPONSIBLE IF THE MODULE IS DISASSEMBLED AND UPON THE REASSEMBLY THE MODULE FAILED .
- 13.7.3 DO NOT CHARGE STATIC ELECTRICITY, AS THE CIRCUIT OF THIS MODULE CONTAINS CMOS LSIS. A WORKMAN'S BODY SHOULD ALWAYS BE STATIC-PROTECTED BY USE OF AN ESD STRAP. WORKING CLOTHES FOR SUCH PERSONNEL SHOULD BE OF STATIC-PROTECTED MATERIAL.
- 13.7.4 ALWAYS GROUND THE ELECTRICALLY-POWERED DRIVER BEFORE USING IT TO INSTALL THE LCD MODULE. WHILE CLEANING THE WORK STATION BY VACUUM CLEANER, DO NOT BRING THE SUCKING MOUTH NEAR THE MODULE; STATIC ELECTRICITY OF THE ELECTRICALLY-POWERED DRIVER OR THE VACUUM CLEANER MAY DESTROY THE MODULE.
- 13.7.5 DON'T GIVE EXTERNAL SHOCK.
- 13.7.6 DON'T APPLY EXCESSIVE FORCE ON THE SURFACE.
- 13.7.7 LIQUID IN LCD IS HAZARDOUS SUBSTANCE. MUST NOT LICK AND SWALLOW.
 WHEN THE LIQUID IS ATTACH TO YOUR, SKIN, CLOTH ETC. WASH IT OUT THOROUGHLY AND IMMEDIATELY.
- 13.7.8 DON'T OPERATE IT ABOVE THE ABSOLUTE MAXIMUM RATING.
- 13.7.9 STORAGE IN A CLEAN ENVIRONMENT, FREE FROM DUST, ACTIVE GAS, AND SOLVENT.
- 13.7.10 STORE WITHOUT ANY PHYSICAL LOAD.
- 13.7.11 REWIRING: NO MORE THAN 3 TIMES.