

1uA Ultra-Low Quiescent Current, 0.6A Output Synchronous BUCK

DESCRIPTION

The ETA3425 is a high-efficiency, DC-to-DC step-down switching regulator, capable of delivering up to 0.6A of output current. It has an ultra-low quiescent current 1uA when there is no load. Running at a fixed frequency of 1.2MHz allows the use of small inductance value and low DCR inductors, thereby achieving higher efficiencies. Other external components, such as ceramic input and output caps, can also be small due to higher switching frequency, while maintaining exceptional low noise output voltages. Internal soft-start control circuitry reduces inrush current. Short-circuit and thermal-overload protection improves design reliability.

ETA3425 is housed in a tiny SOT23-5 and DFN2x2-6 package

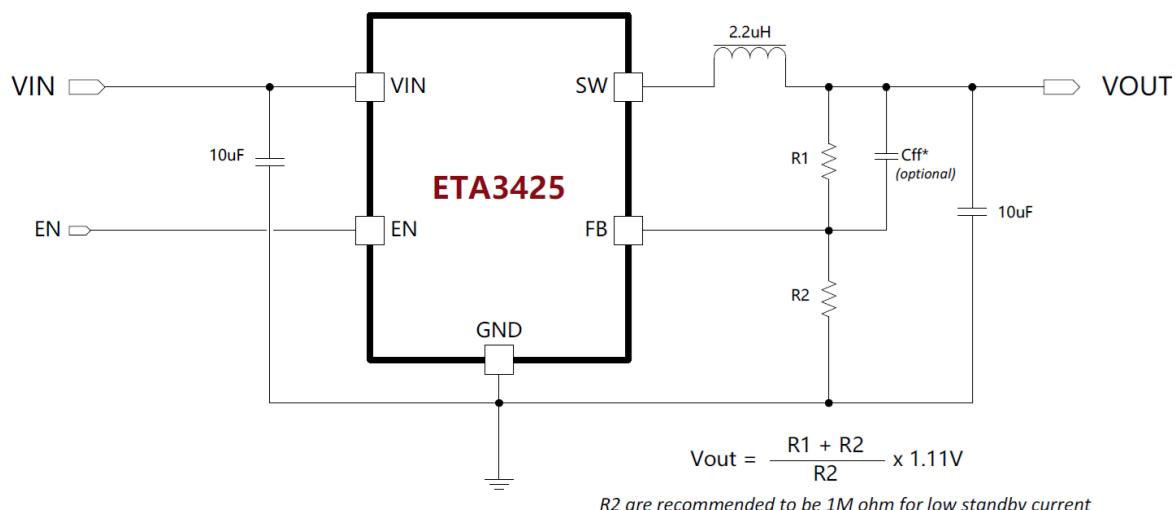
FEATURES

- ◆ Ultra low 1uA I_q, and 1-2uA at standby
- ◆ Up to 96% Efficiency
- ◆ Up to 600mA Max Output Current
- ◆ 1.2MHz Frequency
- ◆ Light Load operation
- ◆ Internal Compensation
- ◆ Tiny Package

APPLICATIONS

- ◆ Wearable
- ◆ IoT
- ◆ Energy Harvest
- ◆ Battery powered devices

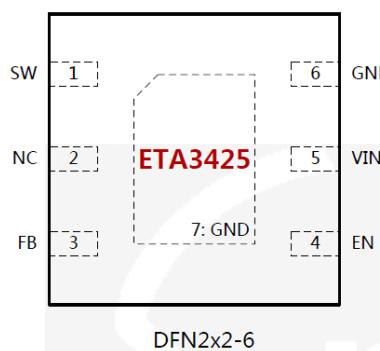
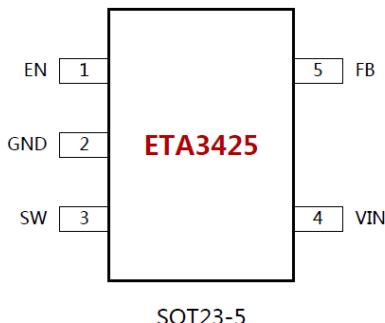
TYPICAL APPLICATION



ORDERING INFORMATION

PART No.	PACKAGE	TOP MARK	Pcs/Reel
ETA3425S2F	SOT23-5	CFYW	3000
ETA3425D2G	DFN2x2-6	CFYW	3000

PIN CONFIGURATION



ABSOLUTE MAXIMUM RATINGS

(Note: Exceeding these limits may damage the device. Exposure to absolute maximum rating conditions for long periods may affect device reliability.)

IN, SW, OUT, EN Voltage	-0.3V to 7.5V
SW to ground current	Internally limited
Operating Temperature Range	-40°C to 85°C
Storage Temperature Range	-55°C to 150°C
Thermal Resistance θ_{JC}	θ_{JA}	
SOT23-5.....	190.....	90 °C /W
DFN2x2-6.....	165.....	45 °C /W
Lead Temperature (Soldering, 10ssec)	260°C
ESD HBM (Human Body Mode)	2kV
ESD MM (Machine Mode)	200V

ELECTRICAL CHARACTERISTICS

($V_{IN} = 5V$, unless otherwise specified. Typical values are at $T_A = 25^\circ C$.)

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
Input Voltage Range		2.6		7	V
Input UVLO	Rising, Hysteresis=170mV		2.25		V
Input Supply Current	No Load, RFB_G =10Mohm		1.2	2	μA
Input Shutdown Current			0.1		μA
FB Voltage		1.094	1.11	1.126	V
Load Regulation			0.4		%/A
Line Regulation	$V_{IN} = 2.7$ to $5.5V$		0.14		%/V
Switching Frequency		0.9	1.2	1.5	MHz
NMOS Switch On Resistance	$ISW = 200mA$		200		$m\Omega$
PMOS Switch On Resistance	$ISW = 200mA$		300		$m\Omega$
PMOS Switch Current Limit			1		A
SW Leakage Current	$V_{OUT}=7V, V_{SW}=0$ or $7V, EN=GND$			1	μA
EN Input Current	$EN=GND$			1	μA
EN Input Low Voltage				0.4	V
EN Input High Voltage			1.0		V

PIN DESCRIPTION

SOT23-5 PIN #	DFN2x2-6 PIN #	NAME	DESCRIPTION
1	4	EN	Enable Pin. Pull high to enable, pull low to disable.
2	6, 7	GND	Ground
3	1	SW	Inductor Connection. Connect a 2.2uH inductor Between SW and the regulator output.
4	5	VIN	Supply Voltage. Short to PIN. Bypass with a 10µF ceramic capacitor to GND
5	3	FB	FB Voltage Pin. Connect an external resistor divider from the output to FB and GND to set the output to a voltage between 1.11V and VIN
	2	NC	Not Connected

TYPICAL CHARACTERISTICS

(Typical values are at $T_A = 25^\circ\text{C}$ unless otherwise specified.)

