General purpose transistors (dual transistors)

EMX26

● Features

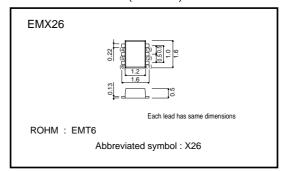
- 1) Two 2SD2654 chips in a EMT package.
- 2) Mounting possible with EMT3 automatic mounting
- 3) Transistor elements are independent, eliminating interference.
- 4) Mounting cost and area can be cut in half.

●Structure

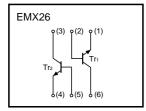
Epitaxial planar type NPN silicon transistor

The following characteristics apply to both Tr1 and Tr2.

●External dimensions (Unit: mm)



●Equivalent circuit



•Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit				
Collector-base voltage	Vсво 60		V				
Collector-emitter voltage	VCEO	50	V				
Emitter-base voltage	V _{EBO}	12	V				
Collector current	Ic	0.15	A (DC)				
Collector current	10	0.2	A (Pulse) *1				
Power dissipation	Pd	150 (TOTAL)	mW *2				
Junction temperature	Tj	150	°C				
Storage temperature	Tstg	-55 to +150	°C				

^{*1} Single pulse Pw=100ms. *2 120mW per element must not be exceeded.

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base breakdown voltage	ВУсво	60	-	-	V	Ic=10μA
Collector-emitter breakdown voltage	BVceo	50	-	_	V	Ic=1mA
Emitter-base breakdown voltage	ВVево	12	-	-	V	Iε=10μA
Collector cutoff current	Ісво	_	-	0.3	μΑ	Vcb=50V
Emitter cutoff current	ІЕВО	_	-	0.3	μΑ	V _{EB} =12V
Collector-emitter saturation voltage	VcE(sat)	_	-	0.3	V	Ic/I _B =50mA/5mA *
DC current transfer ratio	hfe	820	_	2700	_	Vce/lc=5V/1mA *
Transition frequency	f⊤	_	250	_	MHz	Vce=5V, Ie=-10mA, f=100MHz *
Output capacitance	Cob	-	3.5	*	pF	Vcb=5V, Ie=0A, f=1MHz

^{*} Measured using pulse current.

Packaging specifications

	Package	Taping
	Code	T2R
Туре	Basic ordering unit (pieces)	8000
EMX26		0

•Electrical characteristic curves

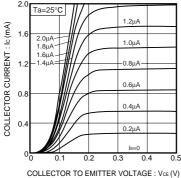


Fig.1 Grounded emitter output characteristics (I)

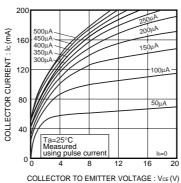


Fig.2 Grounded emitter output characteristics (II)

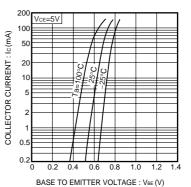


Fig.3 Grounded emitter propagation characteristics

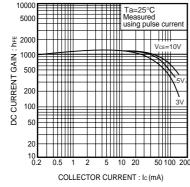


Fig.4 DC current gain vs. collector current (I)

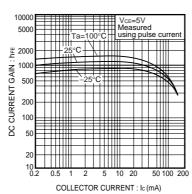


Fig.5 DC current gain vs. collector current (II)

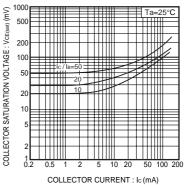
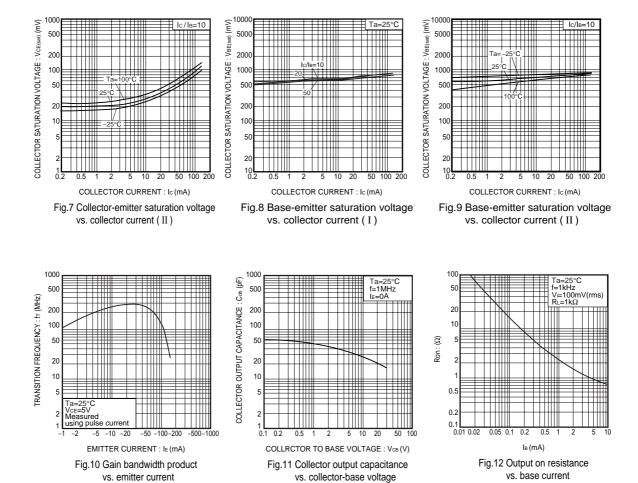


Fig.6 Collector-emitter saturation voltage vs. collector current (I)



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