

EMIF08-1502M16

IPADTM

8 line low capacitance EMI filter and ESD protection in Micro QFN package

Main product characteristics

Where EMI filtering in ESD sensitive equipment is required:

- LCD and CAMERA for Mobile phones
- Computers and printers
- Communication systems
- MCU Boards

Description

The EMIF08-1502M16 is an 8 line highly integrated device designed to suppress EMI/RFI noise in all systems exposed to electromagnetic interference.

This filter includes an ESD protection circuitry, which prevents damage to the application when subjected to ESD surges up to 15 kV on the input or output pins.

Benefits

- EMI (I/O) low-pass filter
- High efficiency in EMI filtering:
 - Greater than 30 dB attenuation at frequencies from 900 MHz to 1.8 GHz
- Cut_off frequency: 300 MHz
- Very low PCB space consuming: 3.3 mm x 1.5 mm
- Very thin package: 0.6 mm max.
- High efficiency in ESD suppression on inputs pins (IEC 61000-4-2 level 4).
- High reliability offered by monolithic integration
- High reduction of parasitic elements through integration
- Lead free package

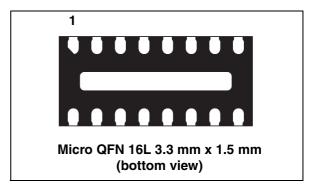
Complies with following standards:

IEC 61000-4-2

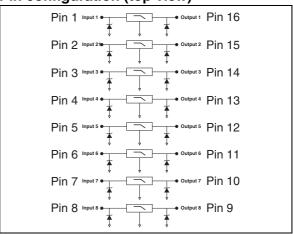
level 4 input pins 15 kV (air discharge)

8 kV (contact discharge)

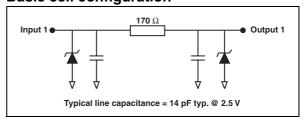
MIL STD 883E - Method 3015-6 Class 3 (all pins)



Pin configuration (top view)



Basic cell configuration



Order code

Part number	Marking		
EMIF08-1502M16	G8		

TM: IPAD is a trademark of STMicroelectronics

October 2006 Rev 1 1/6

1 Characteristics

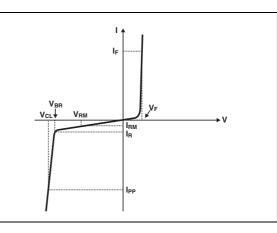
Characteristics

Table 1. Absolute ratings (limiting values)

Symbol	Parameter	Value	Unit
V _{PP}	ESD discharge IEC 61000-4-2 contact discharge on input and output pins	15	kV
Tj	Maximum junction temperature	125	°C
T _{op}	Operating temperature range	- 40 to + 85	°C
T _{stg}	Storage temperature range	- 55 to + 150	°C

Table 2. Electrical characteristics $(T_{amb} = 25^{\circ} C)$

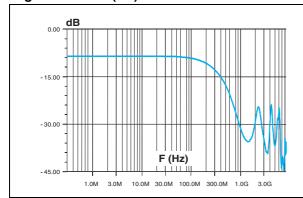
Symbol	Parameter		
V _{BR}	Breakdown voltage		
I _{RM} Leakage current @ V _{RM}			
V_{RM}	Stand-off voltage		
V_{CL}	Clamping voltage		
R_d	Dynamic resistance		
I _{PP}	Peak pulse current		
R _{I/O}	Series resistance between Input & Output		
C _{line} Input capacitance per line			

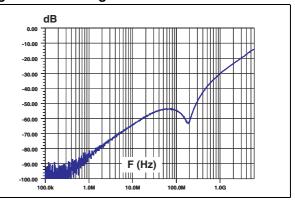


EMIF08-1502M16

Symbol	Test conditions		Тур.	Max.	Unit
V _{BR}	I _R = 1 mA	6	8	10	V
V _F	I = 10 mA	0.5	1.0	1.5	V
I _{RM}	V _{RM} = 3 V per line			100	nA
R _{I/O}	Tolerance ± 10%	153	170	187	Ω
C _{line}	V_R = 2.5 V DC, V_{OSC} = 30 mV, F = 1 MHz	12	14	16.5	pF

Figure 1. S21(dB) attenuation measurement Figure 2. Analog cross talk measurements





577

Figure 3. ESD response to IEC 61000-4-2 (+15 kV air discharge) on one input (V_{in}) and on one output (V_{out})

Figure 4. ESD response to IEC 61000-4-2 (- 15 kV air discharge) on one input (V_{in}) and on one output (V_{out})

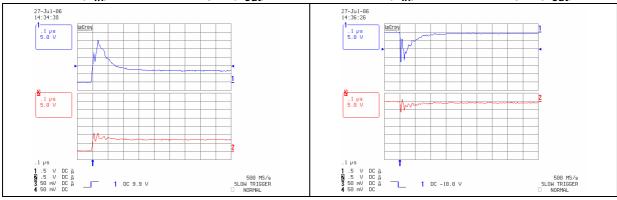
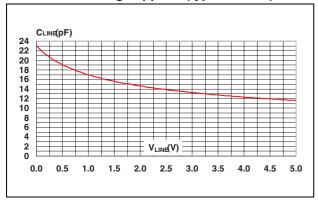
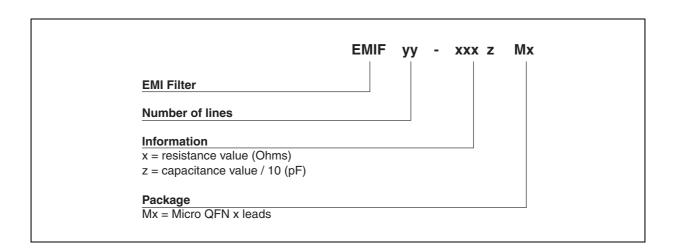


Figure 5. Line capacitance versus reverse voltage applied (typical value)



2 Ordering information scheme



3 Package information

Table 3. Micro QFN 3.3x1.5 16L dimensions

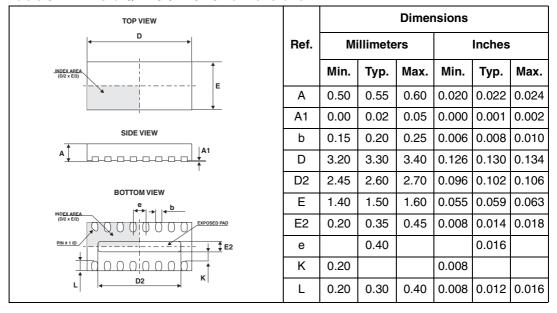
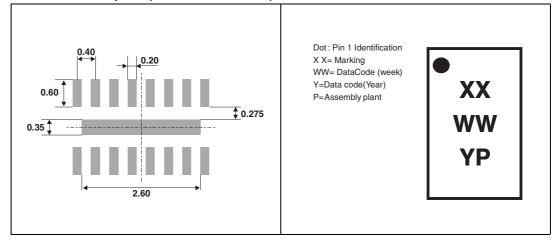


Figure 6. Micro QFN 3.3x1.5 16L footprint (dimensions in mm)

Figure 7. Marking



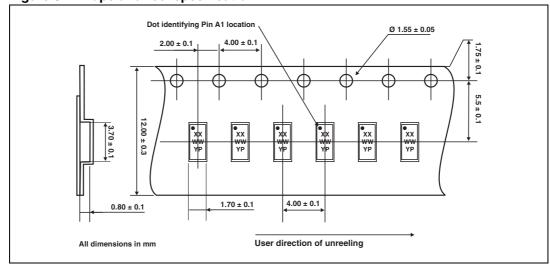


Figure 8. Tape and reel specification

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com.

4 Ordering information

Part number	Marking	Package	Weight	Base qty	Delivery mode
EMIF08-1502M16	G8	Micro QFN	7.2 mg	3000	Tape and reel (7")

5 Revision history

Date	Revision	Changes
24-Oct-2006	1	Initial release.

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