

# Power management (dual transistors)

## EMF8

2SC5585 and DTC144EE are housed independently in a EMT6 package.

### ●Application

Power management circuit

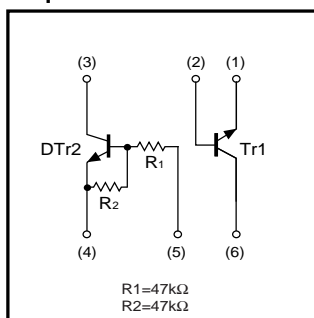
### ●Features

- 1) Power switching circuit in a single package.
- 2) Mounting cost and area can be cut in half.

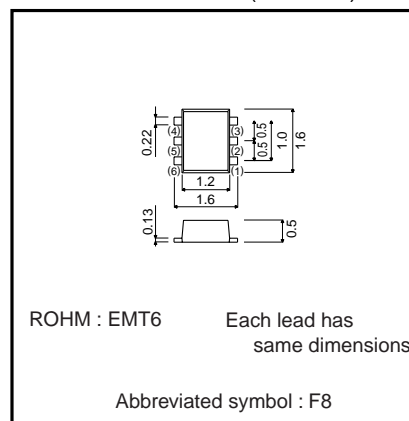
### ●Structure

Silicon epitaxial planar transistor

### ●Equivalent circuits



### ●External dimensions (Unit : mm)



### ●Package, marking, and packaging specifications

Type	EMF8
Package	EMT6
Marking	F8
Code	T2R
Basic ordering unit (pieces)	8000

## Transistors

## ●Absolute maximum ratings (Ta=25°C)

Tr1

Parameter	Symbol	Limits	Unit
Collector-base voltage	V <sub>CB0</sub>	15	V
Collector-emitter voltage	V <sub>CEO</sub>	12	V
Emitter-base voltage	V <sub>EBO</sub>	6	V
Collector current	I <sub>C</sub>	500	mA
	I <sub>CP</sub>	1.0	A *1
Power dissipation	P <sub>C</sub>	150(TOTAL)	mW *2
Junction temperature	T <sub>J</sub>	150	°C
Range of storage temperature	T <sub>stg</sub>	–55 to +150	°C

\*1 Single pulse Pw=1ms

\*2 120mW per element must not be exceeded.

Each terminal mounted on a recommended land.

DTr2

Parameter	Symbol	Limits	Unit
Supply voltage	V <sub>CC</sub>	50	V
Input voltage	V <sub>IN</sub>	–10 to +40	V
Collector current	I <sub>C</sub>	100	mA *1
Output current	I <sub>O</sub>	30	mA
Power dissipation	P <sub>C</sub>	150(TOTAL)	mW *2
Junction temperature	T <sub>J</sub>	150	°C
Range of storage temperature	T <sub>stg</sub>	–55 to +150	°C

\*1 Characteristics of built-in transistor.

\*2 120mW per element must not be exceeded.

Each terminal mounted on a recommended land.

## ●Electrical characteristics (Ta=25°C)

Tr1

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Collector-emitter breakdown voltage	BV <sub>CEO</sub>	12	–	–	V	I <sub>C</sub> =1mA
Collector-base breakdown voltage	BV <sub>CB0</sub>	15	–	–	V	I <sub>C</sub> =10μA
Emitter-base breakdown voltage	BV <sub>EBO</sub>	6	–	–	V	I <sub>E</sub> =10μA
Collector cut-off current	I <sub>CBO</sub>	–	–	100	nA	V <sub>CB</sub> =15V
Emitter cut-off current	I <sub>EBO</sub>	–	–	100	nA	V <sub>EB</sub> =6V
Collector-emitter saturation voltage	V <sub>CE(sat)</sub>	–	100	250	mV	I <sub>C</sub> =200mA, I <sub>B</sub> =10mA
DC current gain	h <sub>FE</sub>	270	–	680	–	V <sub>CE</sub> =2V, I <sub>C</sub> =10mA
Transition frequency	f <sub>T</sub>	–	320	–	MHz	V <sub>CE</sub> =2V, I <sub>E</sub> =–10mA, f=100MHz
Collector output capacitance	C <sub>ob</sub>	–	7.5	–	pF	V <sub>CB</sub> =10V, I <sub>E</sub> =0mA, f=1MHz

DTr2

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Input voltage	V <sub>I(off)</sub>	–	–	0.5	V	V <sub>CC</sub> =5V, I <sub>O</sub> =100μA
	V <sub>I(on)</sub>	3.0	–	–	V	V <sub>O</sub> =0.3V, I <sub>O</sub> =2mA
Output voltage	V <sub>O(on)</sub>	–	100	300	mV	V <sub>O</sub> =10mA, I <sub>I</sub> =0.5mA
Input current	I <sub>I</sub>	–	–	180	μA	V <sub>I</sub> =5V
Output current	I <sub>O(off)</sub>	–	–	500	nA	V <sub>CC</sub> =50V, V <sub>I</sub> =0V
DC current gain	G <sub>I</sub>	68	–	–	–	V <sub>O</sub> =5V, I <sub>O</sub> =5mA
Transition frequency	f <sub>T</sub>	–	250	–	MHz	V <sub>CE</sub> =10V, I <sub>E</sub> =–5mA, f=100MHz *
Input resistance	R <sub>1</sub>	32.9	47	61.1	kΩ	–
Resistance ratio	R <sub>2</sub> /R <sub>1</sub>	0.8	1.0	1.2	–	–

\*Characteristics of built-in transistor.

## Transistors

## ●Electrical characteristic curves

Tr1

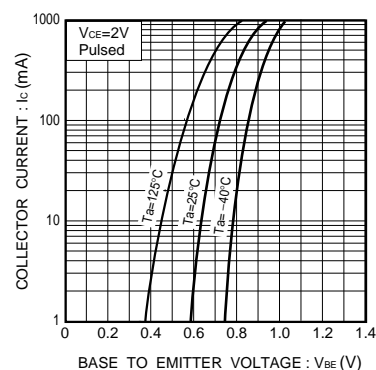


Fig.1 Grounded emitter propagation characteristics

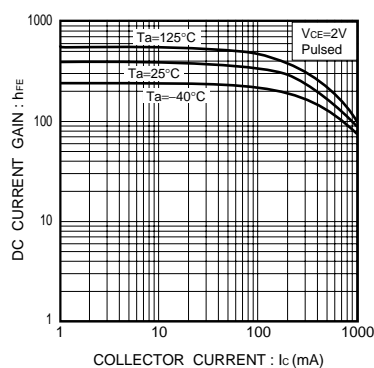


Fig.2 DC current gain vs. collector current

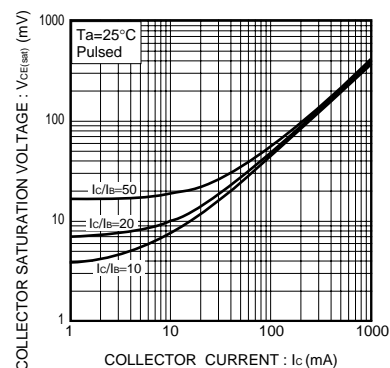


Fig.3 Collector-emitter saturation voltage vs. collector current ( I )

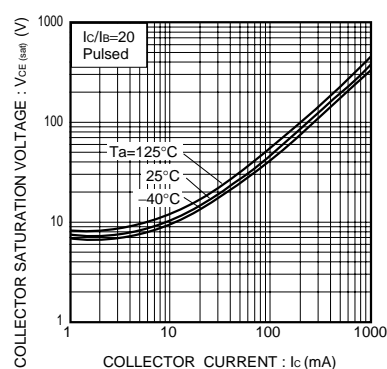


Fig.4 Collector-emitter saturation voltage vs. collector current ( II )

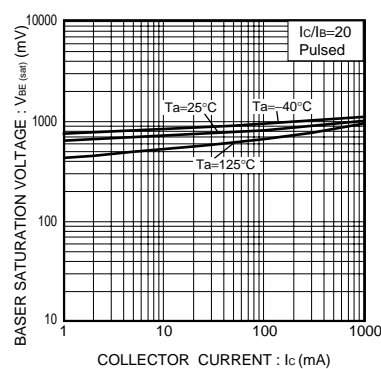


Fig.5 Base-emitter saturation voltage vs. collector current

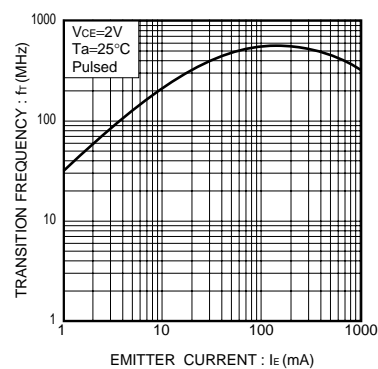
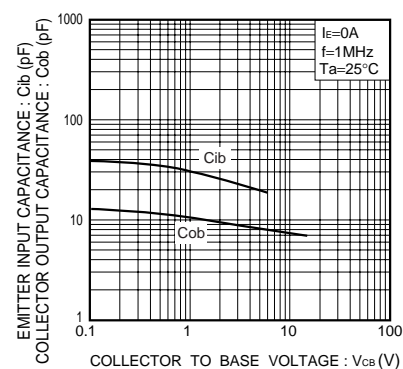


Fig.6 Gain bandwidth product vs. emitter current

Fig.7 Collector output capacitance vs. collector-base voltage  
Emitter input capacitance vs. emitter-base voltage

## Transistors

## DTr2

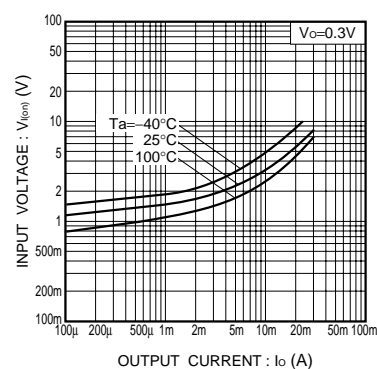


Fig.9 Input voltage vs. output current (ON characteristics)

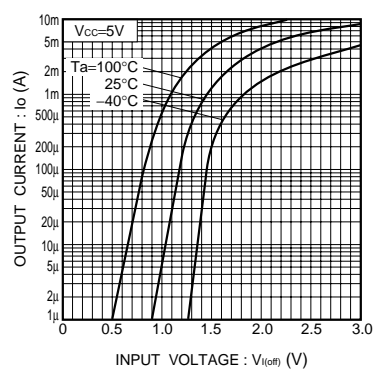


Fig.10 Output current vs. input voltage (OFF characteristics)

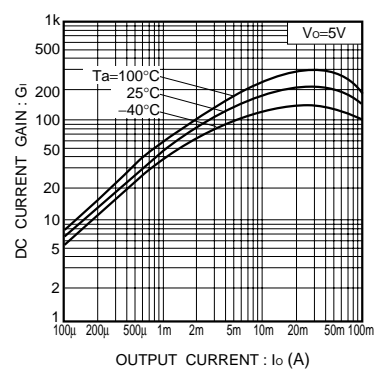


Fig.11 DC current gain vs. output current

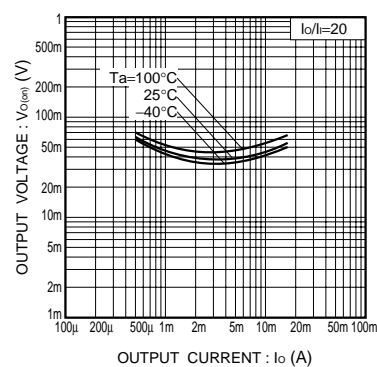


Fig.12 Output voltage vs. output current

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