EL2232D Die

Dual 60 MHz Current Feedback Amplifiers

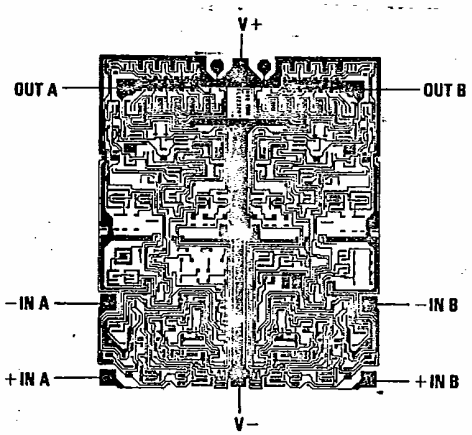
T-79-07-20

Absolute Maximum Ratings (T_A = 25°C)

V_S	Supply Voltage	±18V or +36V
V_{IN}	Input Voltage	±15V or V_S
ΔV_{IN}	Differential Input Voltage	±6V
I_{IN}	Input Current (Pins 2 or 3)	±10 mA
I_{OP}	Peak Output Current	Short Circuit Protected
	Output Short Circuit Duration (Note 1)	Continuous
T_A	Operating Temperature Range	−55°C to 125°C
T_J	Maximum Junction Temperature	175°C

Important Note:
For AC electrical characteristics, refer to the typical electrical table and performance curves in the package data sheet. These characteristics are guaranteed but not tested in die form. Unless otherwise noted, all tests are pulsed tests, therefore $T_J = T_C = T_A$.

Test Level	Test Procedure
I	100% production tested in wafer form. See remarks under Electrical Testing in the General Die section.



DIE SIZE: 86 x 100 MILS

Open Loop Characteristics V_S = ±15V, T_A = 25°C, R_L = 500Ω, unless otherwise specified

Parameter	Description	Conditions	Min	Typ	Max	Test Level	Units
V_{OS}	Input Offset Voltage	V_S = ±5V, ±15V		2	7	I	mV
$+I_N$	+Input Current	V_S = ±5V, ±15V		1.2	3	I	µA
$-I_N$	−Input Current	V_S = ±5V, ±15V		5	20	I	µA
$+R_{IN}$	+Input Resistance		2	20		I	MΩ
CMRR (Note 2)	Common Mode Rejection Ratio	V_S = ±5V, ±15V	56	63		I	dB
−ICMR (Note 2)	−Input Common Mode Rejection			0.25	0.75	I	µA/V
PSRR (Note 3)	Power Supply Rejection Ratio		66	80		I	dB
+IPSR (Note 3)	+Input Current Power Supply Rejection			0.03	0.06	I	µA/V
−IPSR (Note 2)	−Input Current Power Supply Rejection			0.06	0.2	I	µA/V
R_{OL} (Note 4)	Transimpedance ($\Delta V_{OUT}/\Delta(-I_{IN})$)	V_S = ±5V, ±15V	1.2	4		I	MΩ
V_O	Output Voltage Swing	V_S = ±15V	11.5	12.5		I	V
		V_S = ±5V	2	2.5		I	V
I_{OUT}	Output Current	V_S = 15V	23	30		I	mA
I_S	Quiescent Supply Current			9.5	13	I	mA

Note 1: Junction temperature must be below absolute maximum rating when an output is shorted.
Note 2: V_{CM} = ±10V for V_S = ±15V. For V_S = ±5V, V_{CM} = ±2V.
Note 3: V_{OS} is measured at V_S = ±4.5V and at V_S = ±18V. Both supplies are changed simultaneously.
Note 4: V_{OUT} = ±10V for V_S = ±15V, V_{OUT} = ±2V for V_S = ±5V, R_L = 500Ω.