EL2041D Die

T-79-07-10

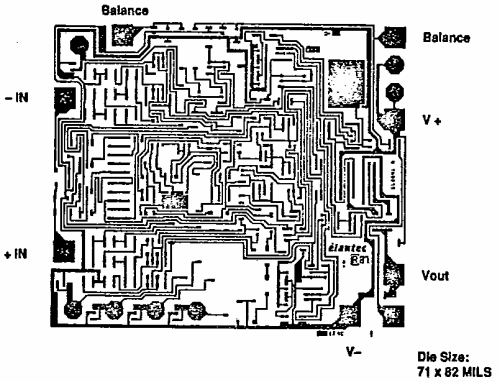
Wideband, Fast Settling, Unity Gain Stable, Operational Amplifiers

Absolute Maximum Ratings (T_A = 25°C)

V_S	Voltage between V+ and V−	35V
ΔV_{IN}	Differential Input Voltage	6V
I_{OP}	Output Current, Peak	50 mA
I_{OC}	Output Current, Continuous	25 mA
T_J	Maximum Junction Temperature	175°C

Important Note:
For AC electrical characteristics, refer to the typical electrical table and performance curves in the package data sheet. These characteristics are guaranteed but not tested in die form. Unless otherwise noted, all tests are pulsed tests, therefore $T_J = T_C = T_A$.

Test Level	Test Procedure
I	100% production tested in wafer form. See remarks under Electrical Testing in the General Die section.



DC Electrical Characteristics V_S = ±15V, R_L = 1 kΩ, T_A = 25°C

Parameter	Description	Min	Typ	Max	Test Level	Units
V_{OS}	Offset Voltage		0.5	2	I	mV
I_B	Bias Current		5	15	I	µA
I_{OS}	Offset Current		1	4	I	µA
V_{CM}	Common Mode Range	±8			I	V
A_{VOL}	Large Signal Voltage Gain (Note 1)	7k	10k		I	V/V
CMRR	Common-Mode Rejection Ratio (Note 2)	70	80		I	dB
V_O	Output Voltage Swing	±11			I	V
I_O	Output Current (Note 4)	±25	±50		I	mA
I_S	Supply Current		13	17	I	mA
PSRR	Power Supply Rejection Ratio (Note 3)	70	80		I	dB

Note 1: V_O = ±10V.
Note 2: Two tests are performed. V_{CM} = 0V to +8V and V_{CM} = 0V to −8V.
Note 3: Two tests are perforfmed. V+ = +15V, and V− is changed from −7V to −15V. V− = −15V, and V+ is changed from +7V to +15V.
Note 4: R_L = 200Ω.