

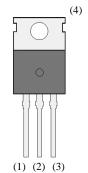
Data Sheet

Features

- $R_{DS(ON)}$ ------6.9 m Ω max. ($V_{GS} = 10 \text{ V}$, $I_D = 44.0 \text{ A}$)
- Q_g -----42.9 nC (V_{GS} = 4.5 V, V_{DS} = 38 V, I_D = 44.0 A)
- Low Total Gate Charge
- High Speed Switching
- Low On-Resistance
- Capable of 4.5 V Gate Drive
- 100 % UIL Tested
- RoHS Compliant

Package

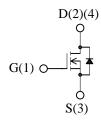
• TO220-3L



Not to scale

Applications

- DC-DC converters
- Synchronous Rectification
- Power Supplies



Absolute Maximum Ratings

• Unless otherwise specified, $T_A = 25$ °C

| Parameter | Symbol | Test conditions | Rating | Unit |
|--|------------------|--|-------------|------|
| Drain to Source Voltage | V_{DS} | | 75 | V |
| Gate to Source Voltage | V_{GS} | | ± 20 | V |
| Continuous Drain Current | I_D | T _C = 25 °C | 85 | A |
| Pulsed Drain Current | I_{DM} | PW ≤ 100μs Duty cycle ≤ 1 % | 170 | A |
| Continuous Source Current (Body Diode) | I_S | | 85 | A |
| Pulsed Source Current (Body Diode) | I_{SM} | $\begin{array}{c} PW \leq 100 \mu s \\ Duty \ cycle \leq 1 \ \% \end{array}$ | 170 | A |
| Single Pulse Avalanche Energy | E _{AS} | $\begin{aligned} &V_{DD} = 38 \text{ V, L} = 1 \text{ mH,} \\ &I_{AS} = 13 \text{ A, unclamped,} \\ &R_G = 4.7 \Omega \\ &Refer \text{ to Figure 1} \end{aligned}$ | 170 | mJ |
| Avalanche Current | I_{AS} | | 30 | A |
| Power Dissipation | P_{D} | T _C = 25 °C | 135 | W |
| Operating Junction Temperature | T_{J} | | 150 | °C |
| Storage Temperature Range | T_{STG} | | - 55 to 150 | °C |

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Thermal Characteristics

• Unless otherwise specified, $T_A = 25$ °C

| Parameter | Symbol | Test Conditions | Min. | Тур. | Max. | Unit |
|---|----------------|-----------------|------|------|------|------|
| Thermal Resistance (Junction to Case) | $R_{	heta JC}$ | | 1 | _ | 0.9 | °C/W |
| Thermal Resistance (Junction to Ambient) | $R_{	heta JA}$ | | - | _ | 62.5 | °C/W |

Electrical Characteristics

• Unless otherwise specified, $T_A = 25$ °C

| Parameter | Symbol | Test Conditions | Min. | Тур. | Max. | Unit |
|--|----------------------|--|------|------|-------|------|
| Drain to Source Breakdown Voltage | V _{(BR)DSS} | $I_D = 100 \ \mu A, \ V_{GS} = 0 \ V$ | 75 | _ | _ | V |
| Drain to Source Leakage Current | I_{DSS} | $V_{DS} = 75 \text{ V}, V_{GS} = 0 \text{ V}$ | _ | _ | 100 | μA |
| Gate to Source Leakage Current | I_{GSS} | $V_{GS} = \pm 20 \text{ V}$ | _ | _ | ± 100 | nA |
| Gate Threshold Voltage | V _{GS(th)} | $V_{DS} = V_{GS}, I_D = 1.5 \text{ mA}$ | 1.0 | 2.0 | 2.5 | V |
| Static Drain to Source On-Resistance | | $I_D = 44.0 \text{ A}, V_{GS} = 10 \text{ V}$ | _ | 5.3 | 6.9 | mΩ |
| | $R_{DS(ON)}$ | $I_D = 22.0 \text{ A}, V_{GS} = 4.5 \text{ V}$ | _ | 6.0 | 7.6 | mΩ |
| Gate Resistance | R_G | f = 1 MHz | _ | 0.8 | - | Ω |
| Input Capacitance | C _{iss} | $V_{DS} = 25 \text{ V}$ $V_{GS} = 0 \text{ V}$ $f = 1 \text{ MHz}$ | _ | 6340 | _ | pF |
| Output Capacitance | Coss | | _ | 575 | _ | |
| Reverse Transfer Capacitance | C_{rss} | | _ | 365 | _ | |
| Total Gate Charge (V _{GS} = 10 V) | Q_{g1} | $V_{DS} = 38 \text{ V}$ $I_D = 44.0 \text{ A}$ | _ | 91.6 | _ | nC |
| Total Gate Charge (V _{GS} = 4.5 V) | Q_{g2} | | _ | 42.9 | _ | |
| Gate to Source Charge | Q_{gs} | | _ | 16.5 | _ | |
| Gate to Drain Charge | Q_{gd} | | _ | 12.4 | _ | |
| Turn-On Delay Time | t _{d(on)} | $V_{DD} = 38 \text{ V}$ $I_D = 44.0 \text{ A}$ $V_{GS} = 10 \text{ V}, R_G = 4.7 \Omega$ Refer to Figure 2 | _ | 10.7 | _ | ns |
| Rise Time | t _r | | _ | 10.1 | _ | |
| Turn-Off Delay Time | $t_{d(off)}$ | | | 49.1 | _ | |
| Fall Time | $t_{ m f}$ | | _ | 21.0 | _ | |
| Source to Drain Diode Forward Voltage | V_{SD} | $I_S = 44.0 \text{ A}, V_{GS} = 0 \text{ V}$ | _ | 0.9 | 1.5 | V |
| Source to Drain Diode Reverse Recovery Time | t _{rr} | $I_F = 44.0 \text{ A}$ $di/dt = 100 \text{ A/}\mu\text{s}$ Refer to Figure 3 | _ | 48.4 | _ | ns |
| Source to Drain Diode Reverse Recovery Charge | Q_{rr} | | _ | 75.7 | _ | nC |

Test Circuits and Performance Curves

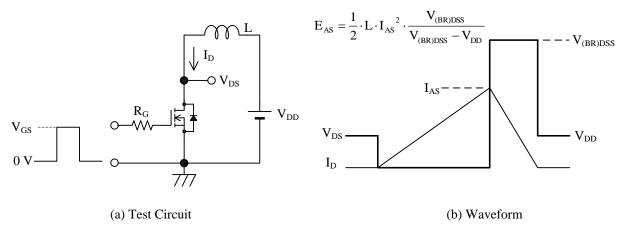


Figure 1. Unclamped Inductive Switching

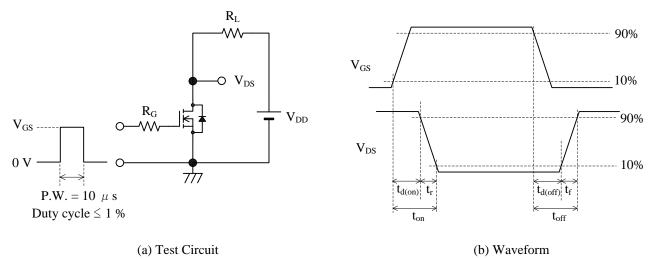


Figure 2. Switching Time

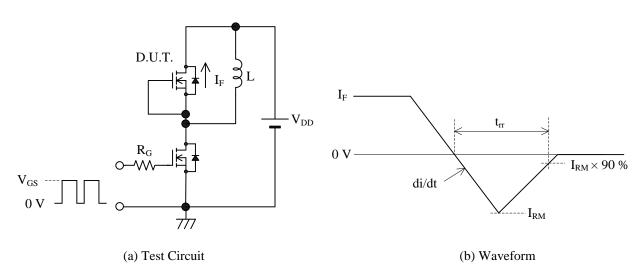
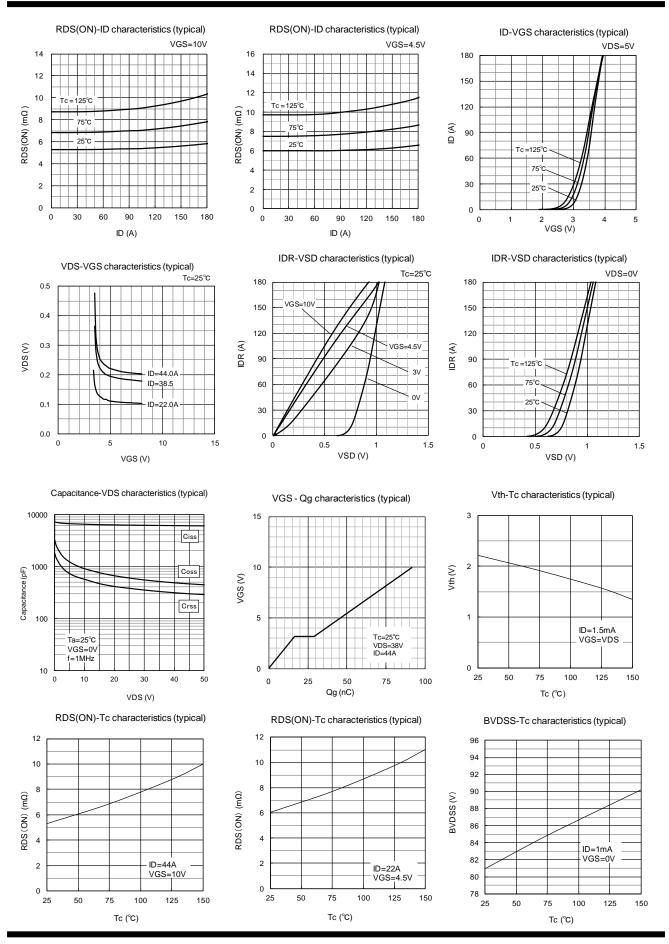
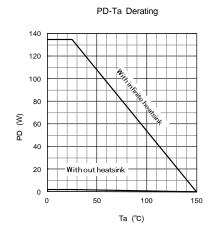
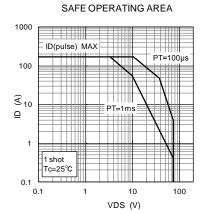
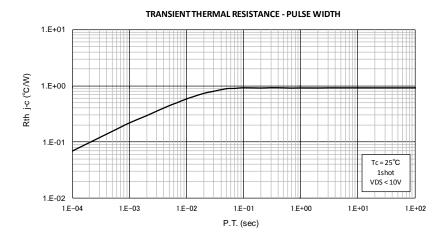


Figure 3. Diode Reverse Recovery Time



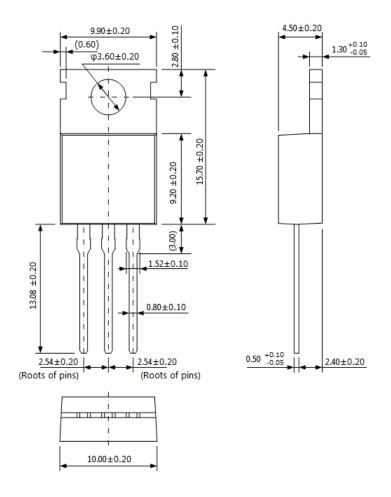






Physical Dimensions

• TO220-3L



NOTES:

- Dimensions in millimeters
- Maximum gate burr height is 0.3 mm.
- Bare lead frame: Pb-free (RoHS compliant)
- When soldering the products, it is required to minimize the working time, within the following limits:

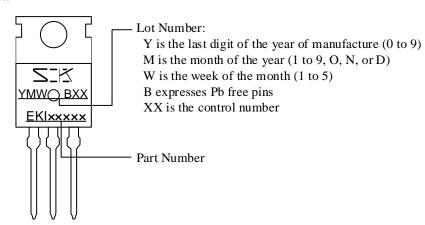
Flow: 260 ± 5 °C / 10 ± 1 s, 2 times

Soldering Iron: 380 ± 10 °C / 3.5 ± 0.5 s, 1 time

Soldering should be at a distance of at least 1.5 mm from the body of the product.

- Recommended screw torque for TO220: 0.490 N·m to 0.686 N·m (5 kgf·cm to 7 kgf·cm)

Marking Diagram



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