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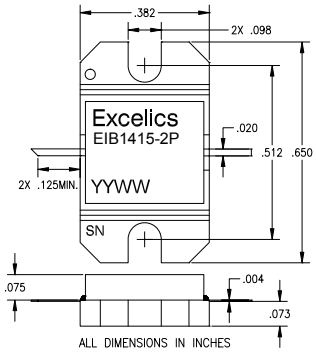
EIB1415-2P

UPDATED 06/14/06

14.40-15.35GHz 2W Internally Matched Power FET



FEATURES

- **14.40-15.35 GHz Bandwidth**
- **Input/Output Impedance Matched to 50 Ohms**
- **+33.0 dBm Output Power at 1dB Compression**
- **7.5 dB Power Gain at 1dB Compression**
- **24% Power Added Efficiency**
- **-46 dBc IM3 at PO = 22.0 dBm SCL**
- **Non-Hermetic Metal Flange Package**

ELECTRICAL CHARACTERISTICS (T_a = 25°C)

Caution! ESD sensitive device.

SYMBOL	PARAMETERS/TEST CONDITIONS[1]	MIN	TYP	MAX	UNITS
P_{1dB}	Output Power at 1dB Compression f = 14.40-15.35GHz V_{DS} = 8 V, I_{DSQ} ≈ 800mA	32.0	33.0		dBm
G_{1dB}	Gain at 1dB Compression f = 14.40-15.35GHz V_{DS} = 8 V, I_{DSQ} ≈ 800mA	6.50	7.50		dB
ΔG	Gain Flatness f = 14.40-15.35GHz V_{DS} = 8 V, I_{DSQ} ≈ 800mA			±0.6	dB
PAE	Power Added Efficiency at 1dB Compression V_{DS} = 8 V, I_{DSQ} ≈ 800mA f = 14.40-15.35GHz		24		%
Id_{1dB}	Drain Current at 1dB Compression f = 14.40-15.35GHz		850	960	mA
IM3	Output 3rd Order Intermodulation Distortion Δf = 10 MHz 2-Tone Test; Pout = 22.0 dBm S.C.L[2] V_{DS} = 8 V, I_{DSQ} ≈ 65% IDSS f = 15.35GHz	-43	-46		dBc
I_{DSS}	Saturated Drain Current V_{DS} = 3 V, V_{GS} = 0 V		1360	1700	mA
V_P	Pinch-off Voltage V_{DS} = 3 V, I_{DS} = 12 mA		-2.5	-3.5	V
R_{TH}	Thermal Resistance[3]		8.0	9.0	°C/W

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Note: 1) Tested with 100 Ohm gate resistor. 2) S.C.L. = Single Carrier Level. 3) Overall Rth depends on case mounting.

MAXIMUM RATINGS AT 25°C

SYMBOLS	PARAMETERS	ABSOLUTE[1]	CONTINUOUS[2]
Vds	Drain-Source Voltage	10V	8V
Vgs	Gate-Source Voltage	-5	-4V
Igsf	Forward Gate Current	21.6mA	7.2mA
Igsr	Reverse Gate Current	-3.6mA	-1.2mA
Pin	Input Power	32.0dBm	@ 3dB Compression
Tch	Channel Temperature	175 °C	175 °C
Tstg	Storage Temperature	-65 to +175 °C	-65 to +175 °C
Pt	Total Power Dissipation	16W	16W

Note: 1. Exceeding any of the above ratings may result in permanent damage.
2. Exceeding any of the above ratings may reduce MTTF below design goals.