



1Mx32 FLASH MODULE

DESCRIPTION

The EDI7F341MV and EDI7F2341MV are organized as one and two banks of 1Mx32 respectively. The modules are based on Intel's 28F008S3 - 1Mx8 Flash device in TSOP packages which are mounted on a FR4 substrate.

Both modules offer access times between 120 and 150ns allowing for operation of high-speed microprocessors without wait states.

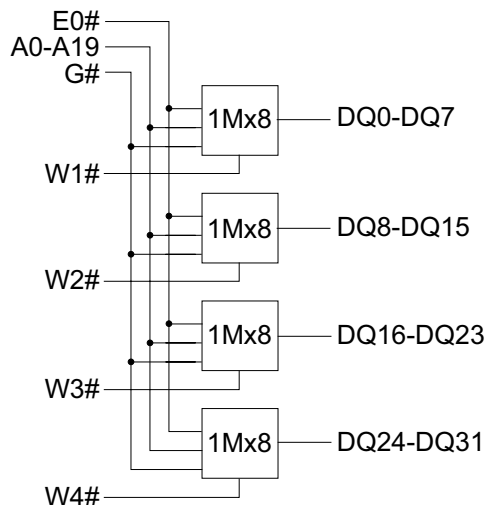
FEATURES

- 1Mx32 and 2x1Mx32 Densities
- Based on Intel's 28F008S3 Flash Device
- Fast Read Access Time - 120ns
- Flexible Smart Voltage
 - 3.0 - 3.6V Program/Erase
 - 3.0 - 3.6V Read
- Low Power Dissipation
 - 30mA per Device Active Current
 - 10μA per Device CMOS Standby Current
- Typical Endurance > 100,000 Cycles
- 3.3V ±10% Supply
- CMOS and t_{TL} Compatible Inputs and Outputs
- Commercial and Industrial Temperature Range
- Package
 - 80 Pin SIMM (JEDEC)
- Also Available with Gold Contacts, P/N 7Gxxx.

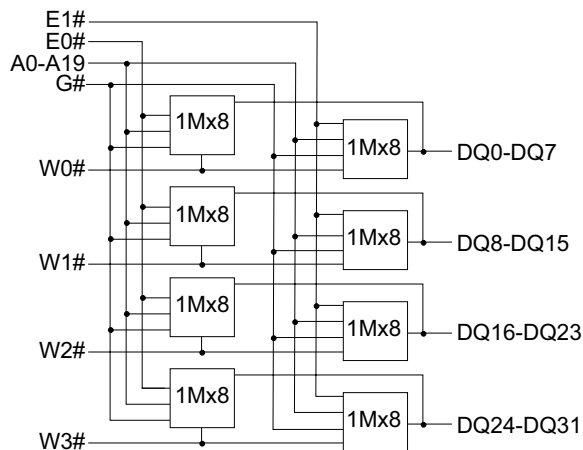
FIG. 1

BLOCK DIAGRAMS

EDI7F341MV-BNC: 1Mx32 80 PIN SIMM



EDI7F2341MV-BNC: 2x1Mx32 80 PIN SIMM



**CAPACITANCE**(f=1.0MHz, $V_{IN} = V_{CC}$ or V_{SS})

		1Meg	2x1Meg	
Parameter	Sym	Max	Max	Unit
Address Lines	CA	35	70	pF
Data Lines	CDQ	15	30	pF
Chip & Write Enable Lines	CC	15	30	pF
Output Enable Lines	CG	35	70	pF

PIN CONFIGURATIONS

Pin	Symbol	Pin	Symbol	Pin	Symbol	Pin	Symbol
1	V_{SS}	21	*	41	A11	61	DQ9
2	V_{CC}	22	*	42	A10	62	DQ8
3	V_{PP}	23	*	43	A9	63	DQ7
4	G#	24	*	44	A8	64	DQ6
5	W0#	25	V_{SS}	45	A7	65	DQ5
6	W1#	26	DQ29	46	A6	66	DQ4
7	NC	27	DQ30	47	A5	67	DQ3
8	DQ16	28	DQ31	48	A4	68	DQ2
9	DQ17	29	W2#	49	A3	69	DQ1
10	DQ18	30	NC	50	A2	70	DQ0
11	DQ19	31	NC	51	A1	71	V_{PP}
12	DQ20	32	NC	52	A0	72	V_{CC}
13	DQ21	33	A19	53	W3#	73	PD1
14	DQ22	34	A18	54	V_{SS}	74	PD2
15	DQ23	35	A17	55	DQ15	75	PD3
16	DQ24	36	A16	56	DQ14	76	PD4
17	DQ25	37	A15	57	DQ13	77	PD5
18	DQ26	38	A14	58	DQ12	78	PD6
19	DQ27	39	A13	59	DQ11	79	PD7
20	DQ28	40	A12	60	DQ10	80	V_{SS}

SIMM Density

Pin	1Meg	2x1Meg
21	NC	NC
22	NC	NC
23	NC	E1#
24	E0#	E0#

Presence Detect Pin Out

Pin	1Meg	2x1Meg
PD1	NC	V_{SS}
PD2	V_{SS}	V_{SS}
PD3	NC	NC
PD4	V_{SS}	V_{SS}

A0-A19	Address Input
E0#, E1#	Chip Enable
W0#-W3#	Write Enable
G#	Output Enable
DQ0-DQ31	Data Input/Output
P_D	Presence Detect
V_{CC}	Power 5V $\pm$ 10%
V_{SS}	Ground
NC	No Connect
V_{PP}	Program Power 3.3V $\pm$ 10%



Part Number	Speed (ns)	Package
EDI7F341MV120BNC	120	500
EDI7F341MV150BNC	150	500

4.655 MAX. NOTE 1

4.384

0.250

0.850 MAX.

0.400

0.125 MAX.

0.125 MIN.

0.225 MIN.

0.050 +.004
- .003

0.050 TYP.

0.250

2.245

4.150

2.192

R 0.062 (2x)

.125 DIA (2x)

C1 C2 C3 C4 R1 R3 J3 R2 C9 C10 C11 C12

U1 U2 U3 U4

P1

153

Part Number	Speed (ns)	Package
EDI7F2341MV120BNC	120	501
EDI7F2341MV150BNC	150	501

[illegible]