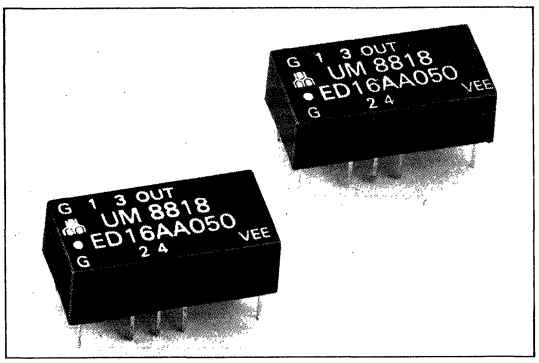
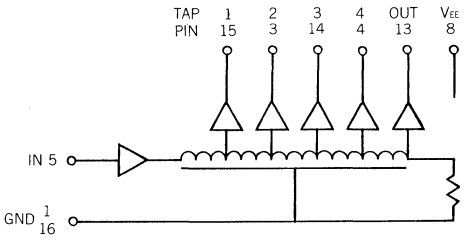
ED16AA SERIES: 16-PIN DIL 5-TAP EQUALLY-SPACED



FEATURES:

- Input and Output ECL Buffered
- 5 Equally-Spaced Delay Taps
- Fits in Standard 16-pin DIL
- Operating Temperature Range: −30°C TO +85°C
- Custom Design (Delays or pin Layouts) Available Upon Request

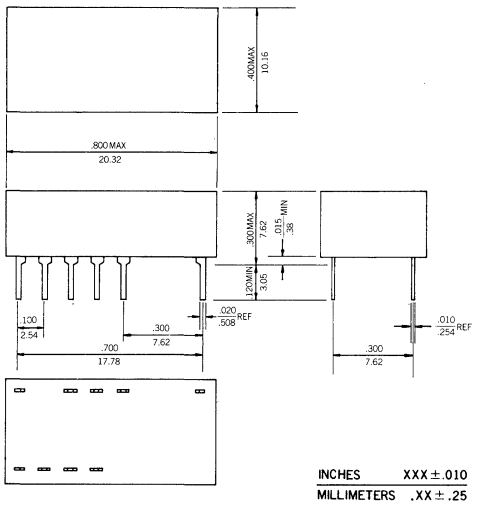
CIRCUIT AND PIN CONNECTIONS:



ELECTRICAL CHARACTERISTICS

Supply Voltage V_{EE}: −5.2±0.25 VDC
Logic 1 Input Voltage: −0.96 V min
Input Current: .26 mA max
Logic 0 Input Voltage: −1.65 V max
Input Current: .5 μA max
All Measurements Made at V_{CC} = −5.2V, 25°C

PACKAGE DIMENSIONS:



INPUT TEST CONDITIONS

Pulse Voltage : 1V (−.75V to −1.75V)
Rise Time : 3.0 ns
Supply Current: 60mA typical
Pulse Width : min. 100% of total delay
Duty Cycle : 33% or less

ELECTRICAL SPECIFICATIONS:

PART NO.	TOTAL DELAY (2)ns	TAP DELAY ns	RISE TIME(3) ns max
ED16AA010	10	2± .4	4
ED16AA020	20	4± .5	4
ED16AA025	25	5±1.0	4
ED16AA050	50	10±2.0	4
ED16AA075	75	15±2.0	4
ED16AA100	100	20±2.0	4
ED16AA150	150	30±2.0	4
ED16AA200	200	40±2.0	4
ED16AA250	250	50±2.5	4
ED16AA300	300	60±3.0	5
ED16AA400	400	80±4.0	5
ED16AA500	500	100±5.0	5

(1) Timedelay measurements referenced to 1st tap.
(2) ±5% or ±2ns which is greater.
(3)Risetime measured from 20% to 80% with loads.