

Marketing Bulletin

DATE: March 24th, 2006

TO: All Sales Personnel

FROM: Mark Stoner

RE: Product Termination

To all concerned parties,

This bulletin is to notify all customers of the discontinuation of the following Ecliptek series effective March 24th, 2006:

Series Description Recommended Replacement E13C9 3.3V 5 x 7mm SMD LVPECL Oscillator E13C7 or E13D8

In compliance with our End of Life (EOL) policy, this will serve as advanced notice of product termination. New orders will not be accepted after July 1st, 2006, with delivery to conclude by October 1st 2006.

If there are any questions pertaining to this bulletin, please fell free to contact me. Thank you again for your cooperation.

Best Regards,

Mark W. Stoner Director of Marketing

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Ecliptek Corporation

E13C9 Series

- RoHS Compliant (Pb-Free)
- LVPECL Output Oscillators
- 3.3V Supply Voltage
- AT-Cut Fundamental Mode Inverted Mesa Crystal
- Ceramic 6-pad SMD Package
- Stability to 25ppm
- Tri-State Enable High and Enable Low Options Available on Pad 1 or Pad 2
- Complementary Output
- Wide Range of Available Frequencies





OBSOLETE

ELECTRICAL SPECIFICATIONS

		-	—				
Nominal Frequ	iency			19.44	0MHz to 200.000MHz		
Operating Tem	perature Range			0°C to	o 70°C, or -40°C to +85	5°C	
Storage Tempe	erature Range			-55°C	to 125°C		
Supply Voltage (V _{cc})				3.3V _{DC}	±5%		
Input Current				75mA	Maximum		
Frequency Tole	erance / Stability	Inclusive of All Conditions: Calibration Tolerance at 25°C,		5°C, ±100p	opm, ±50ppm, or		
		Frequency Stability ove	r the Operating Temperature Ra	ange, ±25pp	om Maximum		
		Supply Voltage Change	, Output Load Change, 1st Year				
		Aging at 25°C, Shock, a	and Vibration				
Output Voltag	e Logic High (V _{он})			V _{cc} -1.0	V _{cc} -1.025V _{DC} Minimum		
Output Voltag	e Logic Low (V _{OL})			V _{cc} -1.0	620V _{DC} Maximum		
Rise Time / Fa	ll Time	20% to 80% of wavefor	m	1.5 nS	1.5 nSeconds Maximum, 600 pSec Typical		
Duty Cycle		at 50% of waveform		50 ±1	50 ±10(%)		
				50 ±5	50 ±5(%)		
Load Drive Cap	pability			50 Oh	50 Ohms into V _{CC} -2.0V _{DC}		
Logic Control / Additional Output		·		No Co	No Connect and Complementary Output or		
		Enable High or Enable I	_OW	Tri-St	ate and Complementar	y Output	
Enable High Tri-State Input Voltage		V _{TH} of 70% of V _{CC} Minimum		Enabl	Enables Output		
		No Connection		Enabl	es Output		
		V _{II} of 30% of V _{CC} Maxim	um	Disab	les Output: High Imped	dance	
Enable Low Tri-State Input Voltage		V _{IH} of 70% of V _{CC} Minimum		Disab	Disables Output: High Impedance		
		No Connection		Enabl	es Output		
		V _{IL} of 30% of V _{CC} Maxim	um	Enabl	es Output		
Output Disabl	e Current	it			25mA Maximum		
Start Up Time				10 mS	10 mSeconds Maximum		
RMS Phase Jitter		< 44.736MHz; F ₁ = 12kHz to 20MHz		5 pSe	5 pSec Maximum		
		≥ 44.736MHz, < 77.760	MHz; $F_3 = 12$ kHz to 20MHz	2 pSe	c Maximum		
		\geq 77.760MHz; $F_{J} = 12kH$	z to 20MHz	1 pSe	c Maximum		
Phase Noise (at 155.520MHz)		at 10Hz Offset at 100Hz Offset		-75dB	-75dBc/Hz Typical -95dBc/Hz Typical		
				-95dB			
		at 1kHz Offset		-125d	IBc/Hz Typical		
		at 10kHz Offset			IBc/Hz Typical		
		at 100kHz Offset			Bc/Hz Typical		
MANUFACTURER ECLIPTEK CORP.	CATEGORY OSCILLATOR	SERIES E13C9	PACKAGE CERAMIC	VOLTAGE 3.3V	CLASS OS1T	REV = DATE 06/04	
LCLIFIER CURP.	OSCILLAIOK	£13L9	CERAPILC	3.31	0211	00/04	

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PART NUMBERING GUIDE

E13C9 E 2 F - 155.520M TR

FREQUENCY TOLERANCE & STABILITY/ OPERATING TEMPERATURE RANGE

C=±100ppm Maximum over 0°C to +70°C
D=±50ppm Maximum over 0°C to +70°C
E=±25ppm Maximum over 0°C to +70°C
G=±100ppm Maximum over -40°C to +85°C
H=±50ppm Maximum over -40°C to +85°C
J=±25ppm Maximum over -40°C to +85°C

DUTY CYCLE

1=50% ±10%, 2=50% ±5%

AVAILABLE OPTIONS

Blank=Tubes

TR=Tape and Reel (Standard)

FREQUENCY

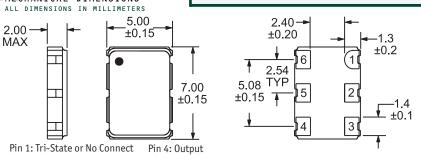
LOGIC CONTROL/ADDITIONAL OUTPUT

C=No Connect and Complementary Output

F=Tri-State (Enable High) on Pad 1 and Complementary Output H=Tri-State (Enable High) on Pad 2 and Complementary Output J=Tri-State (Enable Low) on Pad 1 and Complementary Output

K=Tri-State (Enable Low) on Pad 2 and Complementary Output





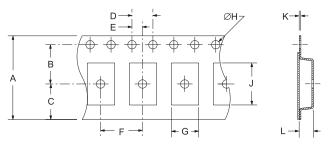
Pin 2: Tri-State or No Connect Pin 5: Complementary Output

Pin 3: Case Ground Pin 6: Supply Voltage

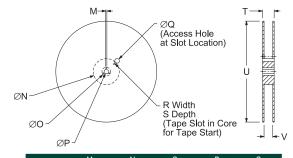
SUGGESTED SOLDER PAD LAYOUT ALL DIMENSIONS IN MILLIMETERS 1.80 (X6) 2.00 (X6) (X6) 0.54 Tolerances= +0.1

TAPE AND REEL DIMENSIONS

ALL DIMENSIONS IN MILLIMETERS



TAPE	А	В	С	D	Е
	16±.31	7.5±.1	6.75±.1	4 ±.1	2±.1
F	G	Н	J	K	L
8±.1	B0*	1.5 +.1-0	A0*	.3 ±.05	K0*



1.5 MIN 50 MIN 20.2 MIN 13±.2 40 MIN R S T U V QTY/REEL 2.5 MIN 10 MIN 22.4 MAX 360 MAX 16.4+2-0 1 1000	REEL	M	N	0	<u> </u>	Q
		1.5 MIN	50 MIN	20.2 MIN	13±.2	40 MIN
2.5 MTN 10 MTN 22.4 MAX 360 MAX 16.4+2-0 1.000	R	S	T	U	٧	QTY/REEL
2.51 111 101 111 22:41 111 3001 111 10:41 2 0 1,000	2.5 MIN	10 MIN	22.4 MAX	360 MAX	16.4+2-0	1,000

*Compliant to EIA 481A

ENVIRONMENTAL/MECHANICAL SPECIFICATIONS

<u>Characteristic</u> <u>Specification</u>

Fine Leak Test MIL-STD-883, Method 1014, Condition A
Gross Leak Test MIL-STD-883, Method 1014, Condition C
Mechanical Shock MIL-STD-202, Method 213, Condition C
Vibration MIL-STD-883, Method 2007, Condition A
Solderability MIL-STD-883, Method 2002
Temperature Cycling MIL-STD-883, Method 1010

Resistance to Soldering Heat
Resistance to Solvents

MIL-STD-883, Method 1010
MIL-STD-202, Method 210
MIL-STD-202, Method 210

MARKING SPECIFICATIONS

Line 1: ECLIPTEK

Line 2: XX.XXX M

Frequency in MHz (5 Digits Maximum + Decimal)

Line 3: XX Y ZZ

Week of Year

Last Digit of Year

Ecliptek Manufacturing Identifier

MANUFACTURER CATEGORY SERIES PACKAGE VOLTAGE CLASS REV.DATE ECLIPTEK CORP. OSCILLATOR E13C9 CERAMIC 3.3V OS1T 06/04