



200V PNP HIGH VOLTAGE TRANSISTOR PowerDI[®]5

Features

- 43% smaller than SOT223; 60% smaller than TO252
- Maximum height just 1.1mm
- Rated up to 3.2W
- V_{CEO} = -200V
- I_C = -2A; I_{CM} = -5A
- Low Saturation voltage
- Lead, Halogen, and Antimony Free/RoHS Compliant (Note 1)
- "Green" Device (Note 2)

Application

• DC – DC conversion

Mechanical Data

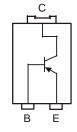
- Case: PowerDl[®]5
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.093 grams (approximate)



Top View

Bottom View





Pin-out diagram

Ordering Information (Note 3)

Part Number	Case	Packaging
DXTP03200BP5-13	PowerDI [®] 5	5000/Tape & Reel

Notes: 1. No purposefully added lead. Halogen and Antimony Free.

2. Diodes Inc's "Green" Policy can be found on our website at http://www.diodes.com

3. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information



DTP3200B = Product Type Marking Code) | = Manufacturers' Code Marking K = Factory Designator YYWW = Date Code Marking YY = Last Two Digits of Year (ex: 09 for 2009) WW = Week code (01 to 53)



Maximum Ratings @T_A = 25°C unless otherwise specified

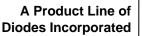
Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V _{CBO}	-220	V
Collector-Emitter Voltage	V _{CEO}	-200	V
Emitter-Base Voltage	V _{EBO}	-7	V
Continuous Collector Current	lc	-2	A
Base Current	IB	-1	A
Peak Pulse Current	Ісм	-5	A

Thermal Characteristics

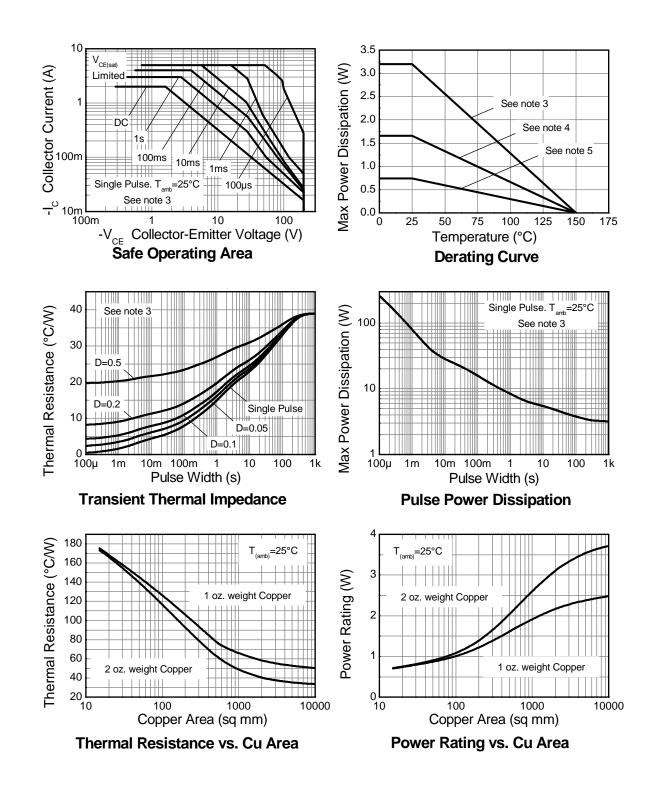
Characteristic	Symbol	Value	Unit
Power Dissipation @ $T_A = 25^{\circ}C$ (Note 4)	PD	3.2	W
Thermal Resistance, Junction to Ambient Air (Note 4) $@T_A = 25^{\circ}C$	$R_{ ext{ heta}JA}$	39	°C/W
Power Dissipation @ $T_A = 25^{\circ}C$ (Note 5)	PD	1.7	W
Thermal Resistance, Junction to Ambient Air (Note 5) $@T_A = 25^{\circ}C$	$R_{ ext{ heta}JA}$	75	°C/W
Power Dissipation @ $T_A = 25^{\circ}C$ (Note 6)	PD	0.74	W
Thermal Resistance, Junction to Ambient Air (Note 6) $@T_A = 25^{\circ}C$	$R_{ ext{ heta}JA}$	169	°C/W
Thermal Resistance, Junction to Collector Terminal	$R_{ ext{ heta}JT}$	5.6	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Notes: 4. Device mounted on FR-4 PCB, single sided 2 oz. copper, collector pad dimensions 25mm x 25mm. Device mounted on FR-4 PCB, single sided 2 oz. copper, collector pad dimensions 25mm x 25mm.
Device mounted on FR-4 PCB, single sided 1 oz. copper, minimum recommended pad layout.











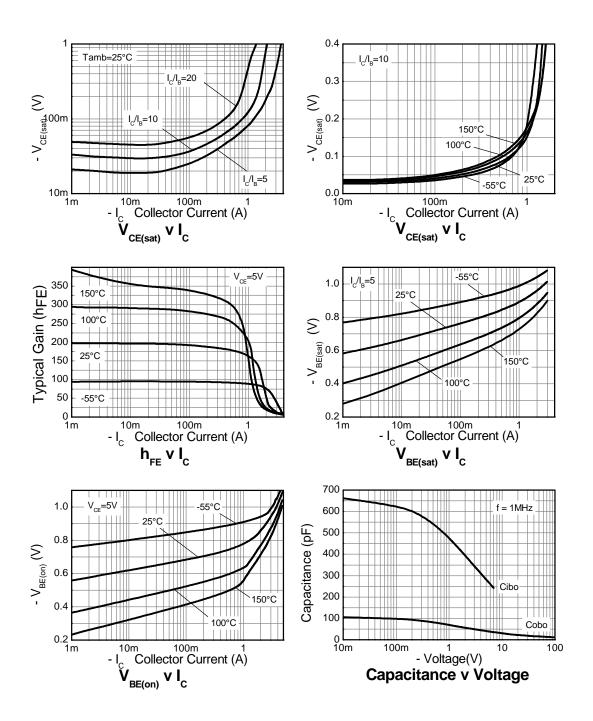
Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
Collector-Base Breakdown Voltage	V _{(BR)CBO}	-220	-245	-	V	I _C = -100μA	
Collector-Emitter Breakdown Voltage (Note 7)	V _{(BR)CEO}	-200	-225	-	V	I _C = -10mA	
Emitter-Base Breakdown Voltage	V _{(BR)EBO}	-7	-8.4	-	V	$I_{E} = -100 \mu A$	
Collector Cutoff Current	1	-	<1	-50	nA	V _{CB} = -200V	
	I _{CBO}		-	-0.5	μA	V _{CB} = -200V, T _A = 100 °C	
Emitter Cutoff Current	I _{EBO}	-	<1	-10	nA	$V_{EB} = -6V$	
		-	-37	-50		$I_{C} = -0.1A, I_{B} = -10mA$	
Collector-Emitter Saturation Voltage (Note 7)	Vorum	-	-130	-155	mV	$I_{C} = -0.5A, I_{B} = -25mA$	
Concetor Emitter Catalation Voltage (Note 7)	V _{CE(sat)}	-	-135	-160	IIIV	I _C = -1A, I _B = -100mA	
		-	-180	-275		$I_{C} = -2A, I_{B} = -400mA$	
Base-Emitter Saturation Voltage (Note 7)	V _{BE(sat)}	-	-955	-1100	mV	$I_{\rm C} = -2A, I_{\rm B} = -400 {\rm mA}$	
Base-Emitter Turn-On Voltage (Note 7)	V _{BE(on)}	-	-860	-1000	mV	$V_{CE} = -5V, I_{C} = -2A$	
		100	195	-		$V_{CE} = -5V, I_{C} = -10mA$	
DC Current Gain (Note 7)	h _{FE}	100	170	300		$V_{CE} = -5V, I_{C} = -1A$	
DC Current Gain (Note 7)		20	50		$V_{CE} = -5V, I_{C} = -2A$		
		-	5	-	-	$V_{CE} = -5V, I_{C} = -5A$	
Transition Frequency	f _T	г –	105	05 –	MHz	$V_{CE} = -10V, I_{C} = -100mA,$	
						f = 50MHz	
Output Capacitance	C _{obo}	-	31	-	pF	V _{CB} = -10V, f = 1MHz	
Delay Time	t _d	-	21	-	ns]	
Rise Time	tr	-	18	-	ns	$V_{CC} = -50V, I_{C} = -1A,$	
Storage Time	ts	_	680	-	ns	$I_{B1} = -I_{B2} = -100 \text{mA}$	
Fall Time	t _f	-	75	-	ns]	

Notes: 7. Pulse Test: Pulse width \leq 300µs. Duty cycle \leq 2.0%.



Typical Characteristic

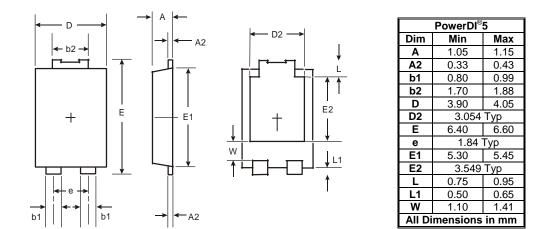


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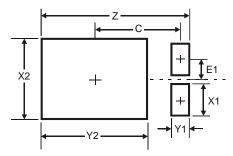


ΕX

Package Outline Dimensions



Suggested Pad Layout



Dimensions	Value (in mm)
Z	6.6
X1	1.4
X2	3.6
Y1	0.8
Y2	4.7
С	3.87
E1	0.9



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